

Single Phase Glass Passivated Silicon **Bridge Rectifier**

Features

- Plastic package has Underwriters Laboratory
- Flammability Classification 94V-0
- High case dielectric strength of 1500 V_{RMS}
- · Glass passivated chip junction
- · Ideal for printed circuit boards
- · High surge overload rating
- High temperature soldering guaranteed: 260°C/ 10 seconds, 0.375 (9.5mm) lead length
- · Not ESD Sensitive

Mechanical Data

Case: Molded plastic body over passivated junctions Terminals: Plated leads, solderable per MIL-STD-750 Method 2026. Mounting position: Any

Maximum ratings at Tc = 25 °C, unless otherwise specified

Maximum ratings at TC = 25						
Parameter	Symbol	Conditions	GBU8J	GBU8K	GBU8M	Unit
Repetitive peak reverse voltage	V _{RRM}		600	800	1000	V
RMS reverse voltage	V _{RMS}		420	560	700	V
DC blocking voltage	V _{DC}		600	800	1000	V
Operating temperature	Tj		-55 to 150	-55 to 150	-55 to 150	°C
Storage temperature	T _{stg}		-55 to 150	-55 to 150	-55 to 150	°C

Electrical characteristics at Tc = 25 °C, unless otherwise specified

Single phase, half sine wave, 60 Hz, resistive or inductive load.

For capacitive load derate current by 20%.

Parameter	Symbol	Conditions	GBU8J	GBU8K	GBU8M	Unit
Maximum average forward rectified current ^{1,2}	Ι _Ο	T _c = 100 °C	8.0	8.0	8.0	А
Peak forward surge current	I _{FSM}	t _p = 8.3 ms, half sine	200	200	200	А
Maximum instantaneous forward voltage drop per leg	V _F	I _F = 8 A	1.1	1.1	1.1	V
Maximum DC reverse current at rated DC blocking voltage per leg	I _R	T _a = 25 °C	5	5	5	μΑ
		T _a = 125 °C	500	500	500	
Rating for fusing	l ² t	t < 8.3 ms	166	166	166	A ² sec
Typical junction capacitance per leg	C_j		94	94	94	pF
Typical thermal resistance per leg ^{1,2}	R _{oja}		21	21	21	
	$R_{\Theta JL}$		2.2	2.2	2.2	°C/W

¹ - Device mounted on 82 mm x 82 mm x 3 mm AI plate heatsink

² - Recommended mounted position is to bolt down device on a heatsink with silicon thermal

compond for maximum heat transfer using #6 screw.

³ - Measured at 1.0 MHz and applied reverse bias of 4.0 V



 $I_0 = 8 A$

 $V_{RRM} = 600 \text{ V} - 1000 \text{ V}$



GBU8J thru GBU8M

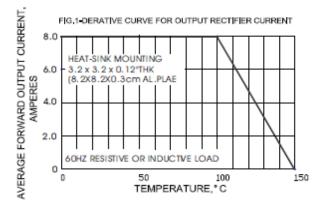
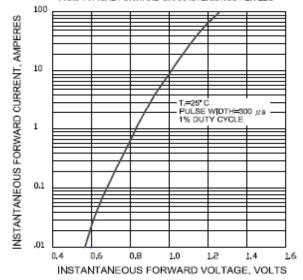
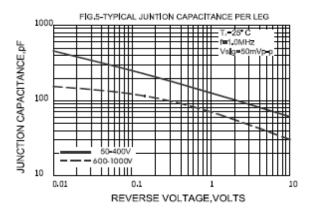
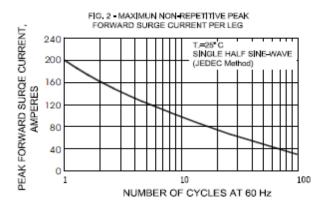
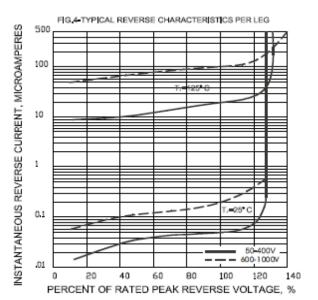


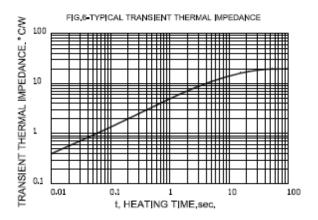
FIG.3-TYPICAL FORWARD CHARACTERISTICS PER LEG







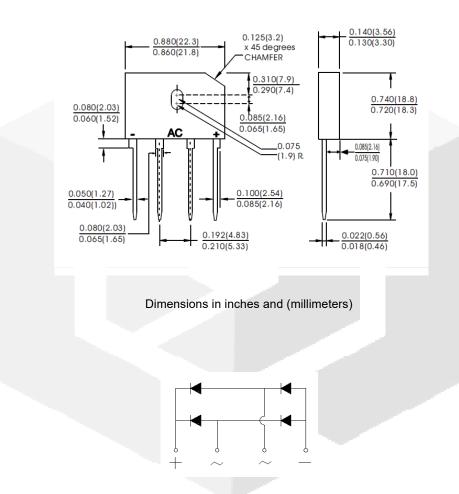






Package dimensions and terminal configuration

Product is marked with part number and terminal configuration.





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GeneSiC Semiconductor: <u>GBU8J</u> <u>GBU8K</u> <u>GBU8M</u>