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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

FAIRCHILD SEMICONDUCTOR®

November 2013

FCP11N60F

N-Channel SuperFET® FRFET® MOSFET

600 V, 11 A, 380 mΩ

Features

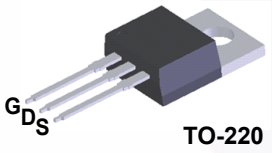
- 650 V @T_J = 150°C
- Typ. $R_{DS(on)}$ = 320 mΩ
- Fast Recovery Type (t_{rr} = 120 ns)
- Ultra Low Gate Charge (Typ. Q_g = 40 nC)
- Low Effective Output Capacitance (Typ. C_{oss}.eff = 95 pF)
- 100% Avalanche Tested
- RoHS compliant

Application

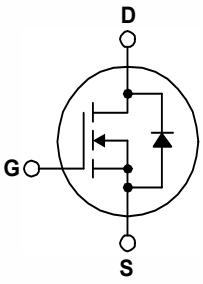
- LCD/LED/PDP TV
- Lighting
- Solar Inverter
- AC-DC Power Supply

Description

SuperFET® MOSFET is Fairchild Semiconductor's first generation of high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications. SuperFET FRFET® MOSFET's optimized body diode reverse recovery performance can remove additional component and improve system reliability.



TO-220



MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FCP11N60F	Unit
V_{DSS}	Drain to Source Voltage		600	V
I_D	Drain Current	- Continuous (T_C = 25°C)	11	A
		- Continuous (T_C = 100°C)	7	
I_{DM}	Drain Current	- Pulsed (Note 1)	33	A
V_{GSS}	Gate to Source Voltage		±30	V
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	340	mJ
I_{AR}	Avalanche Current	(Note 1)	11	A
E_{AR}	Repetitive Avalanche Energy	(Note 1)	12.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
P_D	Power Dissipation	(T_C = 25°C)	125	W
		- Derate above 25°C	1.0	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FCP11N60F	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	1.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max	62.5	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCP11N60F	FCP11N60F	TO-220	-	-	50

Electrical Characteristics $T_C = 25^oC$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{DSS}	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 μA, T_C = 25°C	600	-	-	V
		V_{GS} = 0 V, I_D = 250 μA, T_C = 150°C	-	650	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C	-	0.6	-	V/°C
BV_{DS}	Drain-Source Avalanche Breakdown Voltage	V_{GS} = 0 V, I_D = 11 A	-	700	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 600 V, V_{GS} = 0 V	-	-	1	μA
		V_{DS} = 480 V, T_C = 125°C	-	-	10	
I_{GSS}	Gate to Body Leakage Current	V_{GS} = ±30 V, V_{DS} = 0 V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 250 μA	3.0	-	5.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	V_{GS} = 10 V, I_D = 5.5 A	-	0.32	0.38	Ω
g_{FS}	Forward Transconductance	V_{DS} = 40 V, I_D = 5.5 A	-	6	-	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz	-	1148	1490	pF
C_{oss}	Output Capacitance		-	671	870	pF
C_{rss}	Reverse Transfer Capacitance		-	63	82	pF
C_{oss}	Output Capacitance	V_{DS} = 480 V, V_{GS} = 0 V, f = 1.0 MHz	-	35	-	pF
C_{oss}eff.	Effective Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	-	95	-	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 300 V, I_D = 11 A, R_G = 25 Ω (Note 4)	-	34	80	ns
t_r	Turn-On Rise Time		-	98	205	ns
$t_{d(off)}$	Turn-Off Delay Time		-	119	250	ns
t_f	Turn-Off Fall Time		-	56	120	ns
$Q_{g(tot)}$	Total Gate Charge at 10V	V_{DS} = 480 V, I_D = 11 A, V_{GS} = 10 V (Note 4)	-	40	52	nC
Q_{gs}	Gate to Source Gate Charge		-	7.2	-	nC
Q_{gd}	Gate to Drain “Miller” Charge		-	21	-	nC
Drain-Source Diode Characteristics Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	11	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	33	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0 V, I_{SD} = 11 A	-	-	1.4	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_{SD} = 11 A, dI_F/dt = 100 A/μs	-	120	-	ns
Q_{rr}	Reverse Recovery Charge		-	0.8	-	μC

Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2. I_{AS} = 5.5 A, V_{DD} = 50 V, R_G = 25 Ω, Starting T_J = 25°C.
3. I_{SD} ≤ 11 A, di/dt ≤ 200 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.
4. Essentially independent of operating temperature.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

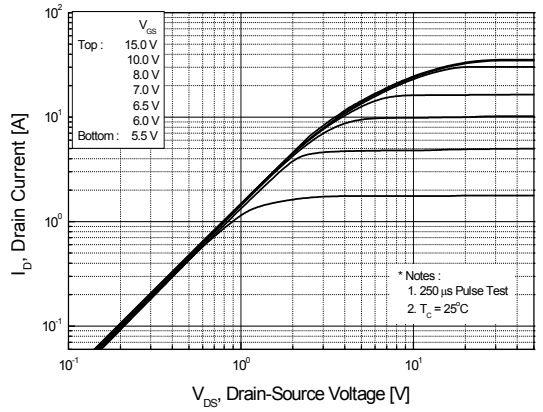


Figure 2. Transfer Characteristics

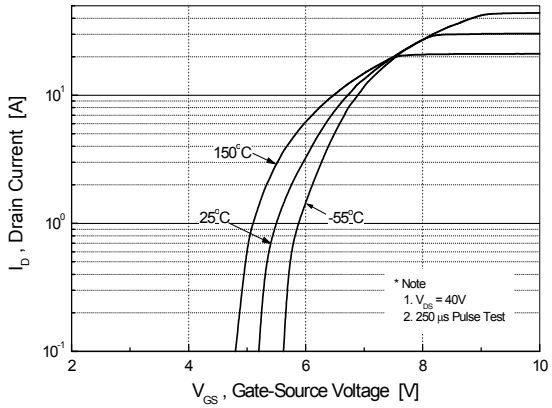


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

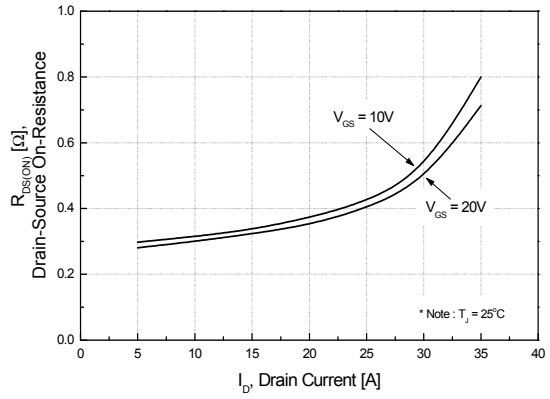


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

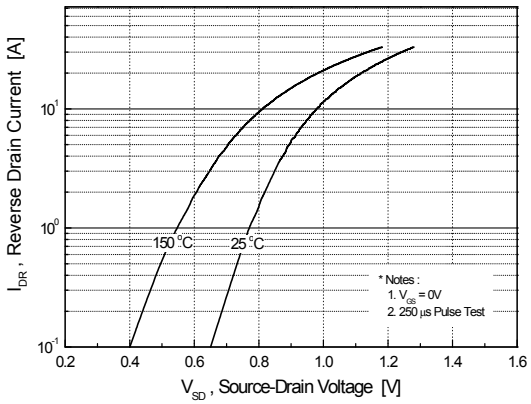


Figure 5. Capacitance Characteristics

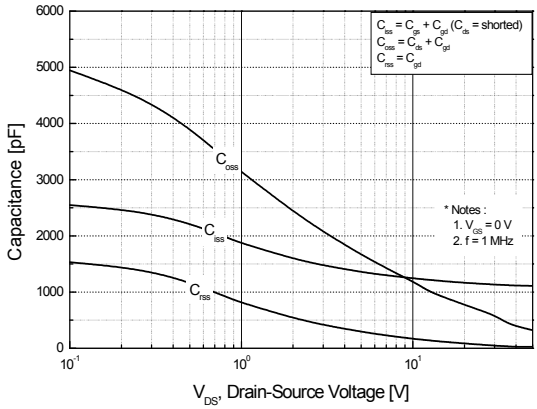
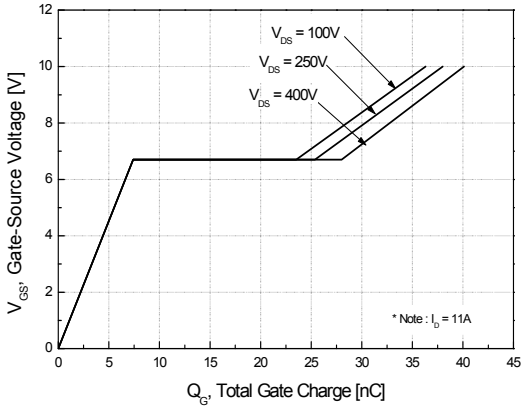


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

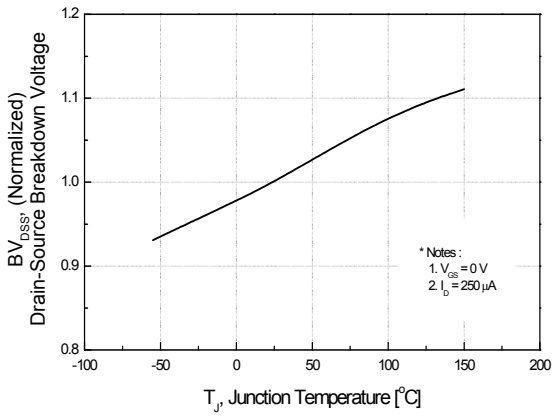


Figure 8. On-Resistance Variation vs. Temperature

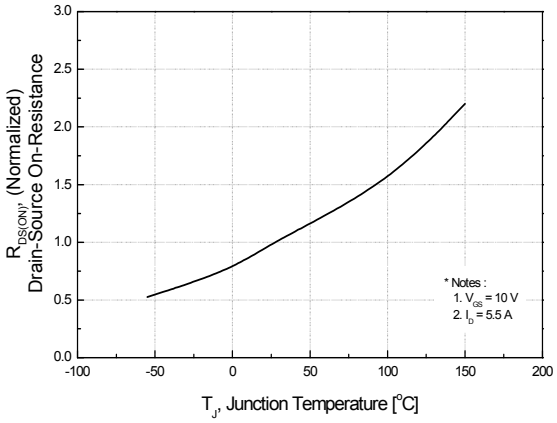


Figure 9. Safe Operating Area

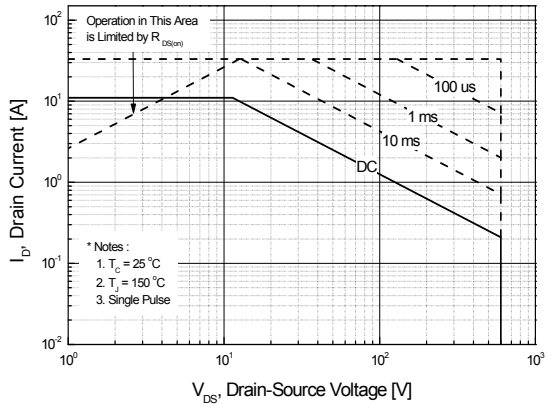


Figure 10. Maximum Drain Current vs. Case Temperature

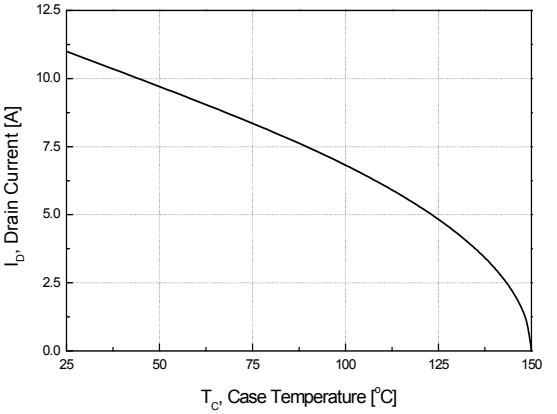


Figure 11. Transient Thermal Response Curve

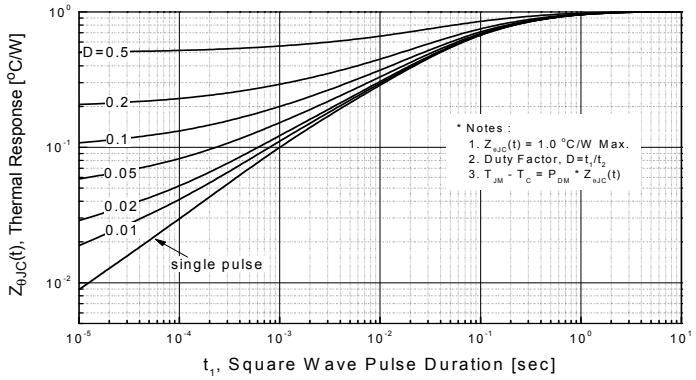


Figure 12. Gate Charge Test Circuit & Waveform

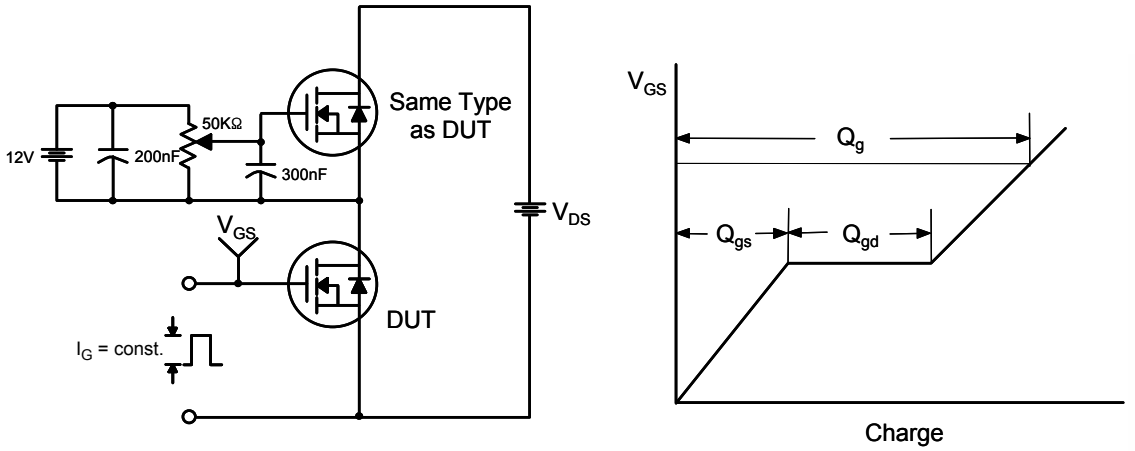


Figure 13. Resistive Switching Test Circuit & Waveforms

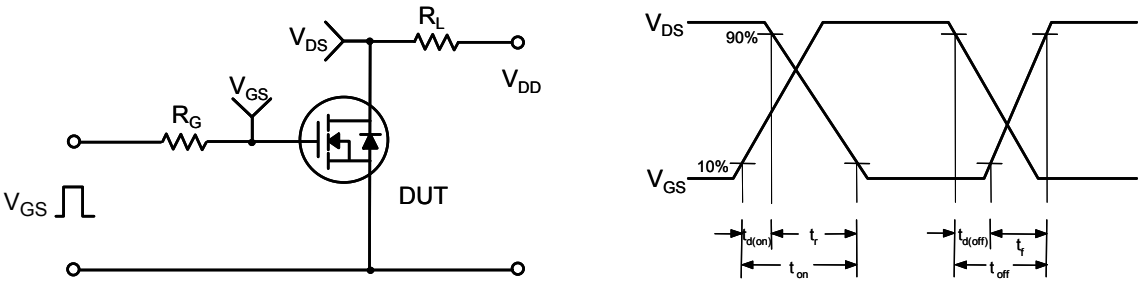


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

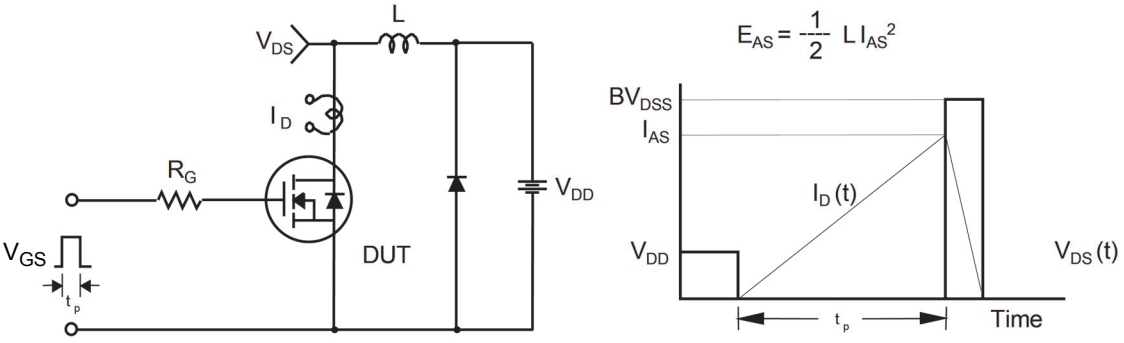
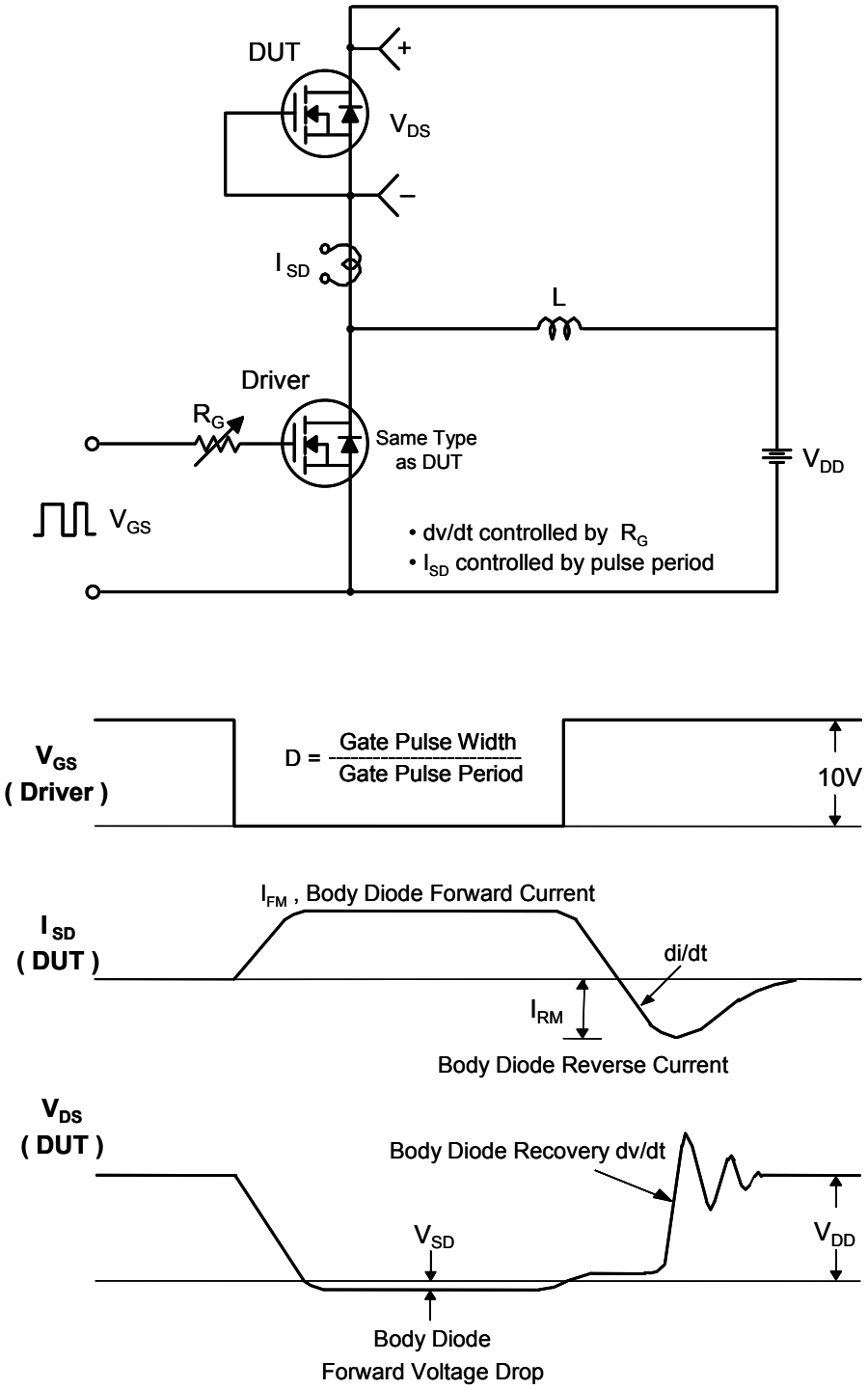
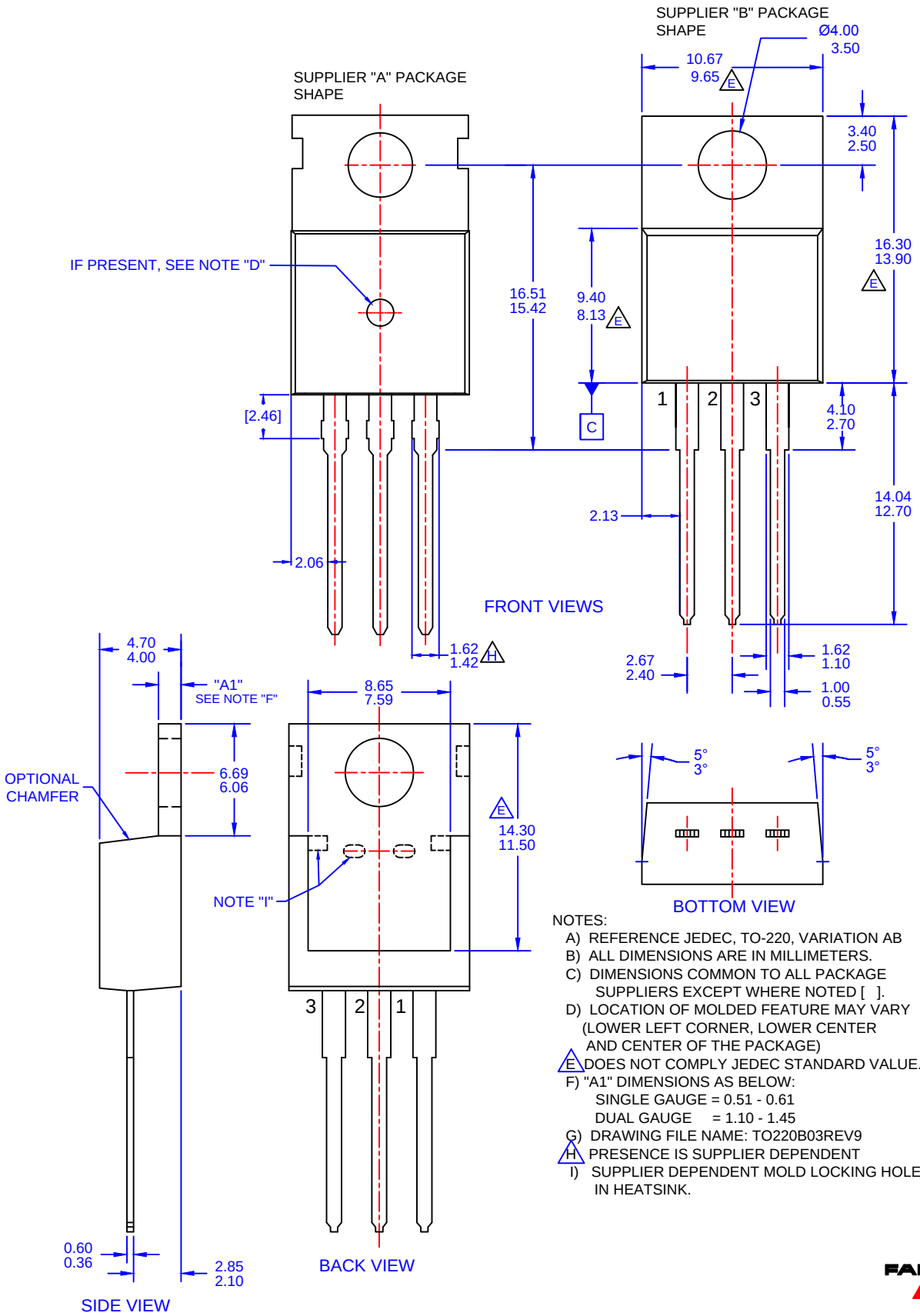


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms





NOTES:

A) REFERENCE JEDEC, TO-220, VARIATION AB
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [ ].
D) LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
E DOES NOT COMPLY JEDEC STANDARD VALUE.
F) "A1" DIMENSIONS AS BELOW:
SINGLE GAUGE = 0.51 - 0.61
DUAL GAUGE = 1.10 - 1.45
G) DRAWING FILE NAME: TO220B03REV9
H PRESENCE IS SUPPLIER DEPENDENT
I) SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK.

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