

## P-Channel 1.8-V (G-S) MOSFET

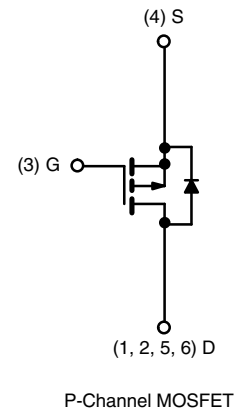
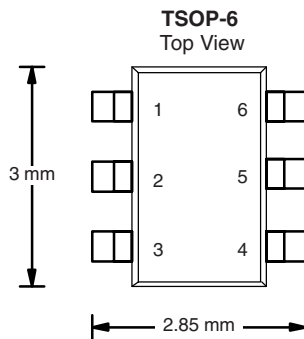
PRODUCT SUMMARY		
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
- 8	0.042 at V <sub>GS</sub> = - 4.5 V	- 5.8
	0.060 at V <sub>GS</sub> = - 2.5 V	- 4.9
	0.080 at V <sub>GS</sub> = - 1.8 V	- 4.2

### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
**HALOGEN**  
**FREE**  
Available



**Ordering Information:** Si3445ADV-T1-E3 (Lead (Pb)-free)  
Si3445ADV-T1-GE3 (Lead (Pb)-free and Halogen-free)

**Marking Code:** C5XXX

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted				
Parameter	Symbol	5 s	Steady State	Unit
Drain-Source Voltage	V <sub>DS</sub>	- 8		V
Gate-Source Voltage	V <sub>GS</sub>	± 8		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	I <sub>D</sub>	T <sub>A</sub> = 25 °C	- 5.8	- 4.4
		T <sub>A</sub> = 70 °C	- 4.7	- 3.5
Pulsed Drain Current	I <sub>DM</sub>	- 20		A
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	- 1.7	- 0.9	
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	T <sub>A</sub> = 25 °C	2.0	1.1
		T <sub>A</sub> = 70 °C	1.3	0.7
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	R <sub>thJA</sub>	50	62.5	°C/W
	Steady State		90	110	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	22	30	

Notes:

a. Surface Mounted on FR4 board, t ≤ 5 s.

For SPICE model information via the Worldwide Web: [www.vishay.com/www/product/spice.htm](http://www.vishay.com/www/product/spice.htm)

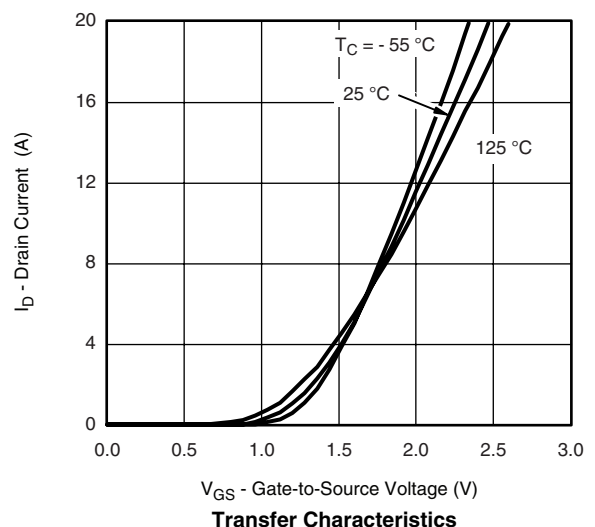
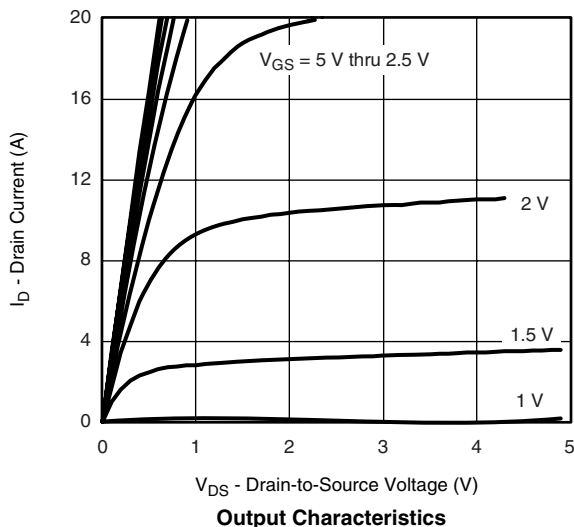
<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.45		-1.0	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -8\text{ V}, V_{GS} = 0\text{ V}$			-1	$\mu\text{A}$
		$V_{DS} = -8\text{ V}, V_{GS} = 0\text{ V}, T_J = 70\text{ }^\circ\text{C}$			-5	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -4.5\text{ V}$	-20			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -5.8\text{ A}$		0.034	0.042	$\Omega$
		$V_{GS} = -2.5\text{ V}, I_D = -4.9\text{ A}$		0.050	0.060	
		$V_{GS} = -1.8\text{ V}, I_D = -0.2\text{ A}$		0.065	0.080	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -4\text{ V}, I_D = -5.8\text{ A}$		16		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -1.7\text{ A}, V_{GS} = 0\text{ V}$		-0.8	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -4\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -5.8\text{ A}$		12.5	19	nC
Gate-Source Charge	$Q_{gs}$			2.4		
Gate-Drain Charge	$Q_{gd}$			2.6		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		8		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -4\text{ V}, R_L = 4\text{ }\Omega$ $I_D \cong -1.0\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 6\text{ }\Omega$		20	30	ns
Rise Time	$t_r$			40	60	
Turn-Off Delay Time	$t_{d(off)}$			80	120	
Fall Time	$t_f$			60	90	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = -1.7\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}$		55	85	

Notes:

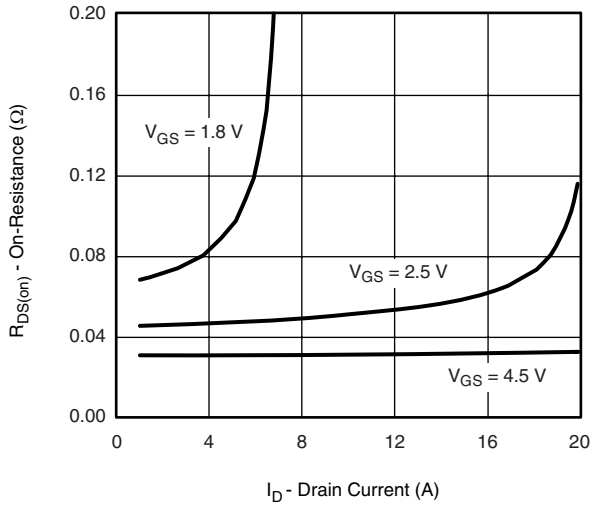
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

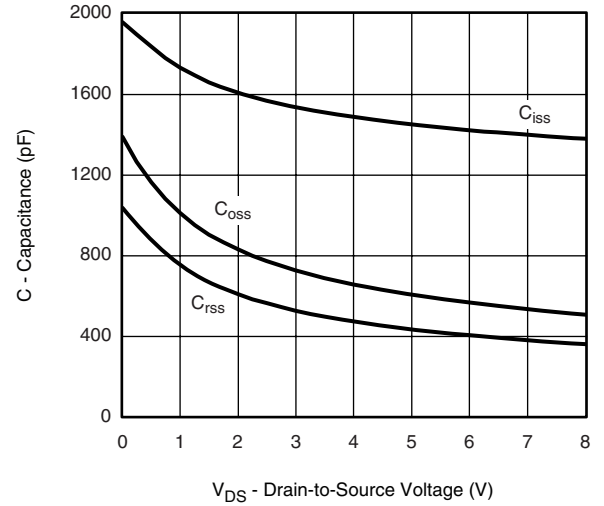
**TYPICAL CHARACTERISTICS**  $25\text{ }^\circ\text{C}$ , unless otherwise noted



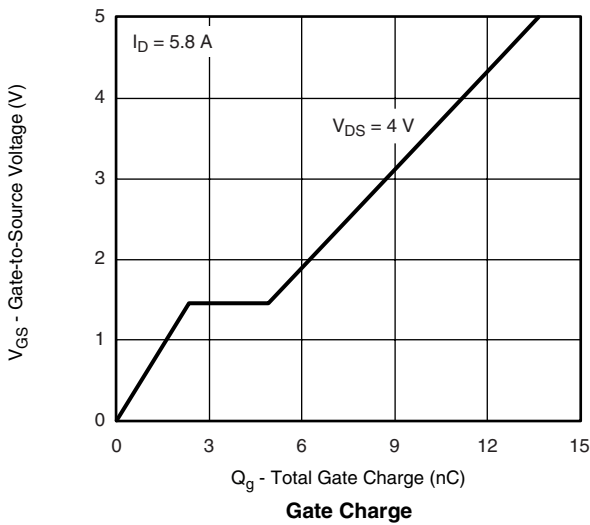
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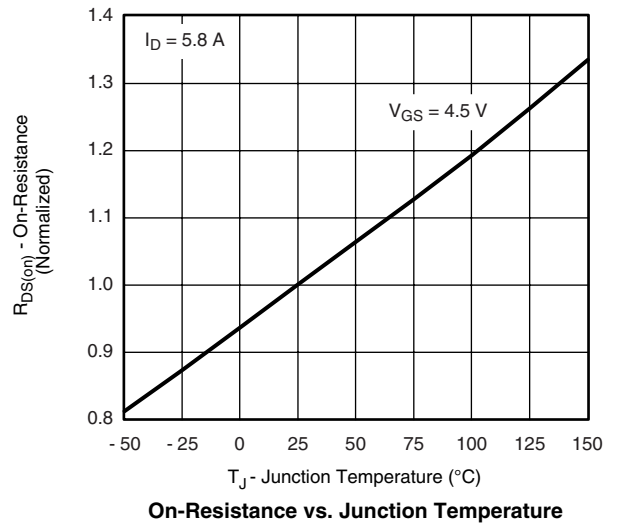
On-Resistance vs. Drain Current



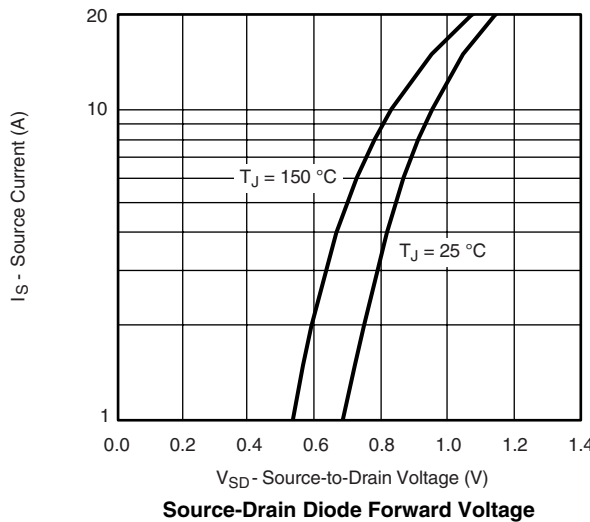
Capacitance



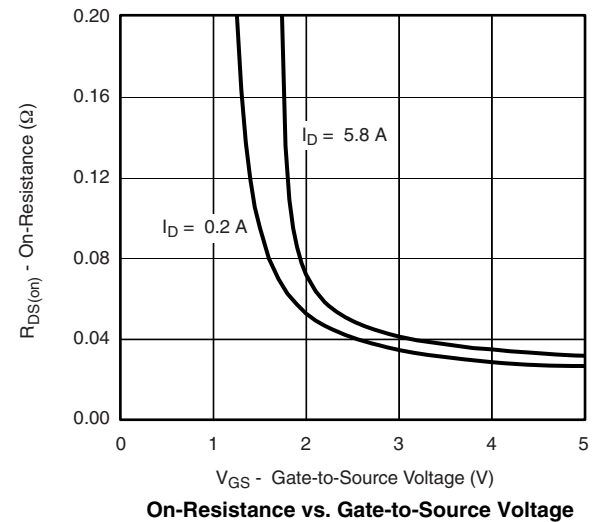
Gate Charge



On-Resistance vs. Junction Temperature

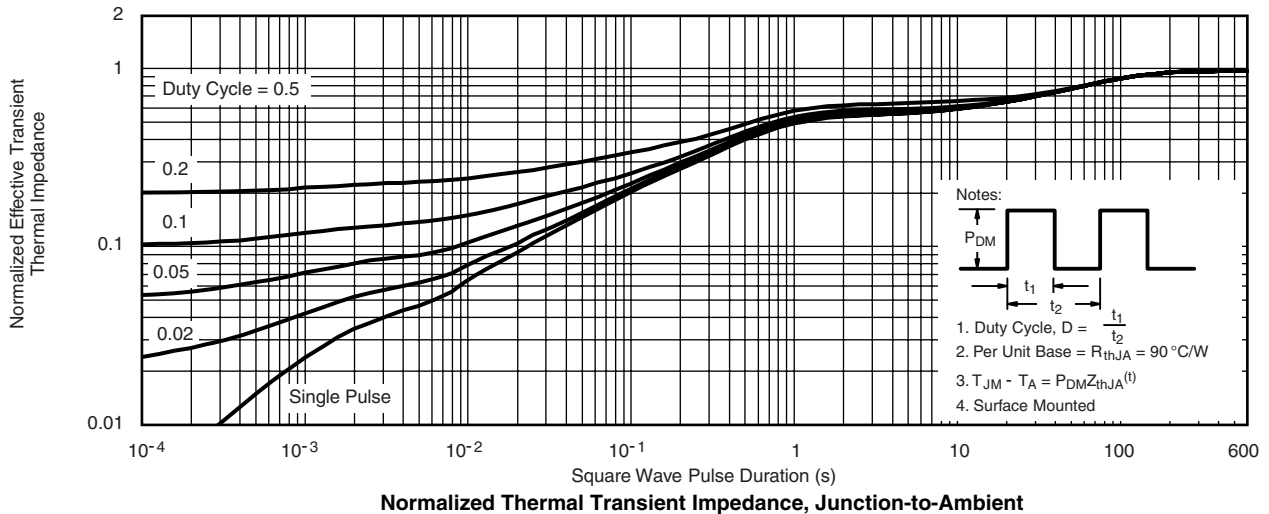
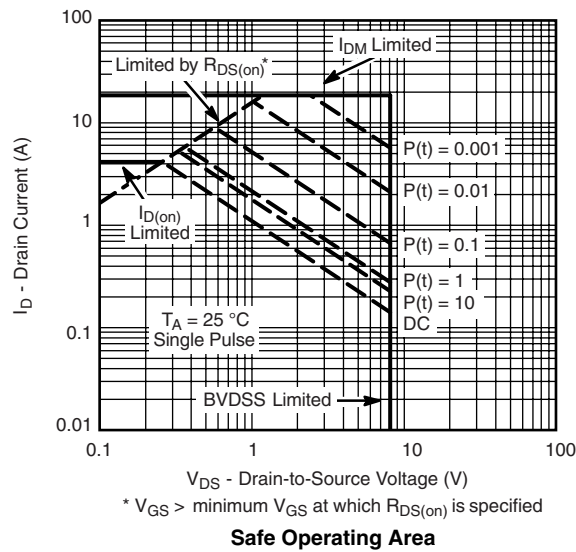
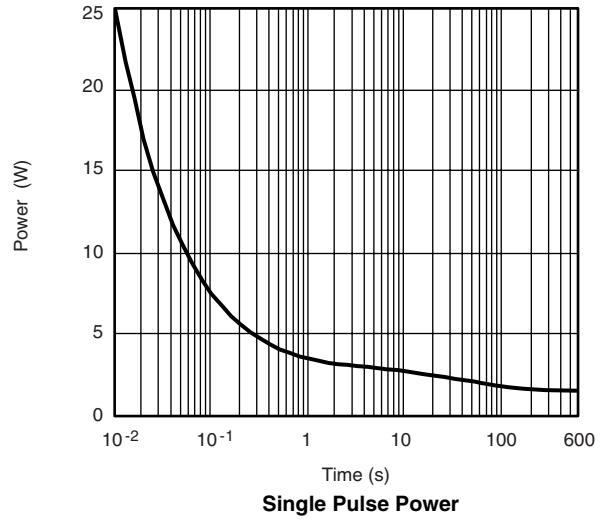
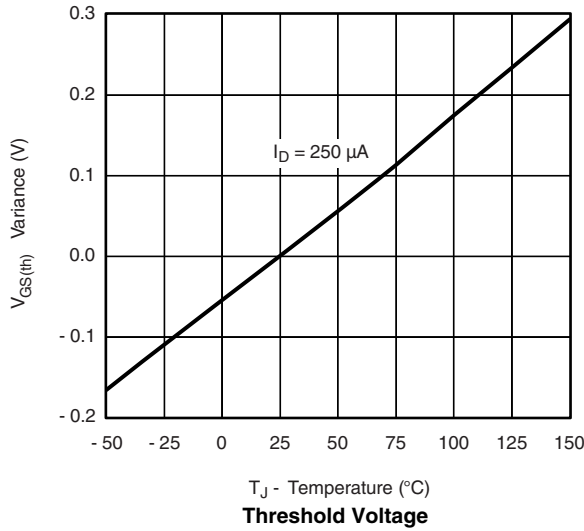


Source-Drain Diode Forward Voltage

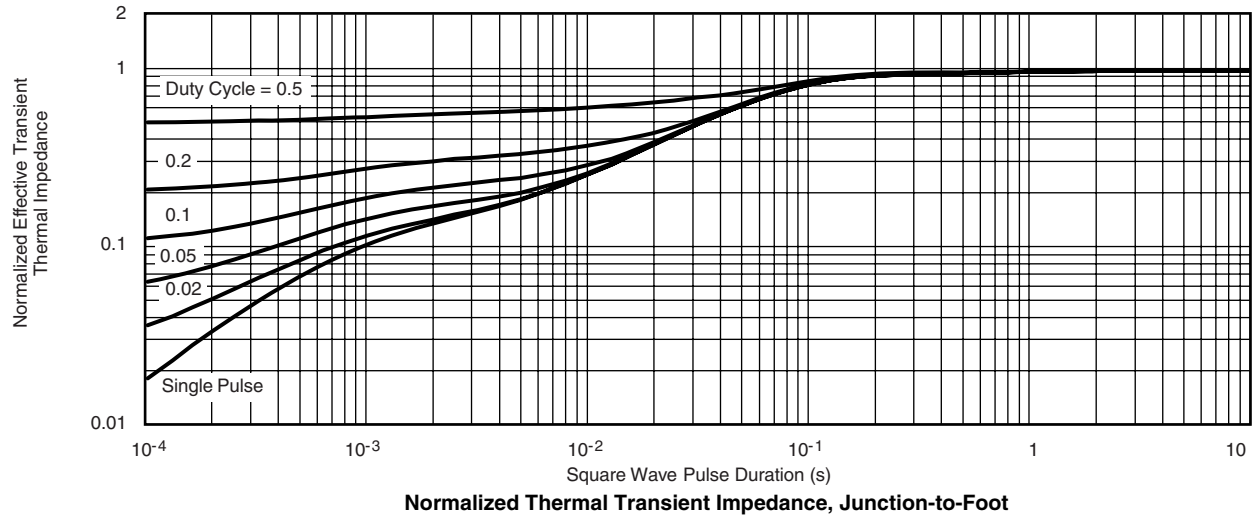


On-Resistance vs. Gate-to-Source Voltage

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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