



SOT223 N-CHANNEL ENHANCEMENT MODE VERTICAL DMOS FET

Product Summary

V _{(BR)DSS}	Max R _{DS(on)}	Max I _D T _A = +25°C	
60V	$1\Omega @ V_{GS} = 10V$	1A	

Description and Applications

- **DC-DC Converters**
- Solenoid / Relay Drivers for Automotive Applications
- Stepper Motor Drivers and Print Head Drivers •

Features and Benefits

- Compact Geometry
- Fast Switching Speeds •
- No Secondary Breakdown and Excellent Temperature Stability
- High Input Impedance and Low Current Drive
- Ease of Paralleling
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

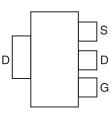
Mechanical Data

- Case: SOT223 .
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208@3
- Weight: 0.112 grams (Approximate)

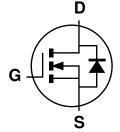


SOT223

Top View



Pin Out Top-view



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
ZVN4206GTA	Standard	SOT223	1,000
ZVN4206GTC	Standard	SOT223	4,000

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied. 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

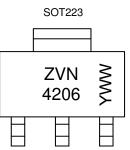
and Lead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain < 900ppm bromine, < 900ppm chlorine (< 1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



ZVN 4206 = Product Type Marking Code YWW = Date Code Marking Y or \overline{Y} = Last Digit of Year (ex: 5= 2015) WW or $\overline{W}W =$ Week Code (01~53)



Maximum Ratings (@T_A = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	ID	1	A
Pulsed Drain Current	I _{DM}	8	А

Thermal Characteristics (@T_A = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation at T _A =+25 °C	P _{tot}	2	W
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.)

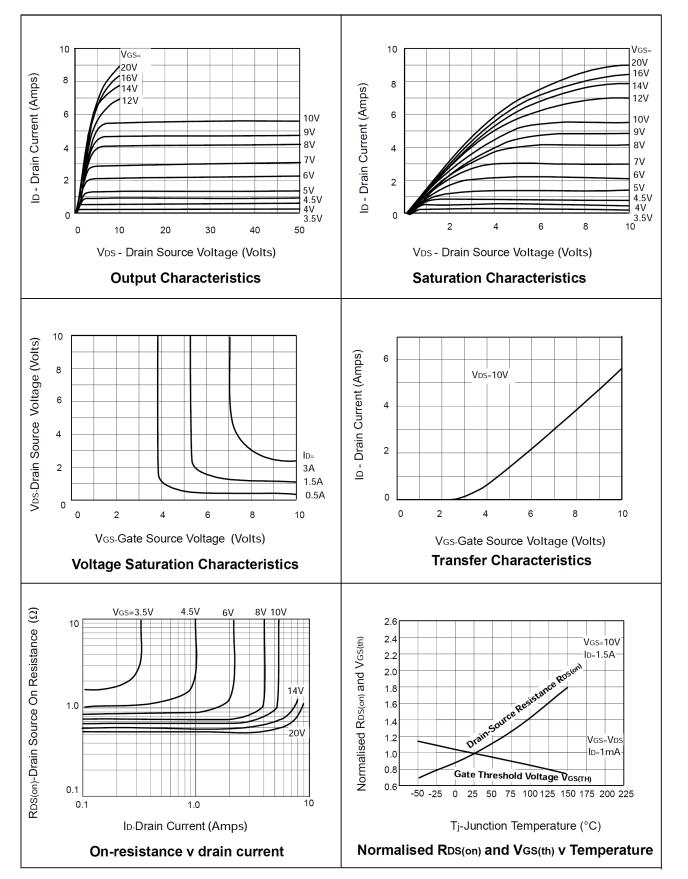
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	—	V	$I_D = 1mA$, $V_{GS} = 0V$	
		_	_	10		$V_{DS} = 60V, V_{GS} = 0V$	
Zero Gate Voltage Drain Current	IDSS			100	μA	V _{DS} = 48V, V _{GS} = 0V , T=+125 ℃ (Note 6)	
Gate-Body Leakage	I _{GSS}	_	—	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
Gate-Source Threshold Voltage	V _{GS(th)}	1.3	_	3	V	$I_D = 1mA$, $V_{DS} = V_{GS}$	
ON CHARACTERISTICS							
On-State Drain Current (Note 5)	I _{D(on)}	3	_	_	Α	$V_{DS} = 25V, V_{GS} = 10V$	
Statia Drain Source On State Registeres (Note E)		_	_	1	Ω	$V_{GS} = 10V, I_D = 1.5A$	
Static Drain-Source On-State Resistance (Note 5)	R _{DS} (ON)	_	_	1.5		$V_{GS} = 5V, I_D = 0.5A$	
Forward Transconductance (Notes 5 & 6)	g fs	300	_	_	mS	V _{DS} = 25V, I _D = 1.5A	
DYNAMIC CHARACTERISTICS						• -	
Input Capacitance (Note 6)	Ciss	_	_	100	pF		
Output Capacitance (Note 6)	Coss	_	_	60	pF	$V_{DS} = 25 V, V_{GS} = 0V$	
Reverse Transfer Capacitance (Note 6)	C _{rss}	_	_	20	pF	-f = 1MHz	
Turn-On Delay Time (Notes 6 & 7)	t _{d(on)}	_	_	8	ns		
Turn-On Rise Time (Notes 6 & 7)	tr	_	_	12	ns	V _{DD} ≈ 25V, V _{GEN} = 10V	
Turn-Off Delay Time (Notes 6 & 7)	t _{d(off)}	_	—	12	Ns	I _D = 1.5A	
Turn-Off Fall Time (Notes 6 & 7)	tf	_	—	15	Ns	1	
Notes: 5. Measured under pulsed conditions. Width=300	us. Duty cycle ≤	2%.				-	

6. Sample test. 7. Switching times measured with 50Ω source impedance and <5ns rise time on a pulse generator.



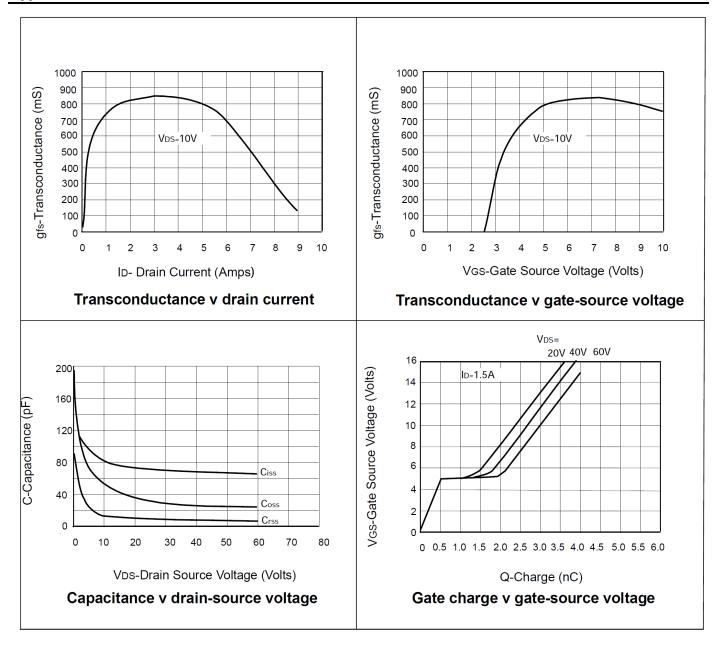
ZVN4206G

Typical Characteristics





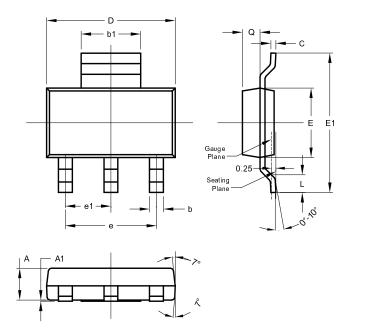
Typical Characteristics (continued)





Package Outline Dimensions

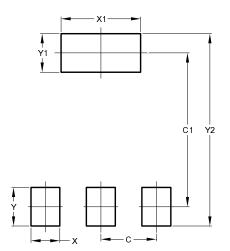
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	SOT	-000			
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Ш	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
C2	8.00



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