

### 74HCT04

#### **HEX INVERTERS**

# Description

The 74HCT04 provides provides six independent inverters with standard push-pull outputs. The device is designed for operation with a power supply range of 4.5V to 5.5V.

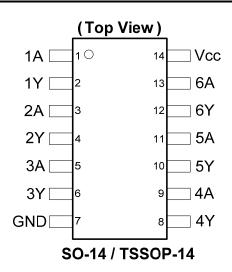
The gates perform the Boolean function:

 $\mathsf{Y}=\overline{\mathsf{A}}$ 

### Features

- Wide Supply Voltage Range from 4.5V to 5.5V
- Pin Compatible with Low Power Schottky (LSTTL)
- Inputs Are TTL Voltage Level Compatible
- Sinks or Sources 4mA at V<sub>CC</sub> = 4.5V
- CMOS Low Power Consumption
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
  - 200-V Machine Model (A115-A)
  - 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

# Pin Assignments



### Applications

- General Purpose Logic
- Wide array of products such as:
  - PCs, networking, notebooks, netbooks
  - Computer peripherals, hard drives, CD/DVD ROM
  - TV, DVD, DVR, set top box

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

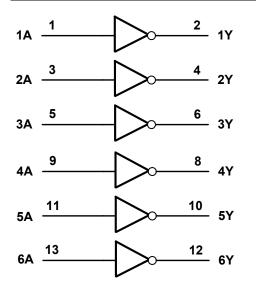
See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</li>



# **Pin Descriptions**

Pin Number	Pin Name	Function
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	V <sub>CC</sub>	Supply Voltage

# Logic Diagram



# **Function Table**

Input	Output
A	Y
Н	L
L	Н



# Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I <sub>IK</sub>	Input Clamp Current V <sub>I</sub> < -0.5V or Vi > V <sub>CC</sub> +0.5V	±20	mA
Ι <sub>ΟΚ</sub>	Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} + 0.5V$	±20	mA
lo	Continuous Output Current -0.5V < V <sub>O</sub> V <sub>CC</sub> +0.5V	+/- 25	mA
lcc	Continuous Current Through Vcc	50	mA
I <sub>GND</sub>	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
Ртот	Total Power Dissipation	500	mW

Notes: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

5. Input Voltage cannot exceed  $V_{CC}$  to the extent the Maximum clamp current is exceeded.

## Recommended Operating Conditions (Note 6) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		4.5	5.5	V
VI	Input Voltage		0	Vcc	V
Vo	Output Voltage		0	V <sub>CC</sub>	V
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC}$ = 4.5V to 5.5V	-	500	ns/V
T <sub>A</sub>	Operating Free-Air Temperature		-40	+125	°C

Note: 6. Unused inputs should be held at  $V_{CC}$  or Ground.

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	V	T <sub>A</sub> = -40°0	C to +85°C	T <sub>A</sub> = -40°C	to +125°C	Unit
Symbol	Parameter	Test Conditions	V <sub>cc</sub>	Min	Max	Min	Max	Unit
V <sub>IH</sub>	High-Level Input Voltage		4.5V to 5.5V	2.0		2.0	—	V
V <sub>IL</sub>	Low-Level Input Voltage		4.5V to 5.5V	_	0.8	—	0.8	V
N/	V <sub>OH</sub> High-Level Output Voltage	Ι <sub>ΟΗ</sub> = -20μΑ	4.5V	4.4	—	4.4	_	v
VOH		I <sub>OH</sub> = -4mA	4.5V	3.80	—	3.70	—	v
N/	Low-Level Output	I <sub>OL</sub> = 20μΑ	4.5V	_	0.1	—	0.1	V
Vol	Voltage	I <sub>OL</sub> = 5.2mA	6.0V	_	0.33	—	0.4	V
li –	Input Current	V <sub>I</sub> = GND to 6.0V	6.0V	_	± 1	—	± 1	μA
Icc	Supply Current	$V_{I}$ = GND or $V_{CC}$ , $I_{O}$ = 0	6.0V	—	20	—	40	μA
ΔI <sub>CC</sub>	Additional Supply Current	One input at $V_{CC}$ -2.1V Other pins at $V_{CC}$ or GND	4.5V to 5.5V	_	675	_	735	μA



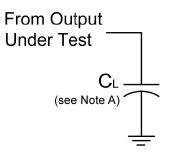
# **Switching Characteristics**

Symbol	ol Parameter Test		lest V		T <sub>A</sub> = +25°C		-40°C to +85°C	-40°C to +125°C	Unit
Symbol	Falameter	Conditions	V <sub>cc</sub>	Min	Тур	Max	Max	Max	Unit
t <sub>PD</sub>	Propagation Delay A <sub>N</sub> to Y <sub>N</sub>	Figure 1 C <sub>L</sub> = 50pF	4.5V	_	12	22	24	29	ns
t <sub>t</sub>	Transition time	Figure 1 C <sub>L</sub> = 50pF	4.5V	_	7	29	29	29	ns

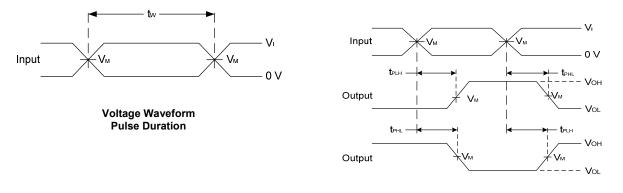
### Operating Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

	Parameter	Test Conditions	V <sub>CC</sub> = 5.5V Typ	Unit
C <sub>pd</sub>	Power Dissipation Capacitance per Gate	f = 1MHz	22	pF
CI	Input Capacitance	$V_I = V_{CC} - or GND$	4	pF

# Parameter Measurement Information



Vcc	Inp	Inputs		CL
	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	
4.5V	3.0V	3ns	1.5V	V <sub>OH</sub> /2



#### Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

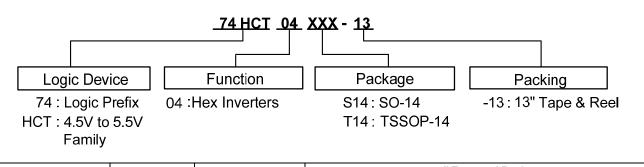
Notes: A.Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz
- C. Inputs are measured separately one transition per measurement
- D.  $t_{\mathsf{PLH}}$  and  $t_{\mathsf{PHL}}$  are the same as  $t_{\mathsf{PD}}$

#### Figure 1 Load Circuit and Voltage Waveforms



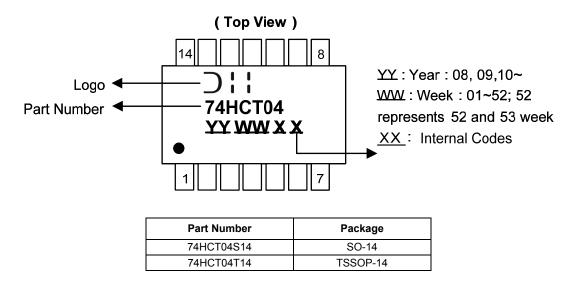
## **Ordering Information**



	Device	Package Code	Dockoging	7" Tape a	and Reel
	Device	Package Coue	Packaging	Quantity	Part Number Suffix
Lead-free Green	74HCT04S14-13	S14	SO-14	2500/Tape & Reel	-13
Pb, Lead-free Green	74HCT04T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

### **Marking Information**

### (1) SO-14, TSSOP-14

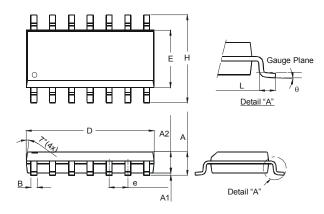




# Package Outline Dimensions (All dimensions in mm.)

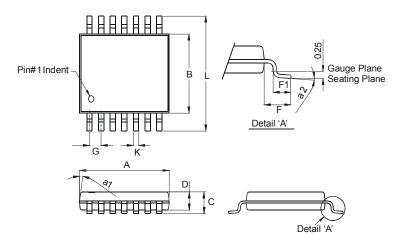
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### Package Type: SO-14



	SO-14			
Dim	Min	Max		
Α	1.47	1.73		
A1	0.10	0.25		
A2	1.45 Typ			
в	0.33	0.51		
D	8.53	8.74		
Е	3.80	3.99		
е	1.27	Тур		
Н	5.80	6.20		
L	0.38	1.27		
θ	0°	8°		
All Di	mensions	s in mm		

### Package Type: TSSOP-14



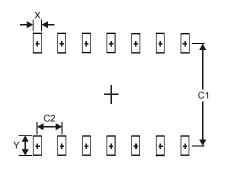
	TSSOP-1	4		
Dim	Min	Max		
a1	7° (	4X)		
a2	0°	8°		
Α	4.9	5.10		
В	4.30	4.50		
С	-	1.2		
D	0.8	1.05		
F	1.00	Тур		
F1	0.45	0.75		
G	0.65	Тур		
κ	0.19	0.30		
L	L 6.40 Typ			
All Dir	nensions	s in mm		



# Suggested Pad Layout

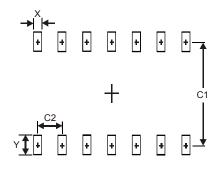
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

#### Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

### Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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