



NOTES:

- 1. ROHS COMPLIANT
- 2. HEADER: DAP (DIALLYL PHTHALATE) WITH FLAMMABILITY RATING UL 94V-0 OR BETTER.
- 3. STORAGE TEMPERATURE: -20°C TO +125°C
- 4. COMPLIANCE TO J-STD:
 - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
 - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
 - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
- 5. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX TO THE PART NUMBER(i.e H5014FNL BECOMES H5014FNLT).







FINAL OUTLINE

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,DUAL,1GD,1:1,SM,TU	H5014FNL-10	PS-2747.001-B	1 OF 3	H5014FNL	A

E-MAIL: PRODINFONETWORK@PULSEELECTRONICS.COM(US). ASIA@PUI SFFI FCTRONICS.COM(Asia)

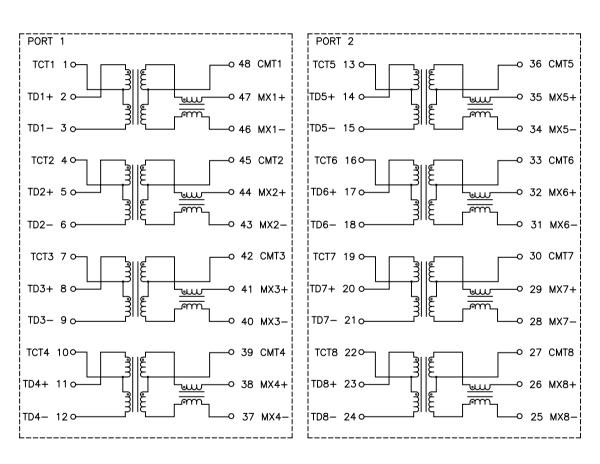
PHONE: USA:858 674 8100, GERMANY:49 7032 78060, SINGAPORE:65 6287 8998, SHANGHAI:86 21 62787060, TAIWAN:886 3 4356768





ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED

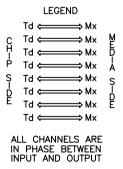
MEETS IEEE 802.3 SPECIFICATION



SCHEMATIC

PARAMETER	SPECIFICATIONS				
OPERATING TEMP	0°C – 70°C				
TURNS RATIO	1.0 ± 2%				
POLARITY	PER SCHEMATIC				
	100 KHz	1-125 MHz			
INSERTION LOSS	-1.2 dB MAX	-0.2-0.002*f ^{^1.4} dB MAX			
RETURN LOSS	.1-40 MHz	40-100 MHz			
$(Z OUT = 100 OHM \pm 15\%)$	-16 dB MIN	-10+20*LOG ₁₀ (f/80 MHz) dB MIN			
INDUCTANCE (OCL) (MEDIA SIDE, 0°C-70°C)	350 uH MIN	(MEASURED AT 100 KHz, 100 mVRMS) AND WITH 8 mA DC BIAS			
	1 MHz	10-100 MHz			
CROSSTALK, ADJACENT CHANNELS	-50 dB MIN	-55+22*LOG ₁₀ (f/10) dB MIN			
COMMON MODE	2 MHz	30-200 MHz			
REJECTION RATIO	-50 dB MIN	-15+20*LOG ₁₀ (f/200) dB MIN			
DC RESISTANCE, 1/2 WINDING	.65 OHMS MAX				
DC RESISTANCE IMBALANCE	±.065 OHMS	S MAX (CENTER TAP SYMMETRY)			
INPUT – OUTPUT ISOLATION	1500 VRMS MIN @ 60 SECONDS				

NOTE: f IS FREQUENCY IN MHz.



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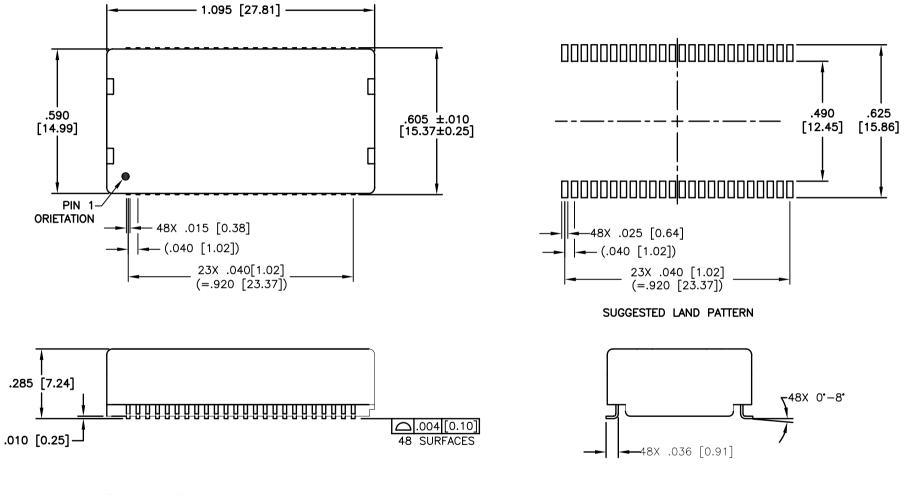
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DIMENSIONS ARE IN INCHES [MILLIMETERS] WITH THE FOLLOWING TOLERANCES: [MILLIMETERS] ARE FOR REFERENCE ONLY. .XX= \pm .01 [\pm 0.25] .XXX= \pm .005 [\pm 0.13]

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Mouser Electronics

Authorized Distributor

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Pulse: H5014FNLT H5014FNL