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FCP22N60N / FCPF22N60NT N-Channel SupreMOS[®] MOSFET 600 V, 22 A, 165 mΩ

Features

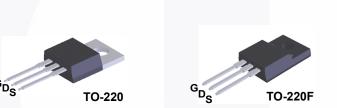
- BV_{DSS} > 650 V @ T_J = 150°C
- R_{DS(on)} = 140 mΩ (Typ.) @ V_{GS} = 10 V, I_D = 11 A
- Ultra Low Gate Charge (Typ. $Q_q = 45 \text{ nC}$)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 196.4 pF)
- 100% Avalanche Tested
- RoHS Compliant

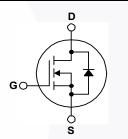
Application

- LCD/LED/PDP TV
- Lighting
- Solar Inverter
- AC-DC Power Supply

Description

The SupreMOS[®] MOSFET is Fairchild Semiconductor's next generation of high voltage super-junction (SJ) technology employing a deep trench filling process that differentiates it from the conventional SJ MOSFETs. This advanced technology and precise process control provides lowest Rsp on-resistance, superior switching performance and ruggedness. SupreMOS MOSFET is suitable for high frequency switching power converter applications such as PFC, server/telecom power, FPD TV power, ATX power, and industrial power applications.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol			Parameter		FCP22N60N	FCPF22N60NT	Unit	
V _{DSS}	Drain to Sourc	e Voltage			6	00	V	
V _{GSS}	Gate to Source	e Voltage			±	45	V	
	Duraine Quantant		- Continuous (T _C = 25°C)		22	22*		
I _D Drain Current			- Continuous (T _C = 100 ^o C)		13.8	13.8*	A	
I _{DM}	Drain Current		- Pulsed	(Note 1)	66	66*	А	
E _{AS}	Single Pulsed	Avalanche En	ergy	(Note 2)	6	72	mJ	
I _{AR}	Avalanche Cui	rrent		(Note 1)	7	7.3	А	
E _{AR}	Repetitive Ava	lanche Energy	/	(Note 1)	2	.75	mJ	
dv/dt	MOSFET dv/d	t			1	00	V/ns	
av/at	Peak Diode Re	ecovery dv/dt		(Note 3)	2	20	v/ns	
	Devuer Dissing	41. m	(T _C = 25 ^o C)		205	39	W	
P _D	Power Dissipa	tion	- Derate Above 25°C		1.64	0.31	W/ºC	
T _J , T _{STG}	Operating and	Storage Temp	erature Range		-55 to	o +150	°C	
TL	Maximum Lea	d Temperature	for Soldering, 1/8" from Case for 5	Seconds	3	00	°C	

*Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FCP22N60N	FCPF22N60NT	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.61	3.2	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	0/00

November 2013

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Tape Width

N/A

N/A

Тур.

Min.

Quantity

50 units

50 units

Max.

Part Number	Top Mark	Package	Packing Method	Reel Siz
FCP22N60N	FCP22N60N	TO-220	Tube	N/A
FCPF22N60NT	FCPF22N60NT	TO-220F	Tube	N/A
ectrical Cha	racteristics T _C = 2	5°C unless oth	erwise noted.	

BV _{DSS}	Drain to Source Breakdown Voltage	I _D = 1 mA, V _{GS} = 0 V, T _J = 25 ^o C	600	-	-	V
		I _D = 1 mA, V _{GS} = 0 V, T _J = 150 ^o C	650	-	-	v
ΔBV_{DSS} / ΔT_{J}	Breakdown Voltage Temperature Coefficient	$I_D = 1 \text{ mA}$, Referenced to 25°C	-	0.68	-	V/ºC
1	Zero Gate Voltage Drain Current	V _{DS} = 480 V, V _{GS} = 0 V	-	-	10	μA
DSS	Zero Gale Voltage Drain Current	V _{DS} = 480 V, T _J = 125 ^o C	-	-	100	μΑ
I _{GSS}	Gate to Body Leakage Current	V_{GS} = ±45 V, V_{DS} = 0 V	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \ \mu A$	2.0	3.0	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 11 A	-	0.140	0.165	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 11 A	-	22	-	S

Dynamic Characteristics

Input Capacitance	V 400 V V 0 V	-	1950	-	pF
Output Capacitance		-	75.9	-	pF
Reverse Transfer Capacitance		-	3	-	pF
Output Capacitance	V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz	-	43.2	-	pF
Effective Output Capacitance	V_{DS} = 0 V to 480 V, V_{GS} = 0 V	-	196.4	-	pF
Total Gate Charge at 10V	V _{DS} = 380 V. I _D = 11 A.	-	45	-	nC
Gate to Source Gate Charge	V _{GS} = 10 V	-	8.7	-	nC
Gate to Drain "Miller" Charge	(Note 4)	-	14.5	-	nC
Equivalent Series Resistance (G-S)	f = 1 MHz	-	1	-	Ω
	Output Capacitance Reverse Transfer Capacitance Output Capacitance Effective Output Capacitance Total Gate Charge at 10V Gate to Source Gate Charge Gate to Drain "Miller" Charge	Output Capacitance $V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V},$ f = 1 MHzReverse Transfer Capacitancef = 1 MHzOutput Capacitance $V_{DS} = 380 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f = 1 MHz}$ Effective Output Capacitance $V_{DS} = 0 \text{ V to } 480 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ Total Gate Charge at 10V $V_{DS} = 380 \text{ V}, \text{ I}_{D} = 11 \text{ A},$ Gate to Source Gate Charge $V_{GS} = 10 \text{ V}$ Gate to Drain "Miller" Charge(Note 4)	$\begin{tabular}{ c c c c c c c } \hline U_{DS} &= 100 \ V, \ V_{GS} &= 0 \ V, \\ f &= 1 \ MHz \end{tabular} & - \\ \hline f &= 1 \ MHz \end{tabular} & - \\ \hline Output Capacitance \end{tabular} & V_{DS} &= 380 \ V, \ V_{GS} &= 0 \ V, \ f &= 1 \ MHz \end{tabular} & - \\ \hline Output Capacitance \end{tabular} & V_{DS} &= 380 \ V, \ V_{GS} &= 0 \ V, \ f &= 1 \ MHz \end{tabular} & - \\ \hline Effective Output Capacitance \end{tabular} & V_{DS} &= 0 \ V \ to \ 480 \ V, \ V_{GS} &= 0 \ V \end{tabular} & - \\ \hline Total \ Gate \ Charge \end{tabular} & to \ V_{DS} &= 380 \ V, \ I_D &= 11 \ A, \end{tabular} & - \\ \hline Gate \ to \ Drain \ "Miller" \ Charge \end{tabular} & (Note \ 4) \end{tabular} & - \\ \hline \end{tabular}$	Output Capacitance $V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1 MHz-75.9Reverse Transfer Capacitancef = 1 MHz-3Output Capacitance $V_{DS} = 380 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ -43.2Effective Output Capacitance $V_{DS} = 0 \text{ V}$ to $480 \text{ V}, V_{GS} = 0 \text{ V}$ -196.4Total Gate Charge at 10V $V_{DS} = 380 \text{ V}, I_D = 11 \text{ A},$ -45Gate to Source Gate Charge $V_{GS} = 10 \text{ V}$ -8.7Gate to Drain "Miller" Charge(Note 4)-14.5	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $

Switching Characteristics

t _{d(on)}	Turn-On Delay Time			-	16.9	-	ns
t _r	Turn-On Rise Time	V _{DD} = 380 V, I _D = 11 A		-	16.7	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10 V, R _G = 4.7 Ω		-	49	-	ns
t _f	Turn-Off Fall Time		(Note 4)	-	4	-	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	22	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Fe	orward Current	-	-	66	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 11 A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 11 A	-	350	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/μs	-	6	-	μC

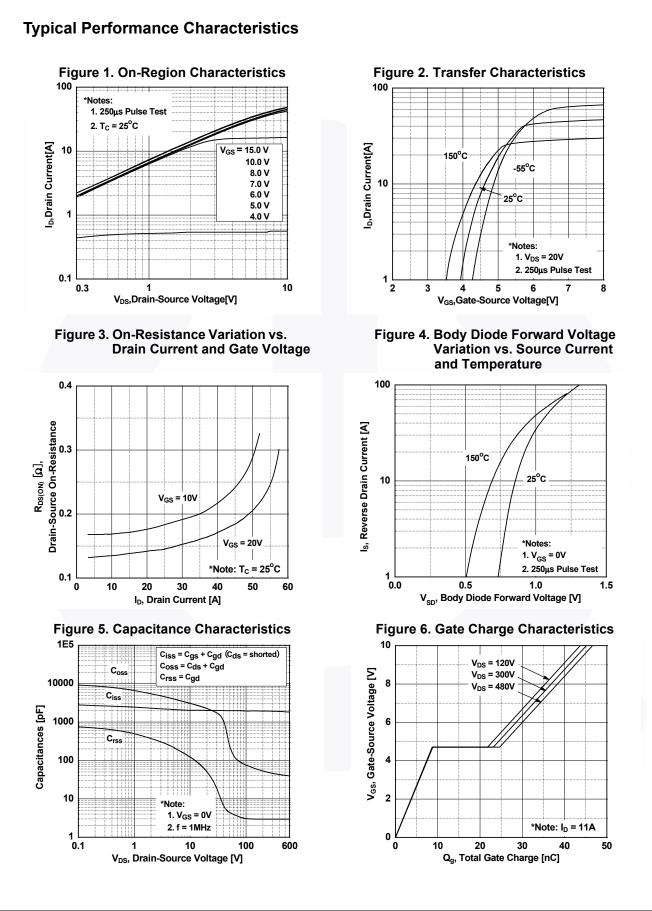
Notes:

1. Repetitive rating: pulse width-limited by maximum junction temperature.

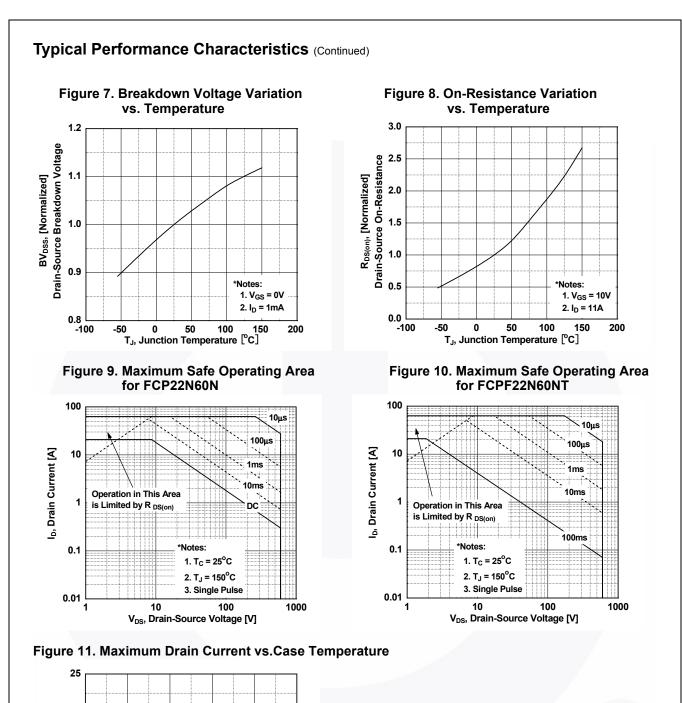
2. I_{AS} = 7.3 A, R_{G} = 25 Ω , starting T_{J} = 25°C.

3. I_{SD} \leq 22 A, di/dt \leq 200 A/µs, V_{DD} \leq 380 V, starting T_J = 25°C.

4. Essentially independent of operating temperature typical characteristics.



3



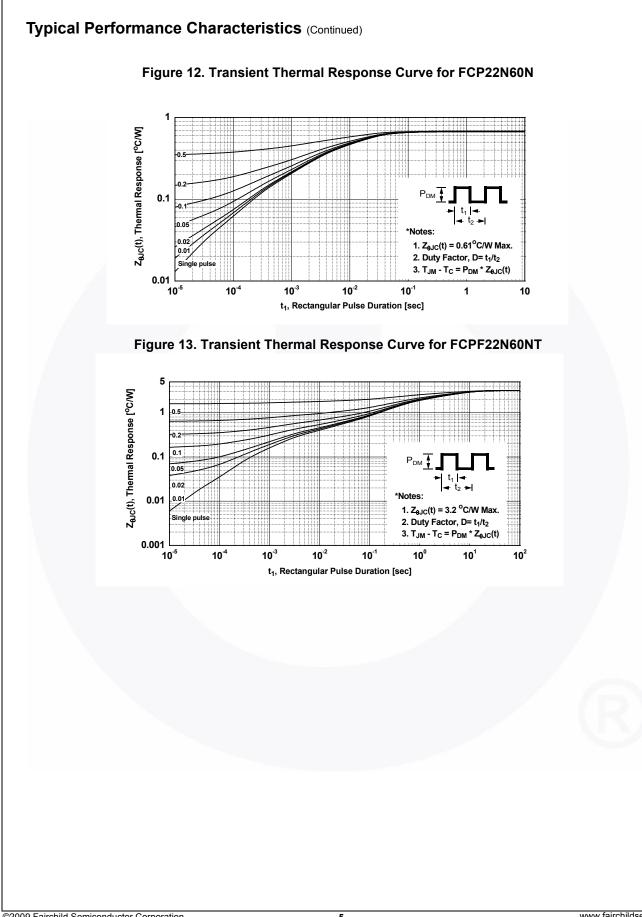
5 0 ∟ 25 50 75 100 125 T_c, Case Temperature [°C] ©2009 Fairchild Semiconductor Corporation

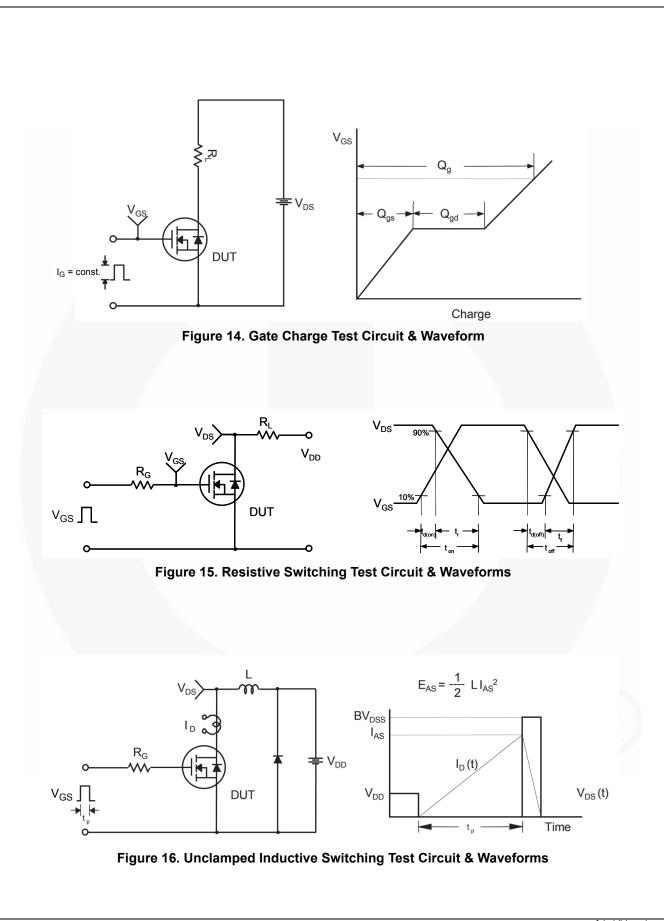
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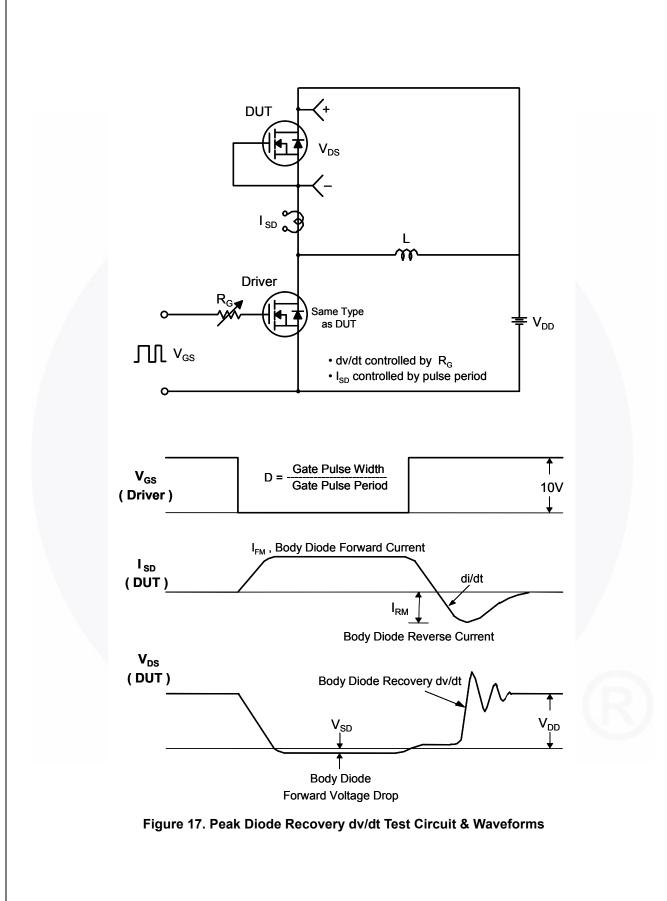
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I_D, Drain Current [A] 15

150







FCP22N60N / FCPF22N60NT — N-Channel SupreMOS[®] MOSFET

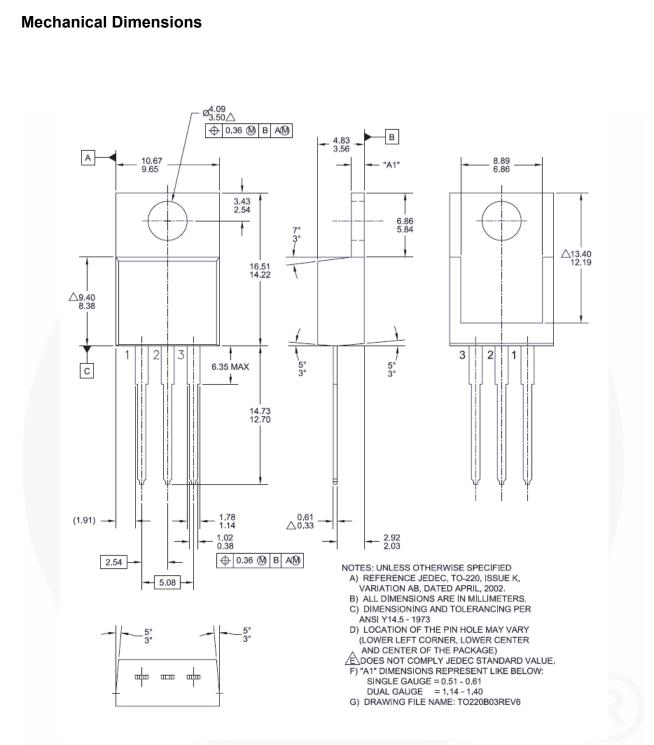
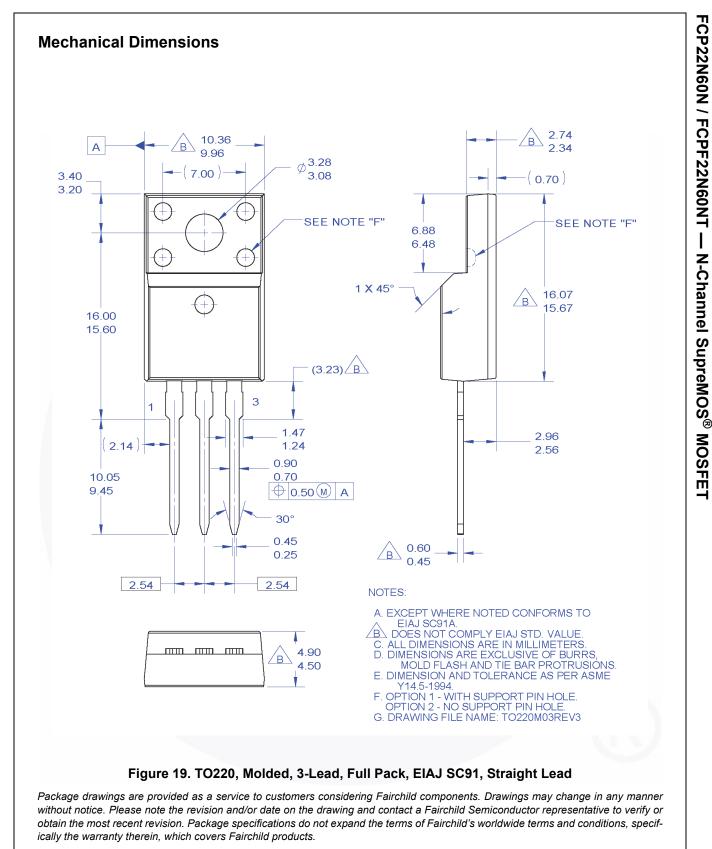


Figure 18. TO-220, Molded, 3-Lead, Jedec Variation AB

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