

Solid Tantalum Chip Capacitors TANTAMOUNT[™], Hi-Rel COTS, Low ESR, Built-in-Fuse, Conformal Coated Case



PERFORMANCE / ELECTRICAL CHARACTERISTICS

www.vishay.com/doc?40209

Operating Temperature: -55 °C to +125 °C (above 85 °C, voltage derating is required) **Capacitance Range:** 10 μ F to 680 μ F **Capacitance Tolerance:** \pm 20 %, \pm 10 % standard **Voltage Rating:** 4 V_{DC} to 50 V_{DC} **Moisture Sensitivity Level 2a**

FEATURES

- High reliability design with reliability screening available
- Surge current testing per MIL-PRF-55365
 options available
- Standard and low ESR options
- Terminations: SnPb, standard. 100 % tin available
- · Circuit protection for mission or safety critical systems
- Fuse characteristics: guaranteed fuse protection at 9 A, 100 ms
- Mounting: surface mount
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

ORD	ERING	INFORMATI	ON					
T96	R	107	к	010	E	S	Α	S
TYPE	CASE CODE		CAPACITANCE TOLERANCE	DC VOLTAGE RATING AT +85 °C	TERMINATION AND PACKAGING	RELIABILITY LEVEL	SURGE CURRENT	ESR
	See Ratings and Case Codes Table.	This is expressed in picofarads. The first two digits are the significant figures. The third is the number of zeros to follow.	K = ± 10 % M = ± 20 %	This is expressed in volts. To complete the three-digit block, zeros precede the voltage rating. A decimal point is indicated by an "R" (6R3 = 6.3 V)	E: Sn / Pb solder / 7" (178 mm) reels L: Sn / Pb solder / 7" (178 mm) ½ reel C: 100 % tin / 7" (178 mm) reels H: 100 % tin / 7" (178 mm) ½ reel	S = 40 h burn-in Z = non- established reliability	A = 10 cycles at +25 °C B = 10 cycles at -55 °C / +85 °C S = 3 cycles at +25 °C	S = std L = low

Note

- We reserve the right to supply higher voltage ratings and tighter capacitance tolerance capacitors in the same case size. Low ESR solid tantalum chip capacitors allow delta ESR of 1.25 times the datasheet limits after mounting
- **DIMENSIONS** in inches [millimeters] Tantalum wire Nib indentifies Anode (+) terminal 0 ÷ н w W H1 CASE CODE J (MAX.) L (MAX.) w H1 н F (REF.) D (REF.) 0.004 0.295 0.243 + 0.012 / - 0.024 0.156 ± 0.012 0.146 ± 0.012 0.079 0.248 R [7.5] [6.2 + 0.3 / - 0.6] $[4.0 \pm 0.3]$ $[3.7 \pm 0.3]$ [2.0] [6.3] [0.1]

Note

• The anode termination (D less B) will be a minimum of 0.010" (0.25 mm)

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T96

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RATINGS AND CASE CODES									
CAP. (µF)	4 V	6.3 V	10 V	16 V	20 V	25 V	35 V	50 V	
10								R	
15							R	R	
22							R	R	
33						R	R		
47					R		R		
68				R		R			
100			R		R	R			
120			R		R				
150			R		R				
180		R		R					
220		R	R	R					
330	R		R	R					
390		R							
470			R						
680		R	R						

CAPACITANCE (µF)	CASE CODE	PART NUMBER	MAX. DCL AT +25 °C (μΑ)	MAX. DF AT +25 °C 120 Hz (%)	STD. (S) MAX. ESR AT +25 °C 100 kHz (Ω)	LOW (L) MAX. ESR AT +25 °C 100 kHz (Ω)	AVAILABLE RELIABILITY LEVELS
		4 V _{DC} A	T +85 °C; 2.7 \	/ _{DC} AT +125 °	С		
330	R	T96R337(1)004(2)(3)(4)(5)	13.2	8	0.230	0.180	S, Z
		6.3 V _{DC}	, AT +85 °C; 4	V _{DC} AT 125 °C)		
180	R	T96R187(1)6R3(2)(3)(4)(5)	10.8	8	0.230	0.180	S, Z
220	R	T96R227(1)6R3(2)(3)(4)(5)	13.2	8	0.230	0.180	S, Z
390	R	T96R397(1)6R3(2)(3)(4)(5)	23.4	8	0.230	0.145	S, Z
680	R	T96R687(1)6R3(2)(3)(4)(5)	40.8	12	0.190	0.145	S, Z
		10 V _{DC}	AT +85 °C; 7	V _{DC} AT 125 °C	;		
100	R	T96R107(1)010(2)(3)(4)(5)	10.0	8	0.240	0.175	S, Z
120	R	T96R127(1)010(2)(3)(4)(5)	12.0	8	0.240	0.170	S, Z
150	R	T96R157(1)010(2)(3)(4)(5)	15.0	8	0.230	0.165	S, Z
220	R	T96R227(1)010(2)(3)(4)(5)	22.0	8	0.230	0.155	S, Z
330	R	T96R337(1)010(2)(3)(4)(5)	33.0	8	0.230	0.155	S, Z
470	R	T96R477(1)010(2)(3)(4)(5)	47.0	14	0.220	0.145	S, Z
680	R	T96R687(1)010(2)(3)(4)(5)	68.0	14	0.190	0.145	S, Z
		16 V _{DC} /	AT +85 °C; 10 \	V _{DC} AT +125 °	C		
68	R	T96R686(1)016(2)(3)(4)(5)	10.9	6	0.700	0.195	S, Z
180	R	T96R187(1)016(2)(3)(4)(5)	28.8	8	0.230	0.155	S, Z
220	R	T96R227(1)016(2)(3)(4)(5)	35.2	8	0.220	0.155	S, Z
330	R	T96R337(1)016(2)(3)(4)(5)	52.8	14	0.210	0.155	S, Z
		20 V _{DC} /	AT +85 °C; 13 \	V _{DC} AT +125 °	C		
47	R	T96R476(1)020(2)(3)(4)(5)	9.4	6	0.300	0.210	S, Z
100	R	T96R107(1)020(2)(3)(4)(5)	20.0	8	0.240	0.170	S, Z
120	R	T96R127(1)020(2)(3)(4)(5)	24.0	8	0.240	0.180	S, Z
150	R	T96R157(1)020(2)(3)(4)(5)	30.0	8	0.240	0.175	S, Z

Note

Part number definitions: ٠

(1) Capacitance tolerance: K, M

(2) Termination and packaging: C, E, H, L(3) Reliability level

(4) Surge current: A, B, S(5) ESR: L, S

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STANDARD	RATING	GS					
CAPACITANCE (µF)	CASE CODE	PART NUMBER	MAX. DCL AT +25 °C (μΑ)	MAX. DF AT +25 °C 120 Hz (%)	STD. (S) MAX. ESR AT +25 °C 100 kHz (Ω)	LOW (L) MAX. ESR AT +25 °C 100 kHz (Ω)	AVAILABLE RELIABILITY LEVELS
		25 V _{DC} /	AT +85 °C; 17 \	V _{DC} AT +125 °	°C		
33	R	T96R336(1)025(2)(3)(4)(5)	8.3	6	0.350	0.230	S, Z
68	R	T96R686(1)025(2)(3)(4)(5)	17.0	6	0.300	0.195	S, Z
100	R	T96R107(1)025(2)(3)(4)(5)	25.0	8	0.300	0.190	S, Z
		35 V _{DC} /	AT +85 °C; 23 \	V _{DC} AT +125 °	°C		
15	R	T96R156(1)035(2)(3)(4)(5)	5.3	6	0.480	0.290	S, Z
22	R	T96R226(1)035(2)(3)(4)(5)	7.7	6	0.380	0.340	S, Z
33	R	T96R336(1)035(2)(3)(4)(5)	11.6	6	0.380	0.300	S, Z
47	R	T96B476(1)035(2)(3)(4)(5)	16.5	6	0.350	0 270	S 7

47	R	T96R476(1)035(2)(3)(4)(5)	16.5	6	0.350	0.270	S, Z			
50 V _{DC} AT +85 °C; 33 V _{DC} AT +125 °C										
10	R	T96R106(1)050(2)(3)(4)(5)	5.0	6	0.750	0.600	S, Z			
15	R	T96R156(1)050(2)(3)(4)(5)	7.5	6	0.500	0.450	S, Z			
22	R	T96R226(1)050(2)(3)(4)(5)	11.0	6	0.490	0.400	S, Z			

Note

Part number definitions: ٠

(1) Capacitance tolerance: K, M

(2) Termination and packaging: C, E, H, L

(3) Reliability level(4) Surge current: A, B, S

(5) ESR: L, S

POWER DISSIPATION	
CASE CODE	MAXIMUM PERMISSIBLE POWER DISSIPATION AT +25 °C (W) IN FREE AIR
R	0.250

STANDARD PACKAGING QUANTITY							
CASE CODE	UNITS PER REEL						
CASE CODE	7" FULL REEL	7" HALF REEL					
R	300	150					

PRODUCT INFORMATION	
Conformal Coated Guide	
Pad Dimensions	www.vishay.com/doc?40150
Packaging Dimensions	
Moisture Sensitivity (MSL)	www.vishay.com/doc?40135
SELECTOR GUIDES	
Solid Tantalum Selector Guide	www.vishay.com/doc?49053
Solid Tantalum Chip Capacitors	www.vishay.com/doc?40091
FAQ	
Frequently Asked Questions	www.vishay.com/doc?40110

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Guide for Conformal Coated Tantalum Capacitors

INTRODUCTION

Tantalum electrolytic capacitors are the preferred choice in applications where volumetric efficiency, stable electrical parameters, high reliability, and long service life are primary considerations. The stability and resistance to elevated temperatures of the tantalum / tantalum oxide / manganese dioxide system make solid tantalum capacitors an appropriate choice for today's surface mount assembly technology.

Vishay Sprague has been a pioneer and leader in this field, producing a large variety of tantalum capacitor types for consumer, industrial, automotive, military, and aerospace electronic applications.

Tantalum is not found in its pure state. Rather, it is commonly found in a number of oxide minerals, often in combination with Columbium ore. This combination is known as "tantalite" when its contents are more than one-half tantalum. Important sources of tantalite include Australia, Brazil, Canada, China, and several African countries. Synthetic tantalite concentrates produced from tin slags in Thailand, Malaysia, and Brazil are also a significant raw material for tantalum production.

Electronic applications, and particularly capacitors, consume the largest share of world tantalum production. Other important applications for tantalum include cutting tools (tantalum carbide), high temperature super alloys, chemical processing equipment, medical implants, and military ordnance.

Vishay Sprague is a major user of tantalum materials in the form of powder and wire for capacitor elements and rod and sheet for high temperature vacuum processing.

THE BASICS OF TANTALUM CAPACITORS

Most metals form crystalline oxides which are non-protecting, such as rust on iron or black oxide on copper. A few metals form dense, stable, tightly adhering, electrically insulating oxides. These are the so-called "valve" metals and include titanium, zirconium, niobium, tantalum, hafnium, and aluminum. Only a few of these permit the accurate control of oxide thickness by electrochemical means. Of these, the most valuable for the electronics industry are aluminum and tantalum.

Capacitors are basic to all kinds of electrical equipment, from radios and television sets to missile controls and automobile ignitions. Their function is to store an electrical charge for later use.

Capacitors consist of two conducting surfaces, usually metal plates, whose function is to conduct electricity. They are separated by an insulating material or dielectric. The dielectric used in all tantalum electrolytic capacitors is tantalum pentoxide.

Tantalum pentoxide compound possesses high-dielectric strength and a high-dielectric constant. As capacitors are being manufactured, a film of tantalum pentoxide is applied to their electrodes by means of an electrolytic process. The film is applied in various thicknesses and at various voltages and although transparent to begin with, it takes on different colors as light refracts through it. This coloring occurs on the tantalum electrodes of all types of tantalum capacitors.

Rating for rating, tantalum capacitors tend to have as much as three times better capacitance / volume efficiency than aluminum electrolytic capacitors. An approximation of the capacitance / volume efficiency of other types of capacitors may be inferred from the following table, which shows the dielectric constant ranges of the various materials used in each type. Note that tantalum pentoxide has a dielectric constant of 26, some three times greater than that of aluminum oxide. This, in addition to the fact that extremely thin films can be deposited during the electrolytic process mentioned earlier, makes the tantalum capacitor extremely efficient with respect to the number of microfarads available per unit volume. The capacitance of any capacitor is determined by the surface area of the two conducting plates, the distance between the plates, and the dielectric constant of the insulating material between the plates.

COMPARISON OF CAPACITOR DIELECTRIC CONSTANTS

	-
DIELECTRIC	e DIELECTRIC CONSTANT
Air or vacuum	1.0
Paper	2.0 to 6.0
Plastic	2.1 to 6.0
Mineral oil	2.2 to 2.3
Silicone oil	2.7 to 2.8
Quartz	3.8 to 4.4
Glass	4.8 to 8.0
Porcelain	5.1 to 5.9
Mica	5.4 to 8.7
Aluminum oxide	8.4
Tantalum pentoxide	26
Ceramic	12 to 400K

In the tantalum electrolytic capacitor, the distance between the plates is very small since it is only the thickness of the tantalum pentoxide film. As the dielectric constant of the tantalum pentoxide is high, the capacitance of a tantalum capacitor is high if the area of the plates is large:

$$C = \frac{eA}{t}$$

where

C = capacitance

e = dielectric constant

A = surface area of the dielectric

t = thickness of the dielectric

Tantalum capacitors contain either liquid or solid electrolytes. In solid electrolyte capacitors, a dry material (manganese dioxide) forms the cathode plate. A tantalum lead is embedded in or welded to the pellet, which is in turn connected to a termination or lead wire. The drawings show the construction details of the surface mount types of tantalum capacitors shown in this catalog.



SOLID ELECTROLYTE TANTALUM CAPACITORS

Solid electrolyte capacitors contain manganese dioxide, which is formed on the tantalum pentoxide dielectric layer by impregnating the pellet with a solution of manganous nitrate. The pellet is then heated in an oven, and the manganous nitrate is converted to manganese dioxide.

The pellet is next coated with graphite, followed by a layer of metallic silver, which provides a conductive surface between the pellet and the can in which it will be enclosed. After assembly, the capacitors are tested and inspected to assure long life and reliability. It offers excellent reliability and high stability for consumer and commercial electronics with the added feature of low cost.

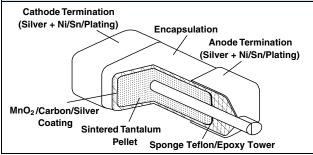
Surface mount designs of "Solid Tantalum" capacitors use lead frames or lead frameless designs as shown in the accompanying drawings.

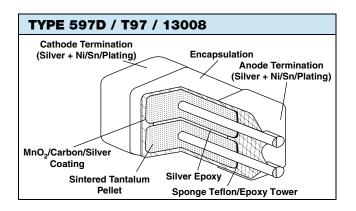
TANTALUM CAPACITORS FOR ALL DESIGN CONSIDERATIONS

Solid electrolyte designs are the least expensive for a given rating and are used in many applications where their very small size for a given unit of capacitance is of importance. They will typically withstand up to about 10 % of the rated DC working voltage in a reverse direction. Also important are their good low temperature performance characteristics and freedom from corrosive electrolytes.

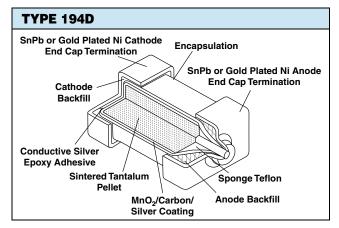
Vishay Sprague patented the original solid electrolyte capacitors and was the first to market them in 1956. Vishay Sprague has the broadest line of tantalum capacitors and has continued its position of leadership in this field. Data sheets covering the various types and styles of Vishay Sprague capacitors for consumer and entertainment electronics, industry, and military applications are available where detailed performance characteristics must be specified.

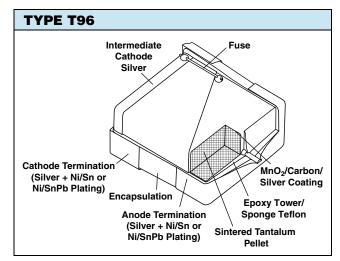


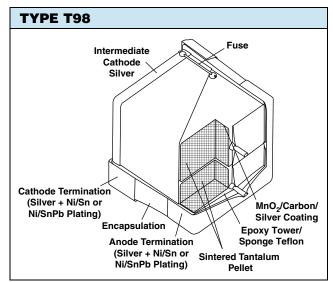




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COMMERCIAL PRODUCTS

SOLID TANTALUM CAPACITORS - CONFORMAL COATED									
SERIES	592W	592D	591D	595D	594D				
PRODUCT IMAGE			Ň						
TYPE		Surface mount	TANTAMOUNT [™] chip, co	nformal coated					
FEATURES	Low profile, robust design for use in pulsed applications	Low profile, maximum CV	Low profile, low ESR, maximum CV	Maximum CV	Low ESR, maximum CV				
TEMPERATURE RANGE	-55 °C to +125 °C (above 40 °C, voltage deratig is required)	-55 °C to +125 °C (above 85 °C, voltage derating is required)							
CAPACITANCE RANGE	330 μF to 2200 μF	1 μF to 2200 μF	1 μF to 1500 μF	0.1 μF to 1500 μF	1 μF to 1500 μF				
VOLTAGE RANGE	6 V to 10 V	4 V to 50 V	4 V to 50 V	4 V to 50 V	4 V to 50 V				
CAPACITANCE TOLERANCE	± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %				
LEAKAGE CURRENT		0.01 CV or 0.5 μA, whichever is greater							
DISSIPATION FACTOR	14 % to 45 %	4 % to 50 %	4 % to 50 %	4 % to 20 %	4 % to 20 %				
CASE CODES	C, M, X	S, A, B, C, D, R, M, X	A, B, C, D, R, M	T, S, A, B, C, D, G, M, R	B, C, D, R				
TERMINATION	100 % matte tin	100 %	matte tin standard, tin /	lead and gold plated av	vailable				

SOLID TANTAL	UM CAPACITOR	S - CONFORMAL	COATED					
SERIES	597D	572D	695D	195D	194D			
PRODUCT IMAGE								
TYPE		Τανταμ	OUNT [™] chip, conformal	coated				
FEATURES	Ultra low ESR, maximum CV, multi-anode	Low profile, maximum CV	Pad compatible with 194D and CWR06	US and European case sizes	Industrial version of CWR06 / CWR16			
TEMPERATURE RANGE		-55 °C to +125 °C (above 85 °C, voltage derating is required)						
CAPACITANCE RANGE	10 μF to 2200 μF	2.2 μF to 220 μF	0.1 μF to 270 μF	0.1 μF to 330 μF	0.1 μF to 330 μF			
VOLTAGE RANGE	4 V to 75 V	4 V to 35 V	4 V to 50 V	2 V to 50 V	4 V to 50 V			
CAPACITANCE TOLERANCE			± 10 %, ± 20 %					
LEAKAGE CURRENT		0.01 CV	′ or 0.5 μA, whichever is	greater				
DISSIPATION FACTOR	6 % to 20 %	6 % to 26 %	4 % to 8 %	4 % to 8 %	4 % to 10 %			
CASE CODES	V, D, E, R, F, Z, M, H	P, Q, S, A, B, T	A, B, D, E, F, G, H	C, S, V, X, Y, Z, R, A, B, D, E, F, G, H	A, B, C, D, E, F, G, H			
TERMINATION	100 % matte tin standard, tin / lead solder plated available	100 % matte tin standard, gold plated available	100 % matte tin / lead and gold	Gold plated standard; tin / lead solder plated and hot solder dipped available				

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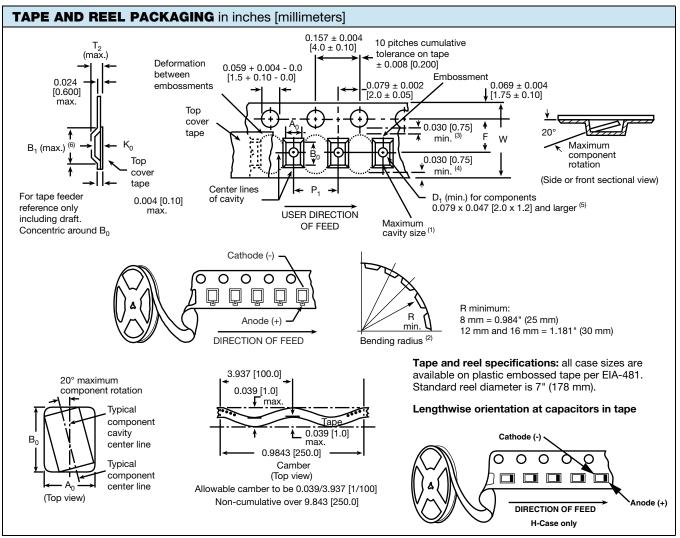


HIGH RELIABILITY PRODUCTS

SOLID TANTALUM CAPACITORS - CONFORMAL COATED								
SERIES	CWR06	CWR16	CWR26	13008	14002			
PRODUCT IMAGE					۲			
ТҮРЕ		ΤΑΝΤΑΜΟ)UNT™ chip, conforma	al coated				
FEATURES	MIL-PRF-55365/4 qualified	MIL-PRF-55365/13 qualified	MIL-PRF-55365/13 qualified	DLA approved				
TEMPERATURE RANGE		-55 °C to +125 °C (above 85 °C, voltage derating is required)						
CAPACITANCE RANGE	0.10 µF to 100 µF	0.33 µF to 330 µF	10 μF to 100 μF	10 μF to 1500 μF	4.7 μF to 680 μF			
VOLTAGE RANGE	4 V to 50 V	4 V to 35 V	15 V to 35 V	4 V to 63 V	4 V to 50 V			
CAPACITANCE TOLERANCE	± 5 %, ± 10 %, ± 20 %	± 5 %, ± 10 %, ± 20 %	± 5 %, ± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %			
LEAKAGE CURRENT	0.01 CV	or 1.0 µA, whichever	s greater	0.01 CV or 0.5 μA, ν	whichever is greater			
DISSIPATION FACTOR	6 % to 10 %	6 % to 10 %	6 % to 12 %	6 % to 20 % 6 % to 14 %				
CASE CODES	A, B, C, D, E, F, G, H	A, B, C, D, E, F, G, H	F, G, H	V, E, F, R, Z, D, M, H, N	B, C, D, R			
TERMINATION	Gold plated	l; tin / lead; tin / lead s	solder fused	Tin /	lead			

SOLID TANTALUM CA	PACITORS - CONFO	ORMAL COATED		
SERIES	T95	T96	T97	Т98
PRODUCT IMAGE				
ТҮРЕ		Tantamount™ chip, Hi-Re	el COTS, conformal coated	
FEATURES	High reliability	High reliability, built in fuse	High reliability, ultra low ESR, multi-anode	High reliability, ultra low ESR, built in fuse, multi-anode
TEMPERATURE RANGE	-55	°C to +125 °C (above 85 °	C, voltage derating is requir	red)
CAPACITANCE RANGE	0.15 μF to 680 μF	10 μF to 680 μF	10 μF to 2200 μF	10 μF to 1500 μF
VOLTAGE RANGE	4 V to 50 V	4 V to 50 V	4 V to 75 V	4 V to 75 V
CAPACITANCE TOLERANCE	± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %
LEAKAGE CURRENT		0.01 CV or 0.5 μA, γ	whichever is greater	
DISSIPATION FACTOR	4 % to 14 %	6 % to 14 %	6 % to 20 %	6 % to 10 %
CASE CODES	A, B, C, D, R, S, V, X, Y, Z	R	V, E, F, R, Z, D, M, H, N	V, E, F, R, Z, M, H
TERMINATION		100 % matte	tin, tin / lead	





Notes

- Metric dimensions will govern. Dimensions in inches are rounded and for reference only.
- (1) A₀, B₀, K₀, are determined by the maximum dimensions to the ends of the terminals extending from the component body and / or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity (A₀, B₀, K₀) must be within 0.002" (0.05 mm) minimum and 0.020" (0.50 mm) maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20°.
- ⁽²⁾ Tape with components shall pass around radius "R" without damage. The minimum trailer length may require additional length to provide "R" minimum for 12 mm embossed tape for reels with hub diameters approaching N minimum.
- ⁽³⁾ This dimension is the flat area from the edge of the sprocket hole to either outward deformation of the carrier tape between the embossed cavities or to the edge of the cavity whichever is less.
- (4) This dimension is the flat area from the edge of the carrier tape opposite the sprocket holes to either the outward deformation of the carrier tape between the embossed cavity or to the edge of the cavity whichever is less.
- ⁽⁵⁾ The embossed hole location shall be measured from the sprocket hole controlling the location of the embossement. Dimensions of embossement location shall be applied independent of each other.
- ⁽⁶⁾ B₁ dimension is a reference dimension tape feeder clearance only.



CARRIER T	APE DIMENSIO	NS in inches [mi	llimeters]			
TAPE WIDTH	W	D ₀	P ₂	F	E ₁	E _{2 min.}
8 mm	0.315 + 0.012 / - 0.004 [8.0 + 0.3 / - 0.1]		0.078 ± 0.0019	0.14 ± 0.0019 [3.5 ± 0.05]		0.246 [6.25]
12 mm	0.479 + 0.012 / - 0.004 [12.0 + 0.3 / - 0.1]	0.059 + 0.004 / - 0	[2.0 ± 0.05]	0.216 ± 0.0019 [5.5 ± 0.05]	0.324 ± 0.004	0.403 [10.25]
16 mm	0.635 + 0.012 / - 0.004 [16.0 + 0.3 / - 0.1]	[1.5 + 0.1 / - 0]	0.078 ± 0.004	0.295 ± 0.004 [7.5 ± 0.1]	[1.75 ± 0.1]	0.570 [14.25]
24 mm	$\begin{array}{c} 0.945 \pm 0.012 \\ [24.0 \pm 0.3] \end{array}$		[2.0 ± 0.1]	0.453 ± 0.004 [11.5 ± 0.1]		0.876 [22.25]

CARRIER T	APE DIMENSIONS in	inches [millimeters	5]		
ТҮРЕ	CASE CODE	TAPE WIDTH W IN mm	P ₁	K _{0 max.}	B _{1 max.}
	A	8	0.157 ± 0.004	0.058 [1.47]	0.149 [3.78]
	В	12	[4.0 ± 0.10]	0.088 [2.23]	0.166 [4.21]
	С	12		0.088 [2.23]	0.290 [7.36]
	D	12	0.315 ± 0.004	0.088 [2.23]	0.300 [7.62]
592D 592W	М	16	[8.0 ± 0.10]	0.091 [2.30]	0.311 [7.90]
591D	R	12		0.088 [2.23]	0.296 [7.52]
	S	8	0.157 ± 0.004	0.058 [1.47]	0.139 [3.53]
	Т	12	[4.0 ± 0.10]	0.088 [2.23]	0.166 [4.21]
	х	24	$\begin{array}{c} 0.472 \pm 0.004 \\ [12.0 \pm 0.10] \end{array}$	0.011 [2.72]	0.594 [15.1]
	A	8	0.157 ± 0.004	0.063 [1.60]	0.152 [3.86]
	В	12	$[4.0 \pm 0.10]$	0.088 [2.23]	0.166 [4.21]
	С	12		0.118 [2.97]	0.290 [7.36]
	D	12	0.315 ± 0.004	0.119 [3.02]	0.296 [7.52]
	G	12	[8.0 ± 0.10]	0.111 [2.83]	0.234 [5.95]
595D	Н	12		0.098 [2.50]	0.232 [5.90]
594D	М	12	0.157 ± 0.004 [4.0 ± 0.10]	0.085 [2.15]	0.152 [3.85]
	R	12	0.315 ± 0.004 [8.0 ± 0.10]	0.148 [3.78]	0.296 [7.52]
	S	8	0.157 ± 0.004	0.058 [1.47]	0.149 [3.78]
	Т	8	[4.0 ± 0.10]	0.054 [1.37]	0.093 [2.36]
	A	8		0.058 [1.47]	0.139 [3.53]
	В	12	0.157 ± 0.004	0.059 [1.50]	0.189 [4.80]
	D	12	[4.0 ± 0.10]	0.063 [1.62]	0.191 [4.85]
	E	12		0.074 [1.88]	0.239 [6.07]
695D	F	12	0.315 ± 0.004 [8.0 ± 0.10]	0.075 [1.93]	0.259 [6.58]
	G	12	0.157 ± 0.004 [4.0 ± 0.10]	0.109 [2.77]	0.301 [7.65]
	н	16	0.315 ± 0.004 [8.0 ± 0.10]	0.124 [3.15]	0.31 [7.87]

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CARRIER TA	PE DIMENSIONS in	inches [millimeters	\$]		
ТҮРЕ	CASE CODE	TAPE WIDTH W IN mm	P ₁	K _{0 max.}	B _{1 max.}
	A	8		0.058 [1.47]	0.139 [3.53]
	В	12	0.157 ± 0.004	0.059 [1.50]	0.189 [4.80]
	C D	8	$[4.0 \pm 0.10]$	0.054 [1.37] 0.067 [1.70]	0.093 [2.36]
	E	12	-	0.074 [1.88]	0.179 [4.55] 0.239 [6.07]
			0.315 ± 0.004		
	F	12	[8.0 ± 0.10]	0.076 [1.93]	0.259 [6.58]
195D	G	12	0.157 ± 0.004 [4.0 ± 0.10]	0.109 [2.77]	0.301 [7.65]
	H ⁽¹⁾	12	$\begin{array}{c} 0.472 \pm 0.004 \\ [12.0 \pm 0.1] \end{array}$	0.122 [3.11]	0.163 [4.14]
	R	12	0.315 ± 0.004 [8.0 ± 0.10]	0.149 [3.78]	0.296 [7.52]
	S	8		0.058 [1.47]	0.149 [3.78]
	V	8	0 1 5 7 . 0 00 4	0.060 [1.52]	0.150 [3.80]
	Х	12	0.157 ± 0.004 [4.0 ± 0.10]	0.069 [1.75]	0.296 [7.52]
	Y	12	[4.0 ± 0.10]	0.089 [2.26]	0.296 [7.52]
	Z	12		0.114 [2.89]	0.288 [7.31]
	A	8	-	0.058 [1.47]	0.149 [3.78]
	В	12		0.087 [2.20]	0.166 [4.21]
5700	Р	8	0.157 ± 0.004	0.043 [1.10]	0.102 [2.60]
572D	P	8	$[4.0 \pm 0.10]$	0.052 [1.32]	0.106 [2.70]
	Q S	8		0.054 [1.37] 0.058 [1.47]	0.140 [3.55] 0.149 [3.78]
	3 T	12		0.061 [1.55]	0.149 [3.78]
	A	8		0.069 [1.75]	0.139 [3.53]
	B	12	-	0.073 [1.85]	0.189 [4.80]
194D	C	12	0.157 ± 0.004	0.069 [1.75]	0.244 [6.20]
CWR06	D	12	[4.0 ± 0.10]	0.068 [1.72]	0.191 [4.85]
CWR16	E	12		0.074 [1.88]	0.239 [6.07]
CWR26	F	12	0.015 . 0.004	0.091 [2.31]	0.262 [6.65]
	G	16	0.315 ± 0.004 [8.0 ± 0.10]	0.134 [3.40]	0.289 [7.34]
	H	16		0.129 [3.28]	0.319 [8.10]
	D	16	0.317 ± 0.004	0.150 [3.80]	0.313 [7.95]
	E	16	[8.0 ± 0.10]	0.173 [4.40]	0.343 [8.70]
	F	16	-	0.205 [5.20]	0.309 [7.85]
507D	H	16	0.476 ± 0.004	0.224 [5.70]	0.313 [7.95]
597D T97	M N	16 16	[12.0 ± 0.1]	0.193 [4.90] 0.283 [7.20]	0.339 [8.60] 0.323 [8.20]
13008	R	16		0.159 [4.05]	0.313 [7.95]
	V	12	0.317 ± 0.004	0.088 [2.23]	0.300 [7.62]
	Z	16	$\frac{[8.0 \pm 0.10]}{0.476 \pm 0.004}$	0.239 [6.06]	0.311 [7.90]
	Z	8	[12.0 ± 0.1]	0.239 [0.06]	0.152 [3.86]
	B	12	0.157 ± 0.004	0.088 [2.23]	0.166 [4.21]
	C	12	[4.0 ± 0.10]	0.117 [2.97]	0.290 [7.36]
	D	12	0.317 ± 0.004	0.119 [3.02]	0.296 [7.52]
TOF	R	12	$[8.0 \pm 0.10]$	0.149 [3.78]	0.296 [7.52]
T95	S	8		0.058 [1.47]	0.149 [3.78]
	V	8	0.157 . 0.004	0.060 [1.52]	0.150 [3.80]
	Х	12	0.157 ± 0.004 [4.0 ± 0.10]	0.069 [1.75]	0.296 [7.52]
	Y	12	[7.0 ± 0.10]	0.089 [2.26]	0.296 [7.52]
	Z	12		0.114 [2.89]	0.288 [7.31]
	В	12	0.157 ± 0.004	0.088 [2.23]	0.166 [4.21]
14002	С	12	[4.0 ± 0.10]	0.117 [2.97]	0.290 [7.36]
	D	12	0.317 ± 0.004	0.119 [3.02]	0.296 [7.52]
	R	12	$[8.0 \pm 0.10]$	0.149 [3.78]	0.296 [7.52]
Т96	R	16	$\begin{array}{c} 0.476 \pm 0.004 \\ [12.0 \pm 0.1] \end{array}$	0.159 [4.05]	0.313 [7.95]
	F	16	0.476 ± 0.004	0.239 [6.06]	0.311 [7.90]
Т98	M	16	$[12.0 \pm 0.1]$	0.193 [4.90]	0.339 [8.60]
	Z	16		0.272 [6.90]	0.307 [7.80]

Note

⁽¹⁾ H case only, packaging code T: lengthwise orientation at capacitors in tape.

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PAD DIMENSIONS in inc	hes [millimeters]		
	A B ↓ C ↓ A B ↓	— A — — ►	
CASE CODE	WIDTH (A)	PAD METALLIZATION (B)	SEPARATION (C)
592D / W - 591D			
A	0.075 [1.9]	0.050 [1.3]	0.050 [1.3]
В	0.118 [3.0]	0.059 [1.5]	0.059 [1.5]
С	0.136 [3.5]	0.090 [2.3]	0.122 [3.1]
D	0.180 [4.6]	0.090 [2.3]	0.134 [3.4]
M		Anode pad: 0.095 [2.4]	0 100 [0 5]
М	0.256 [6.5]	Cathode pad: 0.067 [1.7]	0.138 [3.5]
P	0.040 [0.4]	Anode pad: 0.095 [2.4]	0 110 [0 0]
R	0.240 [6.1]	Cathode pad: 0.067 [1.7]	0.118 [3.0]
S	0.067 [1.7]	0.032 [0.8]	0.043 [1.1]
Х	0.310 [7.9]	0.120 [3.0]	0.360 [9.2]
595D - 594D			
Т	0.059 [1.5]	0.028 [0.7]	0.024 [0.6]
S	0.067 [1.7]	0.032 [0.8]	0.043 [1.1]
A	0.083 [2.1]	0.050 [1.3]	0.050 [1.3]
В	0.118 [3.0]	0.059 [1.5]	0.059 [1.5]
С	0.136 [3.5]	0.090 [2.3]	0.122 [3.1]
D	0.180 [4.6]	0.090 [2.3]	0.134 [3.4]
G	0.156 [4.05]	0.090 [2.3]	0.082 [2.1]
M	0.110 [2.8]	0.087 [2.2]	0.134 [3.4]
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]
195D	0.240 [0.0]	0.000 [2.0]	0.140 [0.0]
A	0.067 [1.7]	0.043 [1.1]	0.028 [0.7]
В	0.063 [1.6]	0.047 [1.2]	0.047 [1.2]
C	0.059 [1.5]	0.031 [0.8]	0.024 [0.6]
D	0.090 [2.3]	0.055 [1.4]	0.047 [1.2]
E	0.090 [2.3]	0.055 [1.4]	0.079 [2.0]
E	0.140 [3.6]	0.063 [1.6]	0.087 [2.2]
G	0.140 [3.0]	0.059 [1.5]	0.126 [3.2]
н	0.154 [3.9]	0.063 [1.6]	0.140 [3.6]
N	0.154 [3.9]	0.063 [1.6]	0.140 [3.6]
R			
	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]
S V	0.079 [2.0]	0.039 [1.0]	0.039 [1.0]
	0.114 [2.9]	0.039 [1.0]	0.039 [1.0]
X	0.118 [3.0]	0.067 [1.7]	0.122 [3.1]
Y	0.118 [3.0]	0.067 [1.7]	0.122 [3.1]
Z	0.118 [3.0]	0.067 [1.7]	0.122 [3.1]

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8 For technical questions, contact: <u>tantalum@vishay.com</u> Document Number: 40150

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PAD DIMENSIO	DNS in inches [mi	illimeters]



	¥	— A — – –	
CASE CODE	WIDTH (A)	PAD METALLIZATION (B)	SEPARATION (C)
CWR06 / CWR16 / CWR26 - 194	ID - 695D		
A	0.065 [1.6]	0.50 [1.3]	0.040 [1.0]
В	0.065 [1.6]	0.70 [1.8]	0.055 [1.4]
С	0.065 [1.6]	0.70 [1.8]	0.120 [3.0]
D	0.115 [2.9]	0.70 [1.8]	0.070 [1.8]
E	0.115 [2.9]	0.70 [1.8]	0.120 [3.0]
F	0.150 [3.8]	0.70 [1.8]	0.140 [3.6]
G	0.125 [3.2]	0.70 [1.8]	0.170 [4.3]
Н	0.165 [4.2]	0.90 [2.3]	0.170 [4.3]
T95			
В	0.120 [3.0]	0.059 [1.5]	0.059 [1.5]
С	0.136 [3.5]	0.090 [2.3]	0.120 [3.1]
D	0.180 [4.6]	0.090 [2.3]	0.136 [3.47]
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]
S	0.080 [2.03]	0.040 [1.02]	0.040 [1.02]
V	0.114 [2.9]	0.040 [1.02]	0.040 [1.02]
X, Y, Z	0.114 [2.9]	0.065 [1.65]	0.122 [3.1]
14002			
В	0.120 [3.0]	0.059 [1.5]	0.059 [1.5]
С	0.136 [3.5]	0.090 [2.3]	0.120 [3.1]
D	0.180 [4.6]	0.090 [2.3]	0.136 [3.47]
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]
T96			
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]
597D - T97 - T98 - 13008		<u> </u>	
D, E, V	0.196 [4.9]	0.090 [2.3]	0.140 [3.6]
F, R, Z	0.260 [6.6]	0.090 [2.3]	0.140 [3.6]
M, H, N	0.284 [7.2]	0.090 [2.3]	0.140 [3.6]

PAD DIMENSION	S in inches [millime	eters]		
		A B ↓ A C ↓ A B ₁ ↓ A B ₁		
CASE CODE	WIDTH (A)	PAD METALLIZATION (B)	PAD METALLIZATION (B1)	SEPARATION (C)
572D			· · · ·	
А	0.079 [2.0]	0.039 [1.0]	0.035 [0.9]	0.047 [1.2]
Q	0.079 [2.0]	0.039 [1.0]	0.035 [0.9]	0.047 [1.2]
S	0.079 [2.0]	0.039 [1.0]	0.035 [0.9]	0.047 [1.2]
В	0.110 [2.8]	0.039 [1.0]	0.035 [0.9]	0.055 [1.4]
		0.024 [0.6]	0.024 [0.6]	0.035 [0.9]
Р	0.055 [1.4]	0.02 + [0.0]		

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RECOMMENDED REFLOW PRO	FILES	
Capacitors should withstand reflow profile as	s per J-STD-020 standard, three cycles.	
ATURE (°C	Max. ramp-up rate = 3 °C/s Max. ramp-down rate = 6 °C/s max. Preheat area Time 25 °C to peak TIME (s)	T T _c -5°C
PROFILE FEATURE	SnPb EUTECTIC ASSEMBLY	LEAD (Pb)-FREE ASSEMBLY
	SnPb EUTECTIC ASSEMBLY	LEAD (Pb)-FREE ASSEMBLY
Preheat / soak	SnPb EUTECTIC ASSEMBLY	LEAD (Pb)-FREE ASSEMBLY
Preheat / soak Temperature min. (T _{s min} .)		
Preheat / soak Temperature min. (T _{s min.}) Temperature max. (T _{s max.})	100 °C	150 °C
Preheat / soak Temperature min. (T _{s min.}) Temperature max. (T _{s max.}) Time (t _s) from (T _{s min.} to T _{s max.})	100 °C 150 °C	150 °C 200 °C
Preheat / soak Temperature min. (T _{s min} .) Temperature max. (T _{s max} .) Time (t _s) from (T _{s min} . to T _{s max} .) Ramp-up	100 °C 150 °C	150 °C 200 °C
Preheat / soak Temperature min. (T _{s min} .) Temperature max. (T _{s max} .) Time (t _s) from (T _{s min} . to T _{s max} .) Ramp-up Ramp-up rate (T _L to T _p)	100 ℃ 150 ℃ 60 s to 120 s	150 °C 200 °C 60 s to 120 s
Preheat / soak Temperature min. $(T_{s min.})$ Temperature max. $(T_{s max.})$ Time (t_s) from $(T_{s min.} \text{ to } T_{s max.})$ Ramp-up Ramp-up rate $(T_L \text{ to } T_p)$ Liquidus temperature (T_L)	100 °C 150 °C 60 s to 120 s 3 °C/s max.	150 °C 200 °C 60 s to 120 s 3 °C/s max.
Preheat / soak Temperature min. $(T_{s min.})$ Temperature max. $(T_{s max.})$ Time (t_s) from $(T_{s min.} \text{ to } T_{s max.})$ Ramp-up Ramp-up rate $(T_L \text{ to } T_p)$ Liquidus temperature (T_L) Time (t_L) maintained above T_L	100 °C 150 °C 60 s to 120 s 3 °C/s max. 183 °C 60 s to 150 s	150 °C 200 °C 60 s to 120 s 3 °C/s max. 217 °C
Preheat / soak Temperature min. $(T_{s min.})$ Temperature max. $(T_{s max.})$ Time (t_s) from $(T_{s min.} \text{ to } T_{s max.})$ Ramp-up Ramp-up rate $(T_L \text{ to } T_p)$ Liquidus temperature (T_L) Time (t_L) maintained above T_L Peak package body temperature (T_p)	100 °C 150 °C 60 s to 120 s 3 °C/s max. 183 °C 60 s to 150 s	150 °C 200 °C 60 s to 120 s 3 °C/s max. 217 °C 60 s to 150 s
Preheat / soak Temperature min. $(T_{s min.})$ Temperature max. $(T_{s max.})$ Time (t_s) from $(T_{s min.}$ to $T_{s max.})$ Ramp-up Ramp-up rate $(T_L \text{ to } T_p)$ Liquidus temperature (T_L) Time (t_L) maintained above T_L Peak package body temperature (T_p) Time $(t_p)^*$ within 5 °C of the specified classification temperature (T_c)	100 °C 150 °C 60 s to 120 s 3 °C/s max. 183 °C 60 s to 150 s Depends on type and	150 °C 200 °C 60 s to 120 s 3 °C/s max. 217 °C 60 s to 150 s d case – see table below
PROFILE FEATURE Preheat / soak Temperature min. $(T_{s min.})$ Temperature max. $(T_{s max.})$ Time (t_s) from $(T_{s min.}$ to $T_{s max.})$ Ramp-up Ramp-up rate $(T_L \text{ to } T_p)$ Liquidus temperature (T_L) Time (t_1) maintained above T_L Peak package body temperature (T_p) Time $(t_p)^*$ within 5 °C of the specified classification temperature (T_c) Ramp-down Ramp-down rate $(T_p \text{ to } T_L)$	100 °C 150 °C 60 s to 120 s 3 °C/s max. 183 °C 60 s to 150 s Depends on type and	150 °C 200 °C 60 s to 120 s 3 °C/s max. 217 °C 60 s to 150 s d case – see table below

PEAK PACKAGE BODY TEMPERATURE (T _p)			
	PEAK PACKAGE BODY TEMPERATURE (Tp)		
TYPE / CASE CODE	SnPb EUTECTIC PROCESS	LEAD (Pb)-FREE PROCESS	
591D / 592D - all cases, except X25H, M and R cases	235 °C	260 °C	
591D / 592D - X25H, M and R cases	220 °C	250 °C	
594D / 595D - all cases except C, D, and R	235 °C	260 °C	
594D / 595D - C, D, and R case	220 °C	250 °C	
572D all cases	n/a	260 °C	
T95 A, B, S, V, X, Y cases	235 °C	260 °C	
T95 C, D, R, and Z cases	220 °C	250 °C	
14002 B case	235 °C	n/a	
14002 C, D, and R cases	220 °C	n/a	
T96 R case	220 °C	250 °C	
195D all cases, except G, H, R, and Z	235 °C	260 °C	
195D G, H, R, and Z cases	220 °C	250 °C	
695D all cases, except G and H cases	235 °C	260 °C	
695D G, H cases	220 °C	250 °C	
597D, T97, T98 all cases, except V case	220 °C	250 °C	
597D, T97, T98 V case	235 °C	260 °C	
194D all cases, except H and G cases	235 °C	260 °C	
194D H and G cases	220 °C	250 °C	

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GUIDE TO APPLICATION

1. **AC Ripple Current:** the maximum allowable ripple current shall be determined from the formula:

$$I_{RMS} = \sqrt{\frac{P}{R_{ESR}}}$$

where,

- P = power dissipation in W at +25 °C as given in the tables in the product datasheets (Power Dissipation).
- R_{ESR} = the capacitor equivalent series resistance at the specified frequency
- 2. **AC Ripple Voltage:** the maximum allowable ripple voltage shall be determined from the formula:

$$V_{RMS} = I_{RMS} \times Z$$

or, from the formula:

$$V_{RMS} = Z_{\sqrt{\frac{P}{R_{ESR}}}}$$

where,

- P = power dissipation in W at +25 °C as given in the tables in the product datasheets (Power Dissipation).
- R_{ESR} = the capacitor equivalent series resistance at the specified frequency
- Z = the capacitor impedance at the specified frequency
- 2.1 The sum of the peak AC voltage plus the applied DC voltage shall not exceed the DC voltage rating of the capacitor.
- 2.2 The sum of the negative peak AC voltage plus the applied DC voltage shall not allow a voltage reversal exceeding 10 % of the DC working voltage at +25 °C.
- Reverse Voltage: solid tantalum capacitors are not intended for use with reverse voltage applied. However, they have been shown to be capable of withstanding momentary reverse voltage peaks of up to 10 % of the DC rating at 25 °C and 5 % of the DC rating at +85 °C.
- 4. **Temperature Derating:** if these capacitors are to be operated at temperatures above +25 °C, the permissible RMS ripple current shall be calculated using the derating factors as shown:

TEMPERATURE	DERATING FACTOR
+25 °C	1.0
+85 °C	0.9
+125 °C	0.4

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5. **Power Dissipation:** power dissipation will be affected by the heat sinking capability of the mounting surface. Non-sinusoidal ripple current may produce heating effects which differ from those shown. It is important that the equivalent I_{RMS} value be established when calculating permissible operating levels. (Power dissipation calculated using derating factor (see paragraph 4)).

6. Attachment:

- 6.1 **Soldering:** capacitors can be attached by conventional soldering techniques, convection, infrared reflow, wave soldering and hot plate methods. The soldering profile chart shows typical recommended time / temperature conditions for soldering. Preheating is recommended to reduce thermal stress. The recommended maximum preheat rate is 2 °C/s. Attachment with a soldering iron is not recommended due to the difficulty of controlling temperature and time at temperature. The soldering iron must never come in contact with the capacitor.
- 7. **Recommended Mounting Pad Geometries:** the nib must have sufficient clearance to avoid electrical contact with other components. The width dimension indicated is the same as the maximum width of the capacitor. This is to minimize lateral movement.
- 8. Cleaning (Flux Removal) After Soldering: TANTAMOUNT[™] capacitors are compatible with all commonly used solvents such as TES, TMS, Prelete, Chlorethane, Terpene and aqueous cleaning media. However, CFC / ODS products are not used in the production of these devices and are not recommended. Solvents containing methylene chloride or other epoxy solvents should be avoided since these will attack the epoxy encapsulation material.



COTS Tantalum Capacitors

ITEM	PERFORMANCE CHAR	ACTERISTICS			
Category temperature range	-55 °C to +85 °C (to +12	5 °C with voltage derating)			
Capacitance tolerance	± 20 %, ± 10 %, tested v	via bridge method, at 25 °C	c, 120 Hz		
Dissipation factor	Limit per Standard Rating	gs table. Tested via bridge	method, at 25 °C, 120 Hz		
ESR	Limit per Standard Rating	gs table. Tested via bridge	method, at 25 °C, 100 kHz		
Leakage current	resistor in series with the 0.5 µA, whichever is grea	e capacitor under test, lea	ors for 5 min using a steady so kage current at 25 °C is no current varies with temperat tor.	ot more than 0.01 CV o	
Capacitance change by temperature	+15 % max. (at +125 °C) +10 % max. (at +85 °C) -10 % max. (at -55 °C)				
Reverse voltage	10 % of the DC rating at 5 % of the DC rating at + 1 % of the DC rating at +	+25 °C 85 °C 125 °C	es in the reverse direction e e application of reverse volta		
Ripple current	temperatures above +25 derating factors: 1.0 at +25 °C 0.9 at +85 °C 0.4 at +125 °C	°C, the permissible RMS ri	to relevant datasheet. If cap pple current (or voltage) shal	II be calculated using the	
Maximum operating and surge	+85	5 °C	+125	5 °C	
voltages vs. temperature	RATED VOLTAGE	SURGE VOLTAGE	CATEGORY VOLTAGE	SURGE VOLTAGE	
	(V)	(V)	(V)	(V)	
	(V) 4.0				
		(V)	(V)	(V)	
	4.0	(V) 5.2	(V) 2.7	(V) 3.4	
	4.0 6.3	(V) 5.2 8.0	(V) 2.7 4.0	(V) 3.4 5.0	
	4.0 6.3 10	(V) 5.2 8.0 13	(V) 2.7 4.0 7.0	(V) 3.4 5.0 8.0	
	4.0 6.3 10 16	(V) 5.2 8.0 13 20	(V) 2.7 4.0 7.0 10	(V) 3.4 5.0 8.0 12	
	4.0 6.3 10 16 20	(V) 5.2 8.0 13 20 26	(V) 2.7 4.0 7.0 10 13	(V) 3.4 5.0 8.0 12 16	
	4.0 6.3 10 16 20 25	(V) 5.2 8.0 13 20 26 32	(V) 2.7 4.0 7.0 10 13 17	(V) 3.4 5.0 8.0 12 16 20	
	4.0 6.3 10 16 20 25 35	(V) 5.2 8.0 13 20 26 32 46	(V) 2.7 4.0 7.0 10 13 17 23	(V) 3.4 5.0 8.0 12 16 20 28	
	4.0 6.3 10 16 20 25 35 40	(M) 5.2 8.0 13 20 26 32 46 52	(V) 2.7 4.0 7.0 10 13 17 23 26	(V) 3.4 5.0 8.0 12 16 20 28 31	
	4.0 6.3 10 16 20 25 35 40 50	(M) 5.2 8.0 13 20 26 32 46 52 65	(V) 2.7 4.0 7.0 10 13 17 23 26 33	(V) 3.4 5.0 8.0 12 16 20 28 31 40	
	4.0 6.3 10 16 20 25 35 40 50 50 ⁽¹⁾	(M) 5.2 8.0 13 20 26 32 46 52 65 60	(V) 2.7 4.0 7.0 10 13 17 23 26 33 33	(V) 3.4 5.0 8.0 12 16 20 28 31 40 40	
Recommended voltage	4.0 6.3 10 16 20 25 35 40 50 50 ⁽¹⁾ 63	(V) 5.2 8.0 13 20 26 32 46 52 65 60 75 75	(V) 2.7 4.0 7.0 10 13 17 23 26 33 33 42	(V) 3.4 5.0 8.0 12 16 20 28 31 40 50 50 50	
derating guidelines	4.0 6.3 10 16 20 25 35 40 50 50 ⁽¹⁾ 63 75 VOLTAC	(V) 5.2 8.0 13 20 26 32 46 52 65 60 75 75	(V) 2.7 4.0 7.0 10 13 17 23 26 33 33 42 50	(V) 3.4 5.0 8.0 12 16 20 28 31 40 40 50 50 LTAGE RATING	
derating guidelines	4.0 6.3 10 16 20 25 35 40 50 50 (¹) 63 75 VOLTAC ≤ 3	(V) 5.2 8.0 13 20 26 32 46 52 65 65 60 75 75 75 75 GE RAIL	(V) 2.7 4.0 7.0 10 13 17 23 26 33 42 50 CAPACITOR VO	(V) 3.4 5.0 8.0 12 16 20 28 31 40 50 50 50 50 3	
derating guidelines	4.0 6.3 10 16 20 25 35 40 50 50 (¹) 63 75 VOLTAC ≤ 3	(V) 5.2 8.0 13 20 26 32 46 52 65 60 75 75 3.3 5	(V) 2.7 4.0 7.0 10 13 17 23 26 33 42 50 CAPACITOR VO 6.	(V) 3.4 5.0 8.0 12 16 20 28 31 40 50 50 50 50 110 112 110 200 210	
Recommended voltage derating guidelines (below 85 °C)	4.0 6.3 10 16 20 25 35 40 50 50 ⁽¹⁾ 63 75 VOLTAC ≤ 3 50	(V) 5.2 8.0 13 20 26 32 46 52 65 60 75 75 3.3 5	(V) 2.7 4.0 7.0 10 13 17 23 26 33 33 42 50 CAPACITOR VOI 6.3 10	(V) 3.4 5.0 8.0 12 16 20 28 31 40 50 50 50 50 23 0 0	
derating guidelines	4.0 6.3 10 16 20 25 35 40 50 50 ⁽¹⁾ 63 75 VOLTAC ≤ 3 50	(V) 5.2 8.0 13 20 26 32 46 52 65 60 75 75 3.3 5 0 2	(V) 2.7 4.0 7.0 10 13 17 23 26 33 33 42 50 CAPACITOR VO 6. 10	(V) 3.4 5.0 8.0 12 16 20 28 31 40 50 50	

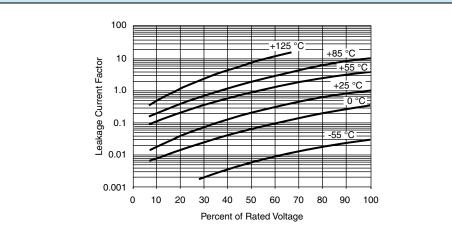
Notes

• All information presented in this document reflects typical performance characteristics

⁽¹⁾ Capacitance value 15 μ F and higher







Notes

- At +25 °C, the leakage current shall not exceed the value listed in the Standard Ratings table
- At +85 °C, the leakage current shall not exceed 10 times the value listed in the Standard Ratings table
- At +125 °C, the leakage current shall not exceed 12 times the value listed in the Standard Ratings table

ITEM	$\begin{tabular}{ c c c c } \hline CONDITION \\ \hline MIL-PRF-55365 \\ 1000 successive test cycles at 85 °C of surge voltage (as specified in the table above), in series with a 33 \Omega resistor at the rate of 30 s ON, 30 s OFF \\ \hline \end{tabular}$	POST TEST PERFORMANCE	
Surge voltage		Capacitance change Dissipation factor Leakage current	Within ± 10 % of initial value Initial specified limit Initial specified limit
Life test at +85 °C	MIL-STD-202, method 108 1000 h application of rated voltage at 85 °C	Capacitance change Dissipation factor Leakage current	Within ± 10 % of initial value Initial specified limit Shall not exceed 125 % of initial limit
Life test at +125 °C	MIL-STD-202, method 108 1000 h application 2/3 of rated voltage at 125 °C	Capacitance change Dissipation factor Leakage current	Within ± 10 % of initial value Initial specified limit Shall not exceed 125 % of initial limit
Moisture resistance	MIL-STD-202, method 106 at rated voltage, 20 cycles	Capacitance change Dissipation factor Leakage current	Within ± 15 % of initial value Shall not exceed 150 % of initial limit Shall not exceed 200 % of initial limit
Stability at low and high temperatures	MIL-PRF-55365	Delta cap limit at -55 °C, 85 °C is \pm 10 % of initial value Delta cap limit at 125 °C is \pm 15 % of initial value Delta cap at step 3 and final step 25 °C is \pm 10 % DCL at 85 °C: 10 x initial specified value DCL at 125 °C: 12 x initial specified value DCL at 25 °C: initial specified value	
Thermal shock	MIL-STD-202, method 107 At -55 °C / +125 °C, for 5 cycles, 30 min at each temperature	Capacitance change Dissipation factor Leakage current	Within ± 10 % of initial value Initial specified limit Initial specified limit



MECHANICAL PERFORMANCE CHARACTERISTICS				
ITEM	CONDITION	POST TEST PERFORMANCE		
Terminal strength / Shear force test	Apply a pressure load of 5 N for 10 s \pm 1 s horizontally to the center of capacitor side body	Capacitance changeWithin ± 10 % of initial valueDissipation factorInitial specified limitLeakage currentInitial specified limit		
		There shall be no mechanical or visual damage to capacitors post-conditioning.		
Vibration	MIL-STD-202, method 204, condition D, 10 Hz to 2000 Hz, 20 <i>g</i> peak, 8 h, at rated voltage	Electrical measurements are not applicable, since the same parts are used for shock (specified pulse) test.		
		There shall be no mechanical or visual damage to capacitors post-conditioning.		
Shock (specified pulse)	MIL-STD-202, method 213, condition I, 100 g peak	Capacitance changeWithin ± 10 % of initial valueDissipation factorInitial specified limitLeakage currentInitial specified limit		
		There shall be no mechanical or visual damage to capacitors post-conditioning.		
Resistance to soldering heat	MIL-STD-202, method 210, condition J (leadbearing capacitors) and K (lead (Pb)-free capacitors), one heat cycle	Capacitance changeWithin ± 10 % of initial valueDissipation factorInitial specified limitLeakage currentInitial specified limit		
Solderability	MIL-STD-202, method 208, ANSI/J-STD-002, test B (leadbearing) and B1 (lead (Pb)-free). Preconditioning per category C (category E - optional).	Solder coating of all capacitors shall meet specified requirements.		
	Does not apply to gold terminations. Lead (Pb)-free and leadbearing capacitors are backward and forward compatible	There shall be no mechanical or visual damage to capacitors post-conditioning.		
Resistance to solvents	MIL-STD-202, method 215	There shall be no mechanical or visual damage to capacitors post-conditioning. Body marking shall remain legible.		
Flammability	Encapsulation materials meet UL 94 V-0 with an oxygen index of 32 %			



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