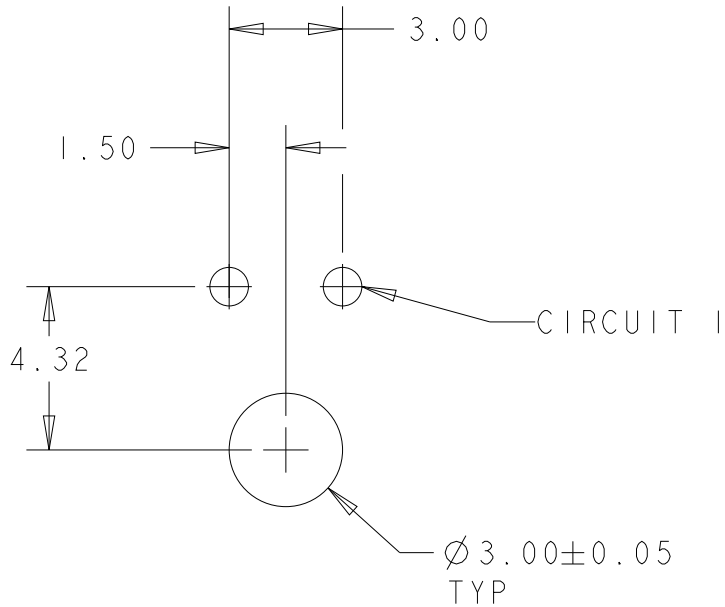


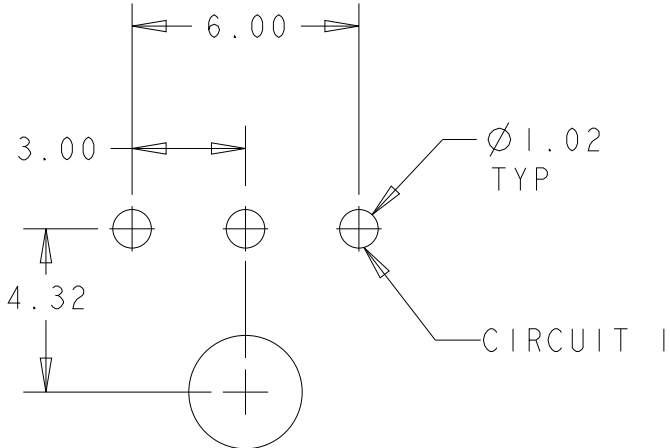
LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
CM	00		-	SEE SHEET 1	-	-	-

D

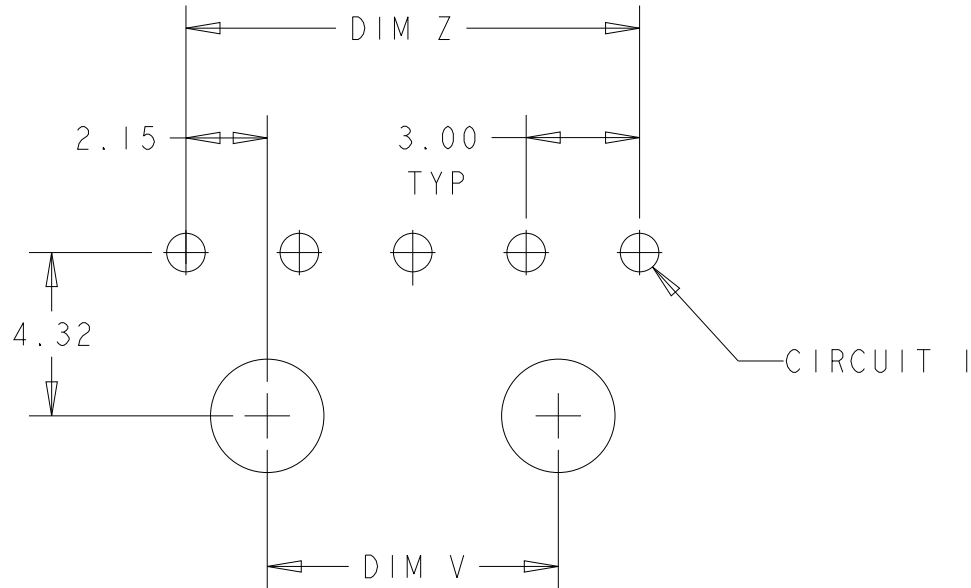
C



PCB LAYOUT FOR 2 POSITION 
COMPONENT SIDE



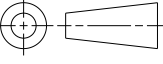
PCB LAYOUT FOR 3 POSITION 
COMPONENT SIDE



PCB LAYOUT FOR 4-12 POSITION 
COMPONENT SIDE

B

A

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN J. ROOT 09OCT2000	 TE Connectivity			
DIMENSIONS:		CHK K. WHITAKER 17-APR-2001				
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD C. JONES 17-APR-2001	NAME RIGHT ANGLE THRU HOLE HEADER ASSY, 0.38 MIC GOLD CONTACTS, WITH PLASTIC HOLDDOWNS, SGL ROW, MICRO MATE-N-LOK™			
mm		PRODUCT SPEC	SIZE		CAGE CODE	DRAWING NO
		APPLICATION SPEC	A2		00779	C-1445089
MATERIAL		WEIGHT	SCALE		SHEET	OF
		CUSTOMER DRAWING	5:1		2	2
SEE TABLE -			REV		A2	

Mouser Electronics

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