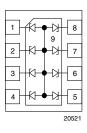


MINIMUM ORDER QUANTITY

260 °C/10 s at terminals

# 8-Line ESD Protection Diode Array in LLP1713-9L





#### **MARKING** (example only)



Dot = pin 1 marking Y = type code (see table below) XX = date code

**ORDERING INFORMATION** 

#### **DESIGN SUPPORT TOOLS** click logo to get started

LLP1713-9L

В



**DEVICE NAME** 

VESD05A8A-HNH

#### **FEATURES**

- Ultra compact LLP1713-9L package
- Low package profile < 0.6 mm</li>
- 8-line ESD protection

**TAPED UNITS PER REEL** 

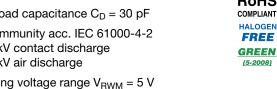
(8 mm TAPE ON 7" REEL)

- Low leakage current I<sub>R</sub> < 1 μA</li>
- Low load capacitance C<sub>D</sub> = 30 pF
- ESD immunity acc. IEC 61000-4-2 ± 25 kV contact discharge ± 30 kV air discharge
- Working voltage range V<sub>RWM</sub> = 5 V
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)

MSL level 1 (according J-STD-020)

• Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





VESD05A8A-HNH	VESD05A8A-HNH-GS08		S08	3000		15 000		
PACKAGE DATA								
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT		NG COMPOUND ABILITY RATING	MOISTUR SENSITIVITY I		SOLDERING CONDITIONS

UL 94 V-0

**ORDERING CODE** 

3.7 mg

RATING	NG TEST CONDITIONS		VALUE	UNIT
Peak pulse current	BiAs-mode: each input (pin 1 to pin 8) to ground (pin 9) acc. IEC 61000-4-5; t <sub>P</sub> = 8/20 μs; single shot	I <sub>PPM</sub>	5	Α
	BiSy-mode: each input (pin 1 to pin 8) to any other input pin. Pin 9 not connected. Acc. IEC 61000-4-5; $t_P=8/20~\mu s/single$ shot	I <sub>PPM</sub>	2.5	Α
Peak pulse power	BiAs-mode: each input (pin 1 to pin 8) to ground (pin 9) acc. IEC 61000-4-5; $t_P=8/20~\mu s$ ; single shot	P <sub>PP</sub>	65	W
	BiSy-mode: each input (pin 1 to pin 8) to any other input pin Pin 9 not connected. Acc. IEC 61000-4-5; $t_P=8/20~\mu s/single$ shot	P <sub>PP</sub>	33	W
	Contact discharge acc. IEC 61000-4-2; 10 pulses BiAs-mode: each input (pin 1 to pin 8) to ground (pin 9)	V <sub>ESD</sub>	± 25	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses BiAs-mode: each input (pin 1 to pin 8) to ground (pin 9)	V <sub>ESD</sub>	± 30	kV
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses BiSy-mode: each input (pin 1 to pin 8) to any other input pin. Pin 9 not connected.	V <sub>ESD</sub>	± 12	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses BiSy-mode: each input (pin 1 to pin 8) to any other input pin. Pin 9 not connected.	V <sub>ESD</sub>	± 12	kV
Operating temperature	Junction temperature	$T_J$	-40 to +125	°C
Storage temperature		T <sub>STG</sub>	-55 to +150	°C



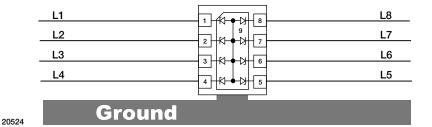
### **BiAs-MODE** (8-line bidirectional asymmetrical protection mode)

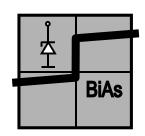
With the VESD05A8A-HNH up to 8 signal- or data-lines (L1 to L8) can be protected against voltage transients. With pin 9 connected to ground and pin 1 up to pin 8 connected to a signal- or data-line which has to be protected. As long as the voltage level on the data- or signal-line is between 0 V (ground level) and the specified maximum reverse working voltage (V<sub>RWM</sub>) the protection diode between data line and ground offer a high isolation to the ground line. The protection device behaves like an open switch.

As soon as any positive transient voltage signal exceeds the break through voltage level of the protection diode, the diode becomes conductive and shorts the transient current to ground. Now the protection device behaves like a closed switch. The clamping voltage (V<sub>C</sub>) is defined by the breakthrough voltage (V<sub>BR</sub>) level plus the voltage drop at the series impedance (resistance and inductance) of the protection device.

Any negative transient signal will be clamped accordingly. The negative transient current is flowing in the forward direction of the protection diode. The low forward voltage  $(V_F)$  clamps the negative transient close to the ground level.

Due to the different clamping levels in forward and reverse direction the VESD05A8A-HNH clamping behaviour is bidirectional and asymmetrical (BiAs).



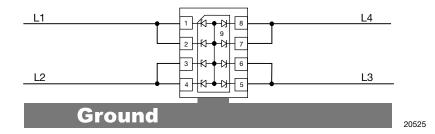


<b>ELECTRICAL CHARACTERISTICS VESD05A8A-HNH</b> (Between pin 1 to 8 and pin 9) (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	8	lines	
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	5	V	
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	5	-	-	V	
Reverse current	at V <sub>R</sub> = V <sub>RWM</sub> = 5 V	I <sub>R</sub>	-	< 0.1	1	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6		8	V	
Reverse clamping voltage	at I <sub>PP</sub> = 5 A acc. IEC 61000-4-5	V <sub>C</sub>	-		13	V	
Forward clamping voltage	at I <sub>F</sub> = 5 A acc. IEC 61000-4-5	$V_{F}$	-		4.5	V	
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	$C_D$	-	30	35	pF	
	at V <sub>B</sub> = 2.5 V; f = 1 MHz	C <sub>D</sub>	_	18	23	pF	



If a higher surge current or peak pulse current (I<sub>PP</sub>) is needed, some protection diodes in the VESD05A8A-HNH can also be used in parallel in order to "multiply" the performance. If two diodes are switched in parallel you get

- double surge power = double peak pulse current (2 x I<sub>PPM</sub>)
- half of the line inductance = reduced clamping voltage
- half of the line resistance = reduced clamping voltage
- double line capacitance (2 x C<sub>D</sub>)
- double reverse leakage current (2 x I<sub>R</sub>)

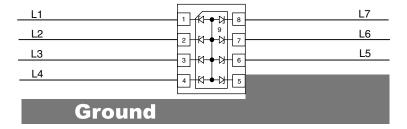


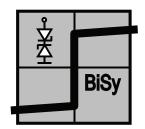
#### **BiSy-MODE** (7-line bidirectional symmetrical protection mode)

If a bipolar symmetrical protection device is needed the VESD05A8A-HNH can also be used as a seven-line protection device. Therefore seven pins (example: pin 1, 2, 3, 4, 6, 7 and 8) has to be connected to the signal or data-line (L1 to L7) and pin 5 to ground. Pin 9 must not be connected!

Positive and negative voltage transients will be clamped in the same way. The clamping current from one data line through the VESD05A8A-HNH to the ground passes one diode in forward direction and the other one in reverse direction. The clamping voltage (V<sub>C</sub>) is defined by the breakthrough voltage (V<sub>BR</sub>) level of one diode plus the forward voltage of the other diode plus the voltage drop at the series impedances (resistances and inductances) of the protection device.

Due to the same clamping levels in positive and negative direction the VESD05A8A-HNH voltage clamping behaviour is also bidirectional and symmetrical (BiSy).





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<b>ELECTRICAL CHARACTERISTICS</b> (Between any of the pins 1 to 8; pin 9 not connected) $(T_{amb} = 25  ^{\circ}C$ , unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	7	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	ı	-	5.5	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	5.5	-	-	V		
Reverse current	at $V_R = V_{RWM} = 5.5 \text{ V}$	I <sub>R</sub>	-	< 0.1	1	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6.5		8.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = 2.5 A acc. IEC 61000-4-5	V <sub>C</sub>	ı		13	V		
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	C <sub>D</sub>	- 1	30	35	pF		
	at $V_R = 2.5 \text{ V}$ ; $f = 1 \text{ MHz}$	C <sub>D</sub>	ı	18	23	pF		

### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

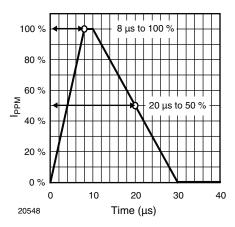


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$ /150 pF)

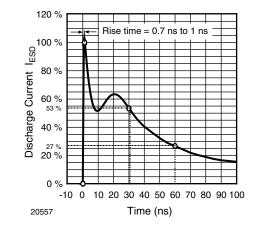


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

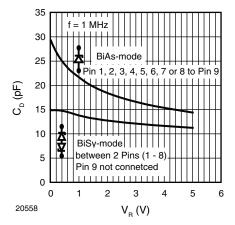


Fig. 3 - Typical Capacitance C<sub>D</sub> vs. Reverse Voltage V<sub>R</sub>

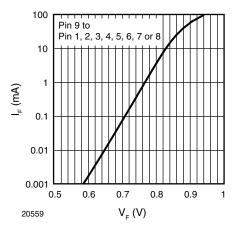


Fig. 4 - Typical Forward Current I<sub>F</sub> vs. Forward Voltage V<sub>F</sub>

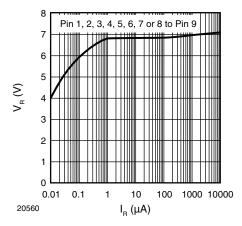


Fig. 5 - Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

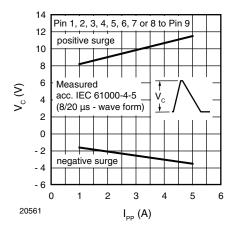


Fig. 6 - Typical Peak Clamping Voltage  $V_C$  vs. Peak Pulse Current  $I_{PP}$ 



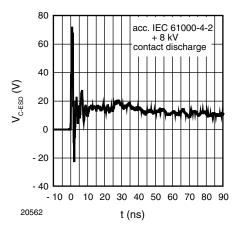


Fig. 7 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

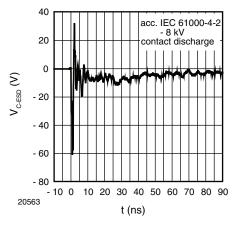


Fig. 8 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

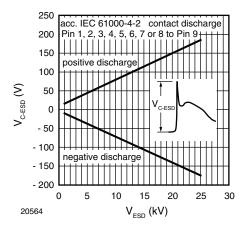
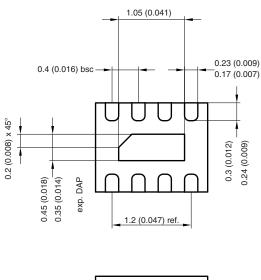
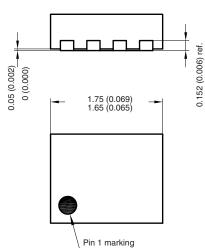
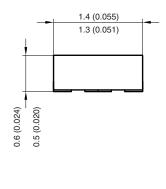


Fig. 9 - Typical Peak Clamping Voltage at ± ESD Contact Discharge (acc. IEC 61000-4-2)

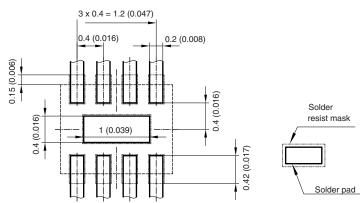
### PACKAGE DIMENSIONS in millimeters (Inches): LLP1713-9L





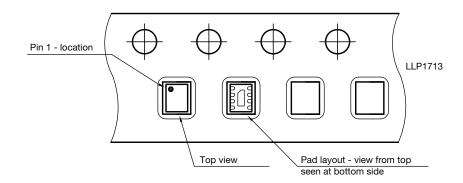


#### Foot print recommendation:



Document no.:S8-V-3906.04-001 (4) Created - Date: 28. August 2006 Rev. 1 - Date: 27. May 2008 20386







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