# VS-MBRS360TRPbF

BoHS

COMPLIANT

Vishay High Power Products

# Schottky Rectifier, 3.0 A



- Small foot print, surface mountable
- Very low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

#### DESCRIPTION

The VS-MBRS360TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	3.0	А		
V <sub>RRM</sub>		60	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	790	A		
V <sub>F</sub>	3.0 Apk, T <sub>J</sub> = 125 °C	0.61	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-MBRS360TRPbF	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	60	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	00	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum and a mark		50 % duty cycle at $T_L$ = 118 °C, rectangular waveform		3.0	
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 105 °C, rectangular waveform		4.0	
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	790	A
	IFSM	10 ms sine or 6 ms rect. pulse		80	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.0 A, L = 10 mH		5.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	А



3.0 A

60 V

30 mA at 125 °C



**PRODUCT SUMMARY** 

I<sub>F(AV)</sub>

 $V_{\mathsf{R}}$ 

 $I_{RM}$ 



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
		3 A	T <sub>J</sub> = 25 °C	0.57	0.74	V
Maximum fanward voltage dran	V <sub>EM</sub> <sup>(1)</sup>	6 A		0.72	0.9	
Maximum forward voltage drop	VFM ("	3 A	T <sub>1</sub> = 125 °C	0.51	0.61	
		6 A	1j = 125 C	0.62	0.77	
		T <sub>J</sub> = 25 °C		-	0.5	mA
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 100 °C	$V_R = Rated V_R$	-	20	
		T <sub>J</sub> = 125 °C		-	30	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		-	180	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		-	3.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		-	10 000	V/µs

#### Note

<sup>(1)</sup> Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>		12	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	46	
Approximate weight			0.24	g
Approximate weight			0.008	oz.
Marking device		Case style SMC (similar to DO-214AB)	V36	

#### Notes

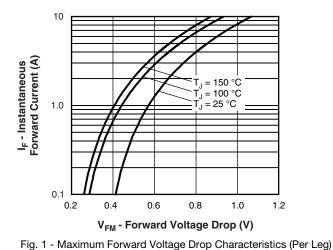
(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



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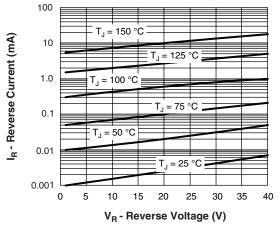


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

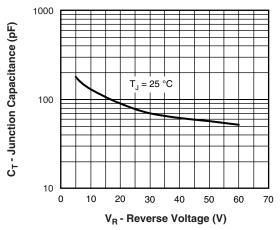


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

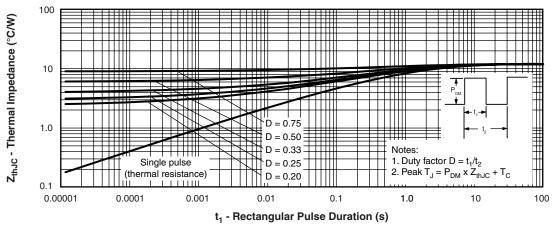
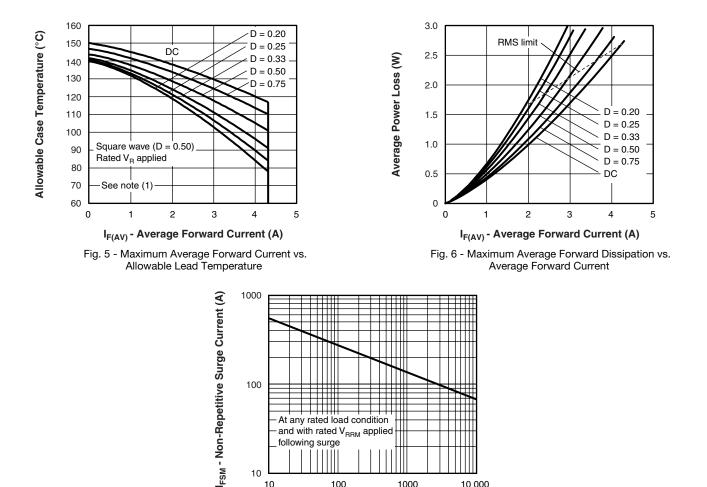


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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following surge

10 10

At any rated load condition and with rated V<sub>BBM</sub> applied

100

t<sub>n</sub> - Square Wave Pulse Duration (µs) Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

1000

10 000

#### Note

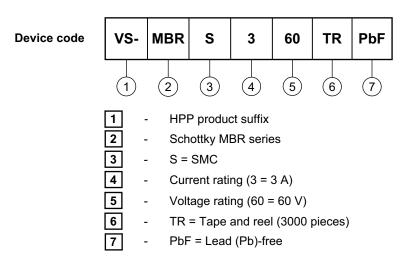
- (1)
- Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ; Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6); Pd<sub>REV</sub> = Inverse power loss =  $V_{R1} \times I_R$  (1 D);  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$





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#### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95023				
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			

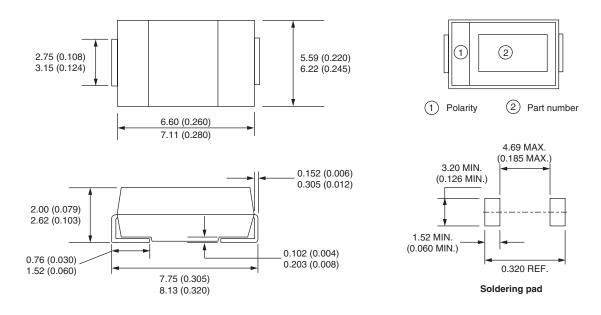


### **Outline Dimensions**

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SMC

### **DIMENSIONS** in millimeters (inches)



1



Vishay

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