Darlington Complementary Silicon Power Transistors

Designed for general-purpose amplifier and low-speed switching applications.



• High DC Current Gain -

$$h_{FE} = 2500 \text{ (Typ)} @ I_{C}$$

= 4.0 Adc

• Collector-Emitter Sustaining Voltage - @ 30 mAdc

• Low Collector-Emitter Saturation Voltage -

$$V_{CE(sat)} = 2.0 \text{ Vdc (Max)} @ I_C = 4.0 \text{ Adc}$$

= 3.0 Vdc (Max) @ $I_C = 6.0 \text{ Adc}$

- Monolithic Construction with Built-In Base-Emitter Shunt Resistors
- Pb-Free Packages are Available*

MAXIMUM RATINGS

| Rating | Symbol | TIP131 | TIP132 TIP137 | Unit |
|------------------------------------------------------|-----------------------------------|-------------|------------------|------|
| Collector-Emitter Voltage | V _{CEO} | 80 | 100 | Vdc |
| Collector-Base Voltage | V _{CB} | 80 100 | | Vdc |
| Emitter-Base Voltage | V _{EB} | 5.0 | | Vdc |
| Collector Current - Continuous Peak | I _C | 8.0 12 | | Adc |
| Base Current | I _B | 300 | | mAdc |
| Total Power Dissipation @ T _C = 25°C | P_{D} | 70 | | W |
| Total Power Dissipation @ T _A = 25°C | P _D | 2.0 | | W |
| Operating and Storage Junction, Temperature Range | T _J , T _{stg} | -65 to +150 | | °C |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit | | | |
|-----------------------------------------|-----------------|------|------|--|--|--|
| Thermal Resistance, Junction-to-Case | $R_{	heta JC}$ | 1.78 | °C/W | | | |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 63.5 | °C/W | | | |

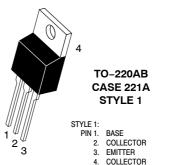
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

http://onsemi.com

DARLINGTON 8 AMPERE COMPLEMENTARY SILICON POWER TRANSISTORS 80-100 VOLTS, 70 WATTS





MARKING

TIP13x = Device Code x = 1, 2, or 7

A = Assembly Location

Y = Year
WW = Work Week
G = Pb-Free Package

ORDERING INFORMATION

| Device | Package | Shipping |
|---------|---------------------|---------------|
| TIP131 | TO-220 | 50 Units/Rail |
| TIP131G | TO-220 (Pb-Free) | 50 Units/Rail |
| TIP132 | TO-220 | 50 Units/Rail |
| TIP132G | TO-220 (Pb-Free) | 50 Units/Rail |
| TIP137 | TO-220 | 50 Units/Rail |
| TIP137G | TO-220 (Pb-Free) | 50 Units/Rail |

1

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

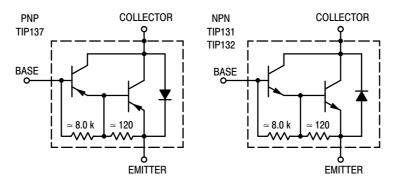


Figure 1. Darlington Circuit Schematic

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

| Characteristic | | Symbol | Min | Max | Unit |
|-----------------------------------------------------------------------------------------------------------------------------|--------------------------|-----------------------|-------------|------------|------|
| OFF CHARACTERISTICS | | • | • | • | • |
| Collector–Emitter Sustaining Voltage (Note 1) $(I_C = 30 \text{ mAdc}, I_B = 0)$ | TIP131 TIP132, TIP137 | V _{CEO(sus)} | 80 100 | - - | Vdc |
| Collector Cutoff Current $(V_{CE} = 40 \text{ Vdc}, I_B = 0)$ $(V_{CE} = 50 \text{ Vdc}, I_B = 0)$ | TIP131 TIP132, TIP137 | I _{CEO} | - - | 0.5 0.5 | mAdc |
| Collector Cutoff Current $(V_{CB} = 80 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 100 \text{ Vdc}, I_E = 0)$ | TIP131 TIP132, TIP137 | I _{CBO} | - - | 0.2 0.2 | mAdc |
| Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0) | | I _{EBO} | - | 5.0 | mAdc |
| ON CHARACTERISTICS (Note 1) | | | | | |
| DC Current Gain $ (I_C = 1.0 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc}) $ $ (I_C = 4.0 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc}) $ | | h _{FE} | 500 1000 | _ 15000 | - |
| Collector–Emitter Saturation Voltage ($I_C = 4.0$ Adc, $I_B = 16$ mAdc) ($I_C = 6.0$ Adc, $I_B = 30$ mAdc) | | V _{CE(sat)} | - - | 2.0 3.0 | Vdc |
| Base-Emitter On Voltage (I _C = 4.0 Adc, V _{CE} = 4.0 Vdc) | | V _{BE(on)} | _ | 2.5 | Vdc |

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

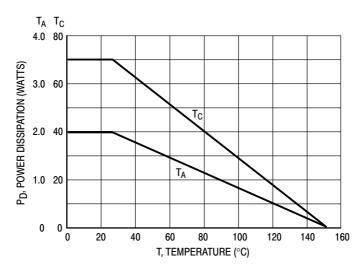


Figure 2. Power Derating

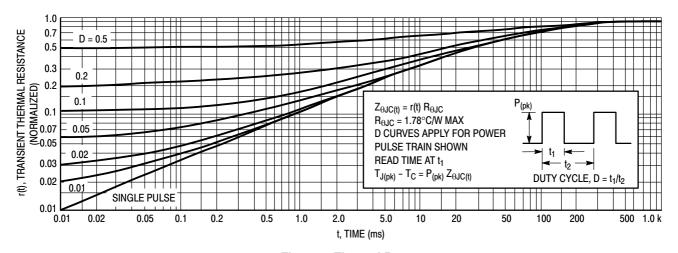
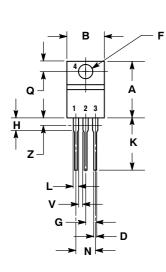
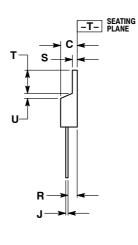


Figure 3. Thermal Response

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AG**





- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

| | INCHES | | MILLIMETERS | |
|-----|--------|-------|-------------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.570 | 0.620 | 14.48 | 15.75 |
| В | 0.380 | 0.405 | 9.66 | 10.28 |
| С | 0.160 | 0.190 | 4.07 | 4.82 |
| D | 0.025 | 0.036 | 0.64 | 0.91 |
| F | 0.142 | 0.161 | 3.61 | 4.09 |
| G | 0.095 | 0.105 | 2.42 | 2.66 |
| Н | 0.110 | 0.161 | 2.80 | 4.10 |
| J | 0.014 | 0.025 | 0.36 | 0.64 |
| K | 0.500 | 0.562 | 12.70 | 14.27 |
| L | 0.045 | 0.060 | 1.15 | 1.52 |
| N | 0.190 | 0.210 | 4.83 | 5.33 |
| Q | 0.100 | 0.120 | 2.54 | 3.04 |
| R | 0.080 | 0.110 | 2.04 | 2.79 |
| S | 0.045 | 0.055 | 1.15 | 1.39 |
| T | 0.235 | 0.255 | 5.97 | 6.47 |
| U | 0.000 | 0.050 | 0.00 | 1.27 |
| ٧ | 0.045 | | 1.15 | |
| Z | | 0.080 | | 2.04 |

STYLE 1: PIN 1. BASE

- COLLECTOR 2.
- 3. **EMITTER**
- COLLECTOR

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