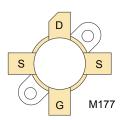


50V, 400W, 150MHz

## RF POWER VERTICAL MOSFET

The VRF2944 is a gold-metallized silicon n-channel RF power transistor designed for broadband commercial and military applications requiring high power and gain without compromising reliability, ruggedness, or inter-modulation distortion.



#### **FEATURES**

- Improved Ruggedness V<sub>(BR)DSS</sub> = 170V
- 400W with 22dB Typ. Gain @ 30MHz, 50V
- · Excellent Stability & Low IMD
- Common Source Configuration
- · Available in Matched Pairs

- 70:1 Load VSWR Capability at Specified Operating Conditions
- Nitride Passivated
- · Refractory Gold Metallization
- Higher Power Version of VRF2933
- · Thermally Enhanced Package
- RoHS Compliant



#### **Maximum Ratings** All Ratings: T<sub>c</sub> =25°C unless otherwise specified

Symbol	Parameter	VRF2933(MP)	Unit
V <sub>DSS</sub>	Drain-Source Voltage	170	V
I <sub>D</sub>	Continuous Drain Current @ T <sub>c</sub> = 25°C	50	Α
$V_{gs}$	Gate-Source Voltage	±40	V
P <sub>D</sub>	Total Device dissipation @ T <sub>c</sub> = 25°C	795	W
T <sub>STG</sub>	Storage Temperature Range	-65 to 150	ڻ ا
T <sub>J</sub>	Operating Junction Temperature Max	200	

#### **Static Electrical Characteristics**

Symbol	Parameter	Min	Тур	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage (V <sub>GS</sub> = 0V, I <sub>D</sub> = 100mA)	170	180		V
V <sub>DS(ON)</sub>	On State Drain Voltage (I <sub>D(ON)</sub> = 25A, V <sub>GS</sub> = 10V)		1.7	2.1	l v
I <sub>DSS</sub>	Zero Gate Voltage Drain Current (V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V)			2.0	mA
I <sub>GSS</sub>	Gate-Source Leakage Current (V <sub>DS</sub> = ±20V, V <sub>DS</sub> = 0V)			2.0	μΑ
$g_{fs}$	Forward Transconductance (V <sub>DS</sub> = 10V, I <sub>D</sub> = 20A)	10			mhos
V <sub>GS(TH)</sub>	Gate Threshold Voltage (V <sub>DS</sub> = 10V, I <sub>D</sub> = 100mA)	2.9	3.6	4.4	V

#### **Thermal Characteristics**

Syml	bol	Characteristic	Min	Тур	Max	Unit
$R_{\theta J}$	С	Junction to Case Thermal Resistance			0.22	°C/W

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

**Dynamic Characteristics** 

W	RF	วฉ	ЛЛ	/ N/I	D
v	1	20	77	1 IVI	

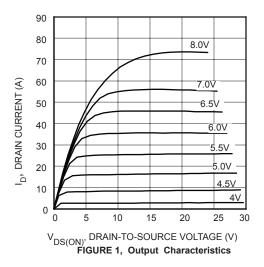
	Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Γ	C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V		1050		
	C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 50V		520		pF
Г	C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz		62		

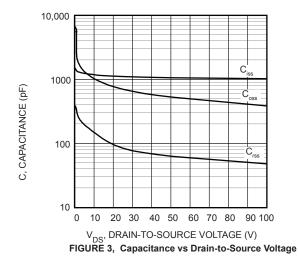
### **Functional Characteristics**

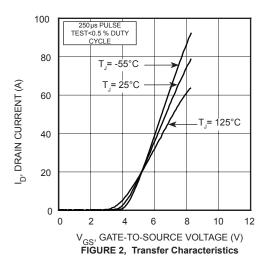
Symbol	Parameter	Min	Тур	Max	Unit
G <sub>PS</sub>	$f_1 = 30MHz, V_{DD} = 50V, I_{DQ} = 250mA, P_{out} = 400W$	23	25		dB
$\eta_{\scriptscriptstyle D}$	$f_1 = 30MHz, V_{DD} = 50V, I_{DQ} = 250mA, P_{out} = 400W$		50		%
Ψ	f = 30MHz, $V_{DD}$ = 50V, $I_{DQ}$ = 250mA, $P_{out}$ = 400W CW 70:1 VSWR - All Phase Angles, 0.2mSec X 20% Duty Factor	No De	gradation	in Output	Power

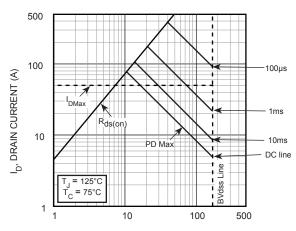
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### **Typical Performance Curves**

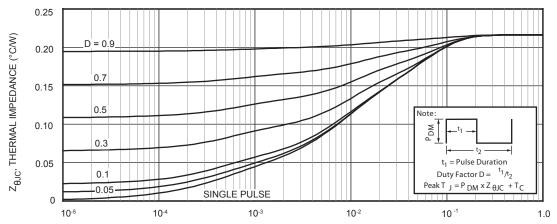








V<sub>DS</sub>, DRAIN-TO-SOURCE VOLTAGE (V) FIGURE 4, Forward Safe Operating Area



RECTANGULAR PULSE DURATION (seconds)
Figure 5. Maximum Effective Transient Thermal Impedance Junction-to-Case vs Pulse Duration

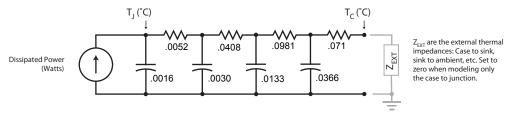
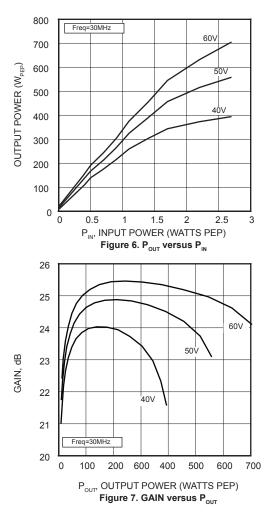


FIGURE 5b, TRANSIENT THERMAL IMPEDANCE MODEL



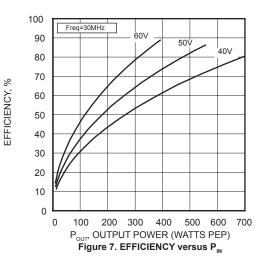
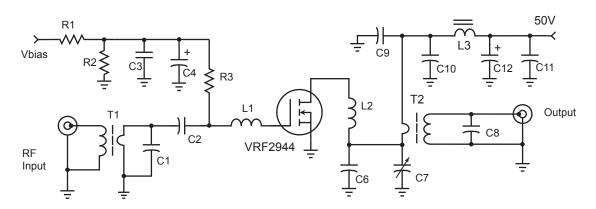


Table 1 - Typical Class AB Large Signal Input - Output Impedance

Freq. (MHz)	Z <sub>in</sub>	Z <sub>out</sub>
30	4.5 - j 2.5	2.15 - j 2.71

 $I_{dq}$  = 100mA  $Z_{OL}$  - Conjugate of optimum load for 400 Watts output at  $V_{dd}$ =150V

## 30 MHz Test Circuit

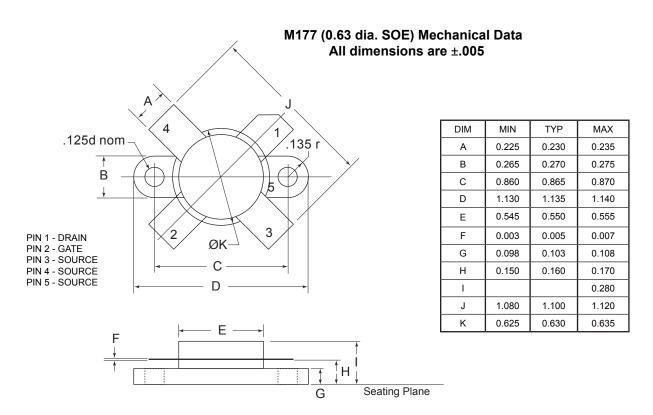


C1 1200pF ATC100B ceramic C2, C3 0.1uF 100V 1206 SMT C9-C11 .047uF NPO 100V 1218 SMT C6 180 pF metal clad mica C7 ARCO 465 mica trimmer C8 100 pF ATC 100E ceramic C4, C12 10uF 100V Electrolytic L1 25 nH - 2t #18 0.2"d .2"l L2 26 nH - 1.5t #12 0.31"d2 L3 2t #16 on 2x 267300081 .5" bead R1-R2 1k  $\Omega$  1/4W R3 100  $\Omega$  1W T1 16:1 transforner 4t #24 teflon on RF Parts Co. T1/2 transformer core T2 9:1 transformer 3t 2-ply #16 teflon on RF Parts Co. T1 transformer core

Adding MP at the end of P/N specifies a matched pair where  $V_{GS(TH)}$  is matched between the two parts.  $V_{TH}$  values are marked on the devices per the following table.

Code	Vth Range	Code 2	Vth Range
А	2.900 - 2.975	М	3.650 - 3.725
В	2.975 - 3.050	N	3.725 - 3.800
С	3.050 - 3.125	Р	3.800 - 3.875
D	3.125 - 3.200	R	3.875 - 3.950
Е	3.200 - 3.275	S	3.950 - 4.025
F	3.275 - 3.350	Т	4.025 - 4.100
G	3.350 - 3.425	W	4.100 - 4.175
Н	3.425 - 3.500	Х	4.175 - 4.250
J	3.500 - 3.575	Υ	4.250 - 4.325
K	3.575 - 3.650	Z	4.325 - 4.400

 $V_{_{TH}}$  values are based on Microsemi measurements at datasheet conditions with an accuracy of 1.0%.



**HAZARDOUS MATERIAL WARNING:** The ceramic portion of the device below the lead plane is beryllium oxide. Beryllium oxide dust is highly toxic when inhaled. Care must be taken during handling and mounting to avoid damage to this area. These devices must never be thrown away with general industrial or domestic waste. BeO substrate weight: 0.703g. Percentage of total module weight which is BeO: 9%.

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