HALOGEN FREE



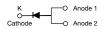
Vishay General Semiconductor

High Current Density Surface Mount Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.26 \text{ V}$ at $I_F = 5 \text{ A}$

eSMP® Series TMBS[®]





PRIMARY CHARACTERISTICS				
I _{F(AV)}	15 A			
V _{RRM}	50 V			
I _{FSM}	200 A			
V _F at I _F = 15 A	0.41 V			
T _J max.	150 °C			
Package	TO-277A (SMPC)			
Diode variation Single die				

FEATURES

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

MECHANICAL DATA

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V15PN50	UNIT	
Device marking code		15N5		
Maximum repetitive peak reverse voltage	V _{RRM}	50	V	
Maximum average forward rectified current (fig. 1)	I _F ⁽¹⁾	15	Α	
	I _F ⁽²⁾	6.0		
Maximum DC reverse voltage	V _{DC}	35	V	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	200	А	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

- (1) Mounted on 30 mm x 30 mm 2 oz. pad PCB
- (2) Free air, mounted on recommended copper pad area



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5.0 A	T _A = 25 °C	V _F ⁽¹⁾	0.38	-	V
	$I_F = 7.5 A$			0.41	-	
	I _F = 15 A			0.48	0.56	
	I _F = 5.0 A	T _A = 125 °C		0.26	=	
	I _F = 7.5 A			0.31	-	
	I _F = 15 A			0.41	0.50	
Reverse current	V _R = 50 V	T _A = 25 °C	T _A = 25 °C	140	3000	μΑ
	V _R = 50 V	T _A = 125 °C	'R (-)	60	140	mA

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V15PN50	UNIT	
Typical thermal resistance	R _{θJA} (1) (2)	70	°C/W	
Typical triefmai resistance	R _{θJM} ⁽³⁾	4		

Notes

- $^{(1)}$ Free air, mounted on recommended copper pad area; thermal resistance $R_{\theta JA}$ junction to ambient
- (2) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$
- (3) Mounted on 30 mm x 30 mm 2 oz. pad PCB; thermal resistance R_{0JM} junction to mount measured at cathode side

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V15PN50-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V15PN50-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

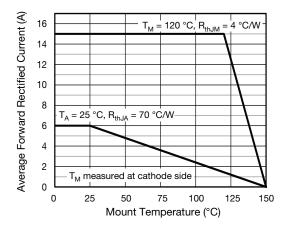


Fig. 1 - Maximum Forward Current Derating Curve (D = Duty Cycle = 0.5)

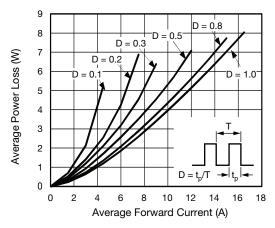


Fig. 2 - Forward Power Loss Characteristics



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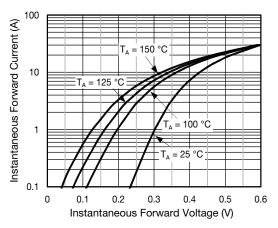


Fig. 3 - Typical Instantaneous Forward Characteristics

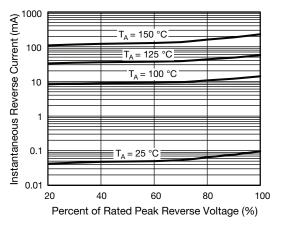


Fig. 4 - Typical Reverse Leakage Characteristics

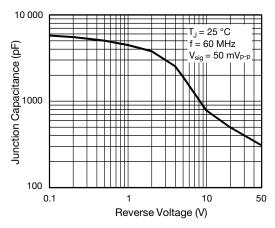


Fig. 5 - Typical Junction Capacitance

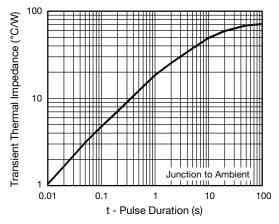
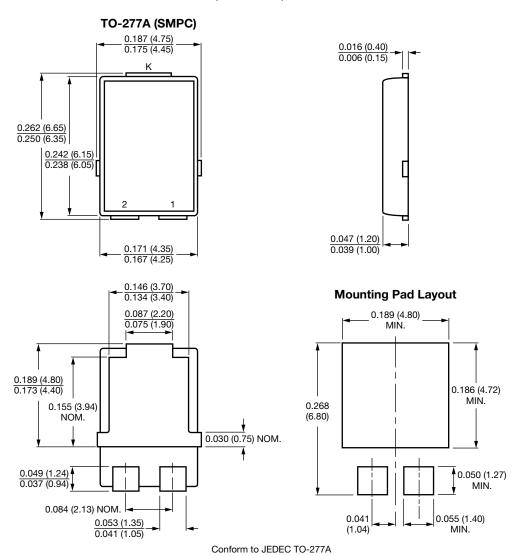


Fig. 6 - Typical Transient Thermal Impedance



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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