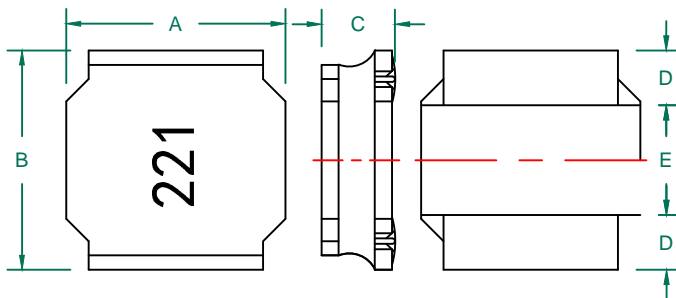


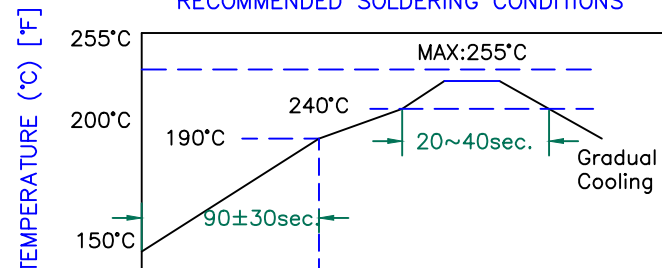
TYS8040221M-10

PHYSICAL DIMENSIONS:

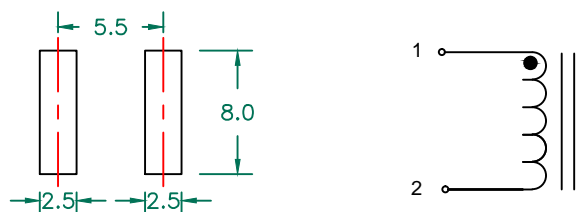
A	8.00	±	0.30
B	8.00	±	0.30
C	4.00	+ -	0.20 0.30
D	2.45	±	0.30
E	3.10	±	0.30



RECOMMENDED SOLDERING CONDITIONS



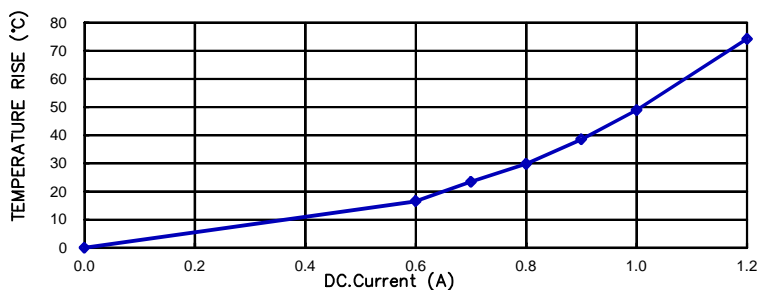
LAND PATTERNS FOR REFLOW SOLDERING



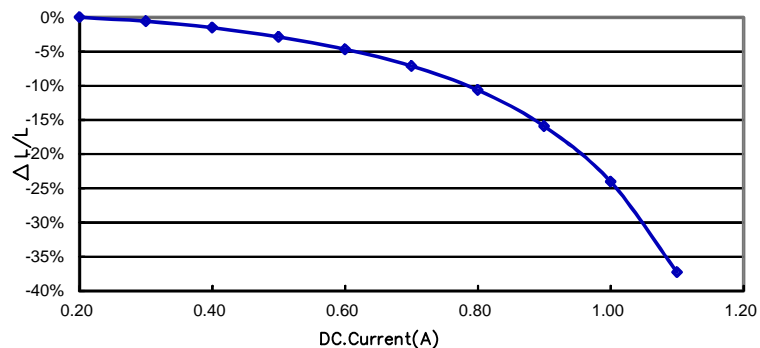
ELECTRICAL SPECIFICATION

	Min	Nom	Max
INDUCTANCE (uH) L @ 100 KHz/1V ±20%	176	220	264
DCR (Ω)		0.599	0.7787

CHARACTERISTICS OF TEMPERATURE RISE



CURRENT VS INDUCTANCE DROP IN RATES



Saturation Current(A)	0.85
SRF (MHz)	3.5
Temperature Rise Current (A)	0.80

NOTES:

- OPERATION TEMPERATURE RANGE: -40°C~+125°C (INCLUDING SELF-HEATING).
- STORAGE TEMPERATURE RANGE (PACKAGING CONDITIONS): -10°C TO +40°C AND RH 70% (MAX.)
- UNLESS OTHERWISE SPECIFIED, THE STANDARD ATMOSPHERIC CONDITIONS FOR MEASUREMENT/TEST AS:
A. AMBIENT TEMPERATURE: 20±15°C.
B. RELATIVE HUMIDITY: 65%±20%.
- SATURATION CURRENT IS THE DC CURRENT AT WHICH THE INDUCTANCE DROPS OFF APPROXIMATELY 30% FROM ITS VALUE WITHOUT CURRENT.(AMBIENT TEMPERATURE 25±5°C)
- TEMPERATURE RISE CURRENT (IRMS):
DC CURRENT THAT CAUSES THE TEMPERATURE RISE (ΔT ≤40°C) FROM 25°C AMBIENT.

DIMENSIONS ARE IN mm .				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
				Laird			
PROJECT/PART NUMBER:				REV	PART TYPE:	DRAWN BY:	
C	CHANGE DIMENSIONS C/D/E	08/18/16	QUI	C	POWER INDUCTOR	QUI	
B	CHANGE TEMP. FROM -25°C~+125°C	12/26/12	QUI	DATE: 06/01/12			
A	ORIGINAL DRAFT	06/01/12	QUI	SCALE: NTS			
REV	DESCRIPTION	DATE	INT	CAD #	TOOL #	SHEET: 1 of 1	
				TYS8040221M-10-C	-		

Mouser Electronics

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