



Body Diode

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON) MAX</sub>	Package	Ι <sub>D</sub> T <sub>A</sub> = +25°C
30V	40mΩ @ V <sub>GS</sub> = 10V	SC59	5.1A
300	50mΩ @ V <sub>GS</sub> = 4.5V	3059	4.3A

#### Description

This new generation MOSFET has been designed to minimize the onstate resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- Load Switch
- **DC-DC Converters**
- **Power Management Functions**

#### **30V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Features**

- Low On-Resistance
- **ESD** Protected Gate
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### Mechanical Data

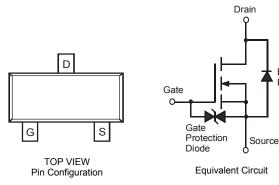
- Case: SC59
- Case Material Molded Plastic. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram
- Weight: 0.014 grams (approximate)





SC59

TOP VIEW



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3070SSN-7	SC59	3000/Tape & Reel

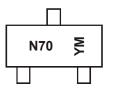
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html

#### Marking Information



N70 = Product Type Marking Code YM = Date Code Marking Y = Year ex: Z = 2012 M = Month ex: 9 = September

Date Code Key		<u>.</u>										
Year	2010		2011	2012		2013	2014		2015	2016	1	2017
Code	Х		Y	Z		А	В		С	D		E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V <sub>DSS</sub>	30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	4.2 3.3	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	5.1 4	А
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	3.7 2.8	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	4.3 3.3	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	60	А		
Maximum Body Diode Forward Current (Note 6)	ls	2	А		

#### **Thermal Characteristics**

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Characteristic	Symbol	Value	Units		
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	Р	0.78	W	
Total Power Dissipation (Note 5)	T <sub>A</sub> = +70°C	PD	0.5	vv	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	Р	160	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ extsf{ heta}JA}$	115	°C/W	
Tatal Bawar Dissinction (Nata 6)	T <sub>A</sub> = +25°C	Р	1.3	W	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	PD	0.8	vv	
Thermal Registeres, Junction to Ambient (Note 6)	Steady state	Р	96	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ extsf{ heta}JA}$	68	°C/W	
Thermal Resistance, Junction to Case (Note 6)		R <sub>eJC</sub>	18	°C/W	
Dperating and Storage Temperature Range		T <sub>J.</sub> T <sub>STG</sub>	-55 to +150	°C	

## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	ii		1		1	1	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		1	μA	$V_{DS}$ =24V, $V_{GS}$ = 0V	
Gate-Body Leakage	I <sub>GSS</sub>	—	—	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.1	—	2.1	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		—	24	40		V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.2A	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	30	50	mΩ	$V_{GS} = 4.5V, I_D = 2A$	
Forward Transfer Admittance	IY <sub>fs</sub> I	—	2.7	_	S	V <sub>DS</sub> = 5V, I <sub>D</sub> =4.2A	
Diode Forward Voltage	V <sub>SD</sub>	_	0.75	1.0	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	—	697	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz	
Output Capacitance	C <sub>oss</sub>	—	97	—	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	67	_	pF		
Gate Resistance	R <sub>g</sub>	_	1.47	_	Ω	$V_{DS}$ = 0V, $V_{GS}$ = 0V, f = 1MHz	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	6	_			
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	—	13.2	—	nC	V <sub>DS</sub> = 15V. I <sub>D</sub> = 9A	
Gate-Source Charge	Q <sub>gs</sub>	_	2.2	_	110	VDS - 13V, ID - 9A	
Gate-Drain Charge	$Q_{gd}$	—	1.8	—			
Turn-On Delay Time	t <sub>D(ON)</sub>	—	4.3	—	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	4.4		ns	$V_{DD}$ =15V, $V_{GEN}$ =10V, $R_{GEN}$ = 6 $\Omega$ ,	
Turn-On Rise Time	tr	_	20.1	—	ns	$R_L=15\Omega$	
Turn-Off Fall Time	t <sub>f</sub>	_	4.1		ns		
Reverse Recovery Time	trr	_	7.3		Ns	IF = 9A, di/dt = 500A/µs	
Reverse Recovery Charge	Q <sub>rr</sub>	_	7.9	_	nC	IF = 9A, di/dt = 500A/µs	

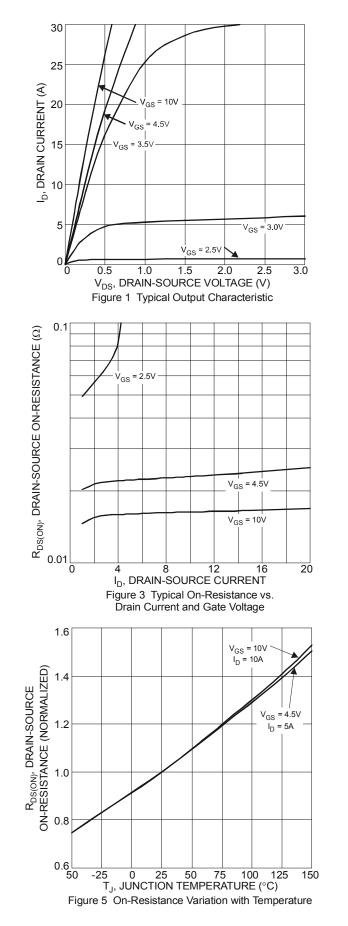
Notes:

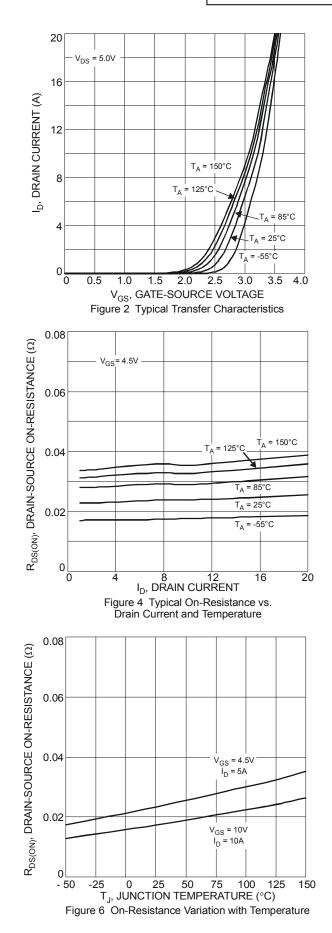
5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided. The power dissipation  $P_D$  is based on t<10s  $R_{BJA}$ .

Device mounted on 1" x 1" FR-4 PCB with high coverage 2 oz. Copper, single sided. The power dissipation P<sub>D</sub> is based on t<10s R<sub>8JA</sub>.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.

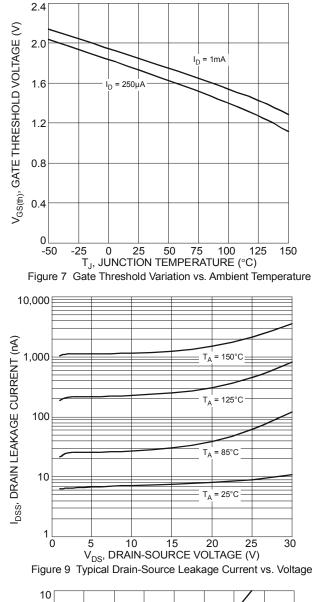


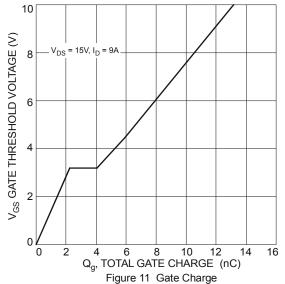


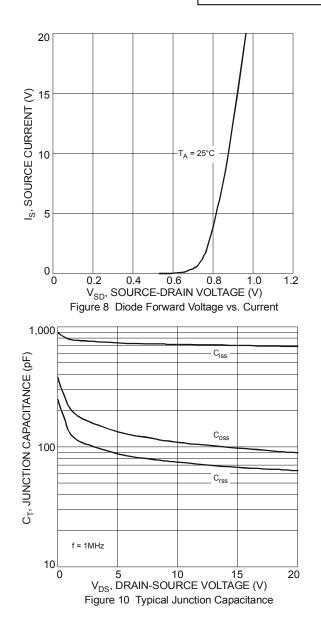


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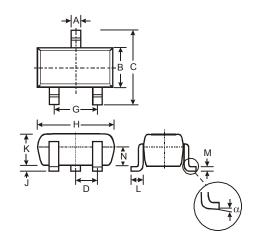






## **Package Outline Dimensions**

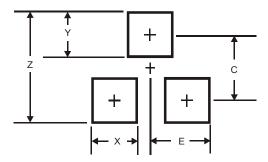
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SC59						
Dim	Min	Max	Тур			
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
С	2.70	3.00	2.80			
D	-	-	0.95			
G	-	-	1.90			
н	2.90	3.10	3.00			
J	0.013	0.10	0.05			
к	1.00	1.30	1.10			
L	0.35	0.55	0.40			
М	0.10	0.20	0.15			
Ν	0.70	0.80	0.75			
α	0°	8°	-			
All	Dimens	ions in	mm			

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	3.4
Х	0.8
Y	1.0
С	2.4
Е	1.35



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