

2.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

PowerDl[®]123

DFLS230L

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- High Current Capability and Low Forward Voltage Drop
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDl[®]123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-02
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.01 grams (approximate)



Top View

Ordering Information (Note 2)

Part Number	Case	Packaging
DFLS230L-7	PowerDI [®] 123	3000/Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.

2. For packaging details, go to our website at http://www.diodes.com.

Marking Information



F03A = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

Date Code Key

Year	2004	20	005	2006	2007	20	08	2009	2010	20)11	2012
Code	R		S	Т	U	,	V	W	Х	Ŷ	Y	Z
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

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Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

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Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	30	V
RMS Reverse Voltage	V _{R(RMS)}	21	V
Average Forward Current @ T _T = 121°C	I _{F(AV)}	2.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	33	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	PD	1.67	W
Power Dissipation (Note 4)	PD	556	mW
Thermal Resistance Junction to Ambient (Note 3)	$R_{\theta JA}$	60	°C/W
Thermal Resistance Junction to Ambient (Note 4)	R _{θJA}	180	°C/W
Thermal Resistance Junction to Soldering (Note 5)	$R_{\theta JS}$	10	°C/W
Operating Temperature Range	TJ	-40 to +125	٥C
Storage Temperature Range	T _{STG}	-40 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V _{(BR)R}	30	_	_	V	I _R = 1.0mA
Forward Voltage	VF		0.310 0.375	0.420	V	I _F = 1.0A I _F = 2.0A
Leakage Current (Note 6)	I _R		0.260	 1.0	mA	V _R = 5V, T _A = 25°C V _R = 30V, T _A = 25°C
Total Capacitance	CT	_	76	_	pF	V _R = 10V, f = 1.0MHz

3. Part mounted on 2"x2" GETEK board with 1"x1" copper pad, 25% anode, 75% cathode. $T_A = 25^{\circ}C$.

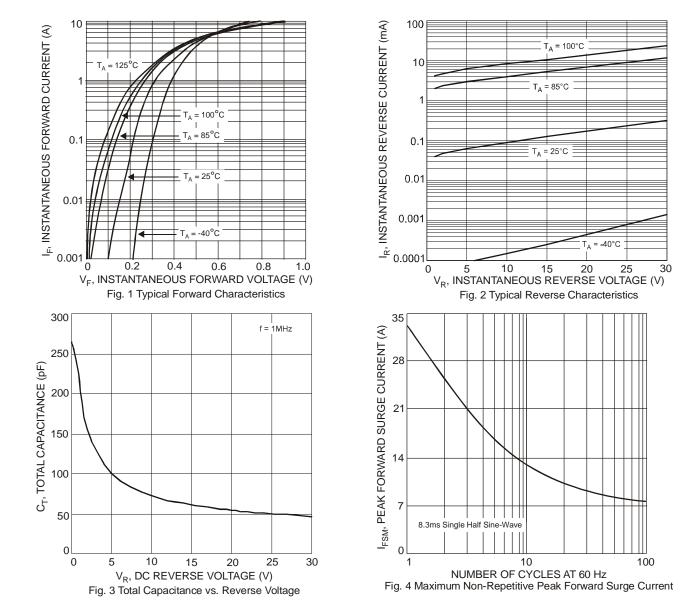
4. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com.

5. Theoretical R_{eJS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.

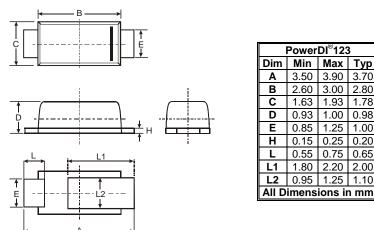
6. Short duration pulse test used to minimize self-heating effect.

Notes:





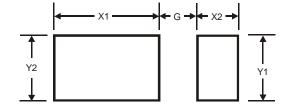
Package Outline Dimensions



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Suggested Pad Layout



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

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