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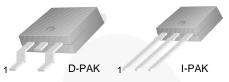


March 2014

KSH47 / KSH50 NPN Epitaxial Silicon Transistor

Features

- · High-Voltage and High-Reliability
- D-PAK for Surface-Mount Applications
- Lead-Formed for Surface Mount Application (No Suffix)
- Straight Lead (I-PAK, " I " Suffix)
- Electrically Similar to Popular TIP47 and TIP50



1.Base 2.Collector 3.Emitter

Ordering Information

Part Number	Top Mark Package Pa		Packing Method
KSH47TF	KSH47	TO-252 3L (DPAK)	Tape and Reel
KSH50TF	KSH50	TO-252 3L (DPAK)	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit	
V _{CBO} Co	Collector-Base Voltage	KSH47	350	V
		KSH50	500	V
V _{CEO}	Collector-Emitter Voltage	KSH47	250	V
		KSH50	400	V
V _{EBO}	Emitter-Base Voltage		5	V
I _C	Collector Current (DC)	1	Α	
I _{CP}	Collector Current (Pulse)	2	Α	
Ι _Β	Base Current		0.6	Α
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	- 65 to 150	°C	

1

Thermal Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
D.	Collector Dissipation (T _C = 25°C)	15.0	W
P _C	Collector Dissipation (T _A = 25°C)	1.56	VV

Electrical Characteristics

Values are at $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage ⁽¹⁾	KSH47	$I_{\rm C} = 30$ mA, $I_{\rm B} = 0$	250			V
		KSH50		400			
I _{CEO}	Collector Cut-Off Current	KSH47	$V_{CE} = 150 \text{ V}, I_{B} = 0$			0.2	mA
		KSH50	$V_{CE} = 300 \text{ V}, I_{B} = 0$			0.2	ША
I _{CES}	Collector Cut-Off Current	KSH47	$V_{CE} = 350 \text{ V}, V_{EB} = 0$			0.1	mA
		KSH50	$V_{CE} = 500 \text{ V}, V_{EB} = 0$			0.1	IIIA
I _{EBO}	Emitter Cut-Off Current		$V_{BE} = 5 \text{ V, } I_{C} = 0$			1	mA
h _{FE}	DC Current Gain ⁽¹⁾		$V_{CE} = 10 \text{ V}, I_{C} = 0.3 \text{ A}$	30		150	
			$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ A}$	10			
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽¹⁾		$I_C = 1 A, I_B = 0.2 A$			1	V
V _{BE} (on)	Base-Emitter On Voltage ⁽¹⁾		$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ A}$	•		1.5	V
f _T	Current Gain Bandwidth Product		$V_{CE} = 10 \text{ V}, I_{C} = 0.2 \text{ A}$	10			MHz

Note:

1. Pulse test: $pw \le 300 \mu s$, duty cycle $\le 2\%$.

Typical Performance Characteristics

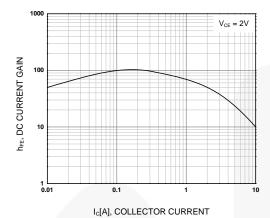


Figure 1. DC Current Gain

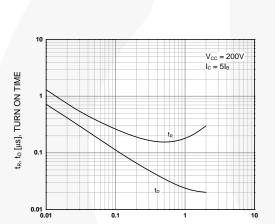


Figure 3. Turn-On Time

I_c[A], COLLECTOR CURRENT

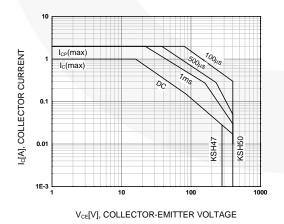


Figure 5. Safe Operating Area

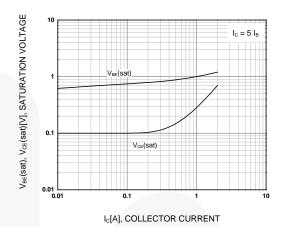


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

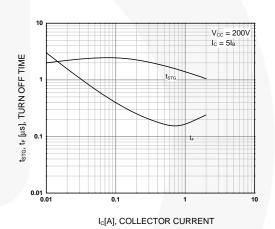


Figure 4. Turn-Off Time

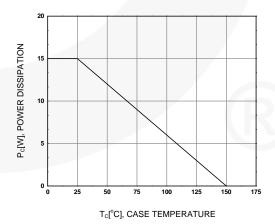


Figure 6. Power Derating

Physical Dimensions

TO-252 3L

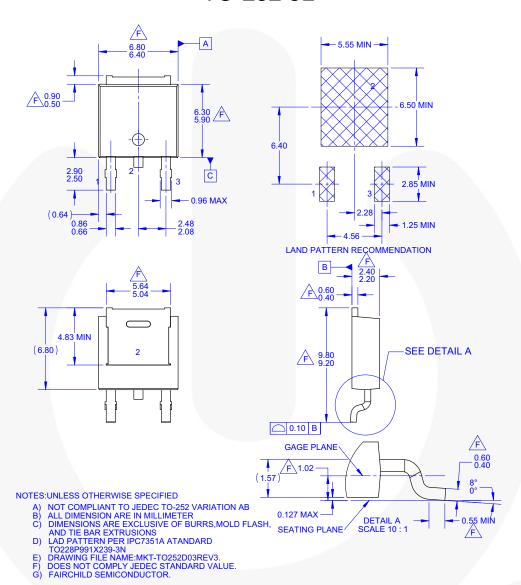


Figure 7. 3-LEAD, TO-252, JEDEC TO-252 VAR. AB, SURFACE MOUNT (DPAK) (ACTIVE)

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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