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November 2013

FAIRCHILD

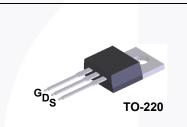
FQP2N80 N-Channel QFET[®] MOSFET 800 V, 2.4 A, 6.3 Ω

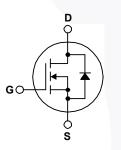
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 2.4 A, 800 V, $R_{DS(on)}$ = 6.3 Ω (Max.) @ V_{GS} = 10 V, I_{D} = 1.2 A
- Low Gate Charge (Typ. 12 nC)
- Low Crss (Typ. 5.5 pF)
- 100% Avalanche Tested





Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

Symbol	Parameter		FQP2N80	Unit
V _{DSS}	Drain-Source Voltage		800	V
I _D	Drain Current - Continuous ($T_C = 25^\circ$	C)	2.4	A
	- Continuous (T _C = 100	°C)	1.52	A
I _{DM}	Drain Current - Pulsed	(Note 1)	9.6	A
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	180	mJ
I _{AR}	Avalanche Current	(Note 1)	2.4	A
E _{AR}	Repetitive Avalanche Energy	(Note 1)	8.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.0	V/ns
P _D	Power Dissipation ($T_C = 25^{\circ}C$)		85	W
	- Derate above 25°C		0.68	W/°C
T _J , T _{STG}	Operating and Storage Temperature Ran	ge	-55 to +150	°C
TL	Maximum Lead Temperature for Solderir 1/8" from Case for 5 seconds	ıg,	300	°C

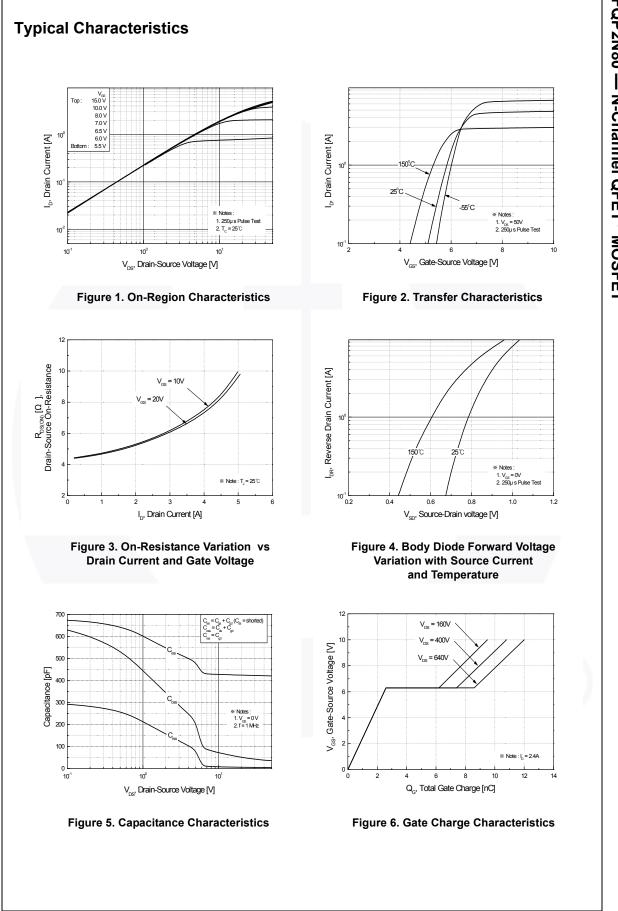
Thermal Characteristics

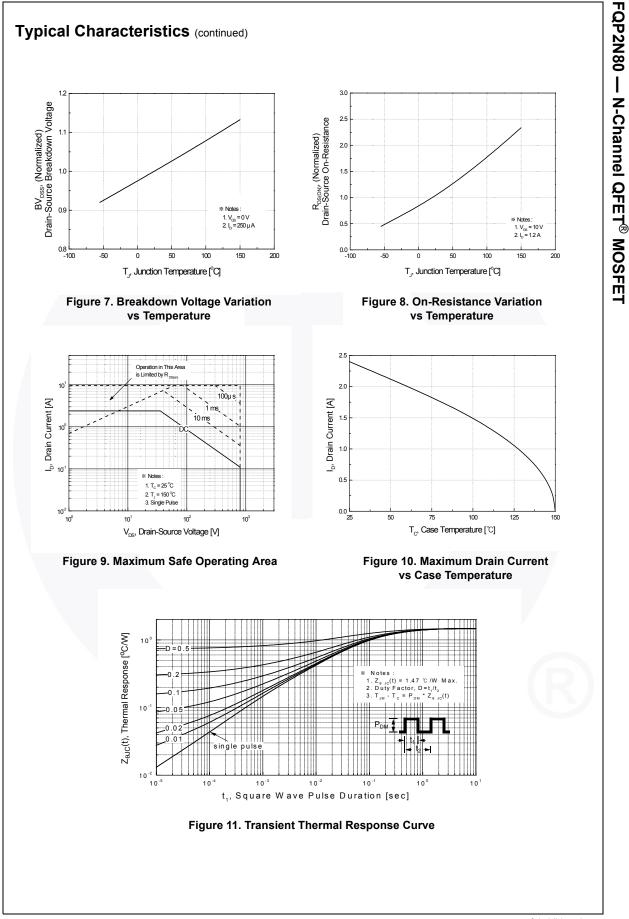
Symbol	Parameter	FQP2N80	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.47	°C/W
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

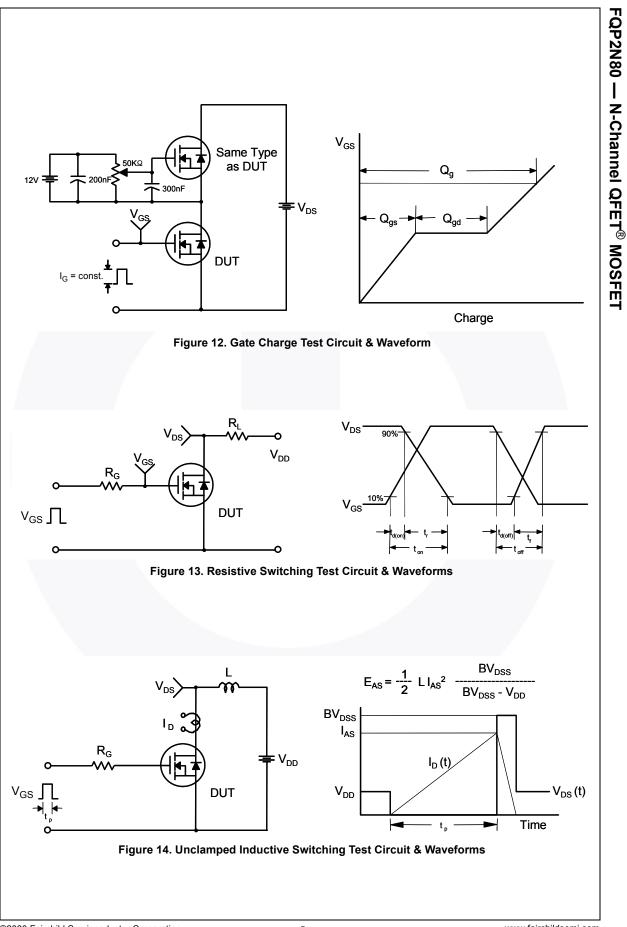
Part NumberTop MarkPackageFQP2N80FQP2N80TO-220		Package	Packing Method	Reel Size	Tape Width		th Q	Quantity	
		Tube N/A		N/A		5	50 units		
lectri	cal Cl	naracteristics	T _C = 25°C	unless otherwise noted.					
Symbol		Parameter		Test Conditi	ons	Min	Тур	Max	Unit
Off Cha	aracter	istics							
BV _{DSS}	Drain-Source Breakdown Voltage		/oltage	V _{GS} = 0 V, I _D = 250 μA		800			V
	Breakdown Voltage Temperature Coefficient		$I_D = 250 \ \mu$ A, Referenced to 25°C						
ΔT_{J}						0.9		V/°C	
DSS	Zero Gate Voltage Drain Current		V_{DS} = 800 V, V_{GS} = 0) V			10	μA	
			V _{DS} = 640 V, T _C = 125°C		-		100	μA	
GSSF	Gate-E	Body Leakage Curre	nt, Forward	V _{GS} = 30 V, V _{DS} = 0 V				100	nA
IGSSR	Gate-E	Body Leakage Curre	nt, Reverse	V _{GS} = -30 V, V _{DS} = 0 V				-100	nA
On Cha	ractor	istics							
V _{GS(th)}	1	Threshold Voltage		V _{DS} = V _{GS} , I _D = 250	uА	3.0		5.0	V
R _{DS(on)}		atic Drain-Source $V_{GS} = 10 V, I_D = 1.2 A$		μ.	0.0		0.0	v	
S(on)						4.9	6.3	Ω	
9 _{FS}	Forwa	rd Transconductance	;	$V_{\rm DS}$ = 50 V, $I_{\rm D}$ = 1.2 /	Ą		2.65		S
	ia Cha	ventovinting							
	1	racteristics					405	550	-5
C _{iss} C _{oss}		Capacitance Capacitance		$V_{DS} = 25 V, V_{GS} = 0$	V,		425 45	550	pF
C _{oss}		· ·	200	f = 1.0 MHz			5.5	60 7.0	pF pF
Orss	Revers	se Transfer Capacita	lice				5.5	7.0	рг
Switchi	ing Ch	aracteristics							
t _{d(on)}	Turn-C	n Delay Time		V _{DD} = 400 V, I _D = 2.4	Δ		12	35	ns
r	Turn-C	n Rise Time		$R_{\rm G} = 25 \ \Omega$	· л ,		30	70	ns
t _{d(off)}	Turn-C	off Delay Time		NG 2032			25	60	ns
f	Turn-C	off Fall Time			(Note 4)		28	65	ns
Qg	Total C	ate Charge		V _{DS} = 640 V, I _D = 2.4	A,		12	15	nC
Q _{gs}	Gate-S	Source Charge		$V_{GS} = 10 V$			2.6		nC
Q _{gd}	Gate-E	Drain Charge			(Note 4)		6.0		nC
	1			d Maximum Rati	ngs				
S		um Continuous Drai						2.4	A
SM	-	um Pulsed Drain-So						9.6	A
V _{SD}		Source Diode Forwa	rd Voltage	$V_{GS} = 0 \text{ V, } \text{I}_{S} = 2.4 \text{ A}$ $V_{GS} = 0 \text{ V, } \text{I}_{S} = 2.4 \text{ A},$ $d\text{I}_{F} / dt = 100 \text{ A}/\mu\text{s}$				1.4	V
		se Recovery Time					480		ns
Q _{rr}	Revers	se Recovery Charge					2.0		μC

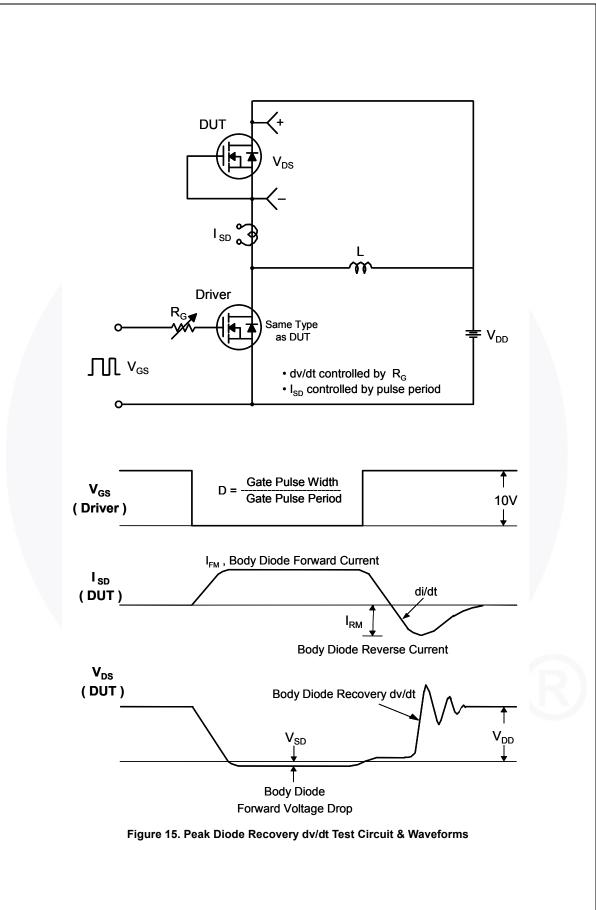
Essentially independent of operating temperature.

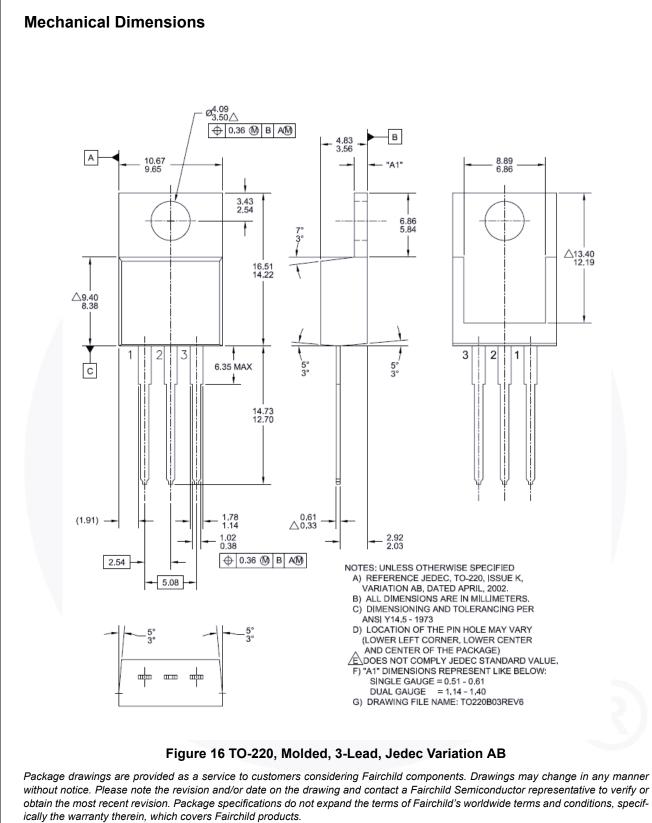
FQP2N80 — N-Channel QFET[®] MOSFET











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http://www.fairchildsemi.com/package/packageDetails.html?id=PN_TT220-003



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QP2N80 -

N-Channel QFET[®] MOSFE

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