

Vishay Siliconix

# N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	(V) $R_{DS(on)}(\Omega)$ $I_D($			
30	0.011 at V <sub>GS</sub> = 10 V	12		
	0.0145 at V <sub>GS</sub> = 4.5 V	9.8		

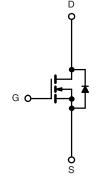
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested



#### **APPLICATIONS**

- Notebook PC
  - Core
  - System Power



N-Channel MOSFET

		SO-8			
s [	1			8	D
s [	2			7	D
s [	3			6	D
G [	4			5	D
	_	Top View	'		

Ordering Information: Si4688DY-T1-E3 (Lead (Pb)-free)

Si4688DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	<b>T</b> <sub>A</sub> = 25 °C, unle	ss otherwise r	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		$V_{DS}$	30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Dunis Comment /T 450 °C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	12	8.9	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		9.5	7.1	
Pulsed Drain Current		I <sub>DM</sub>	40		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.3	1.3	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	20 20		
Avalanche Energy	L = 0.1 min	E <sub>AS</sub>			mJ
M	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5	1.4	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	' D	1.6	0.9	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manipulation to Applicate	t ≤ 10 s	- R <sub>thJA</sub>	43	50	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		73	90	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	19	25	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

# **Si4688DY**

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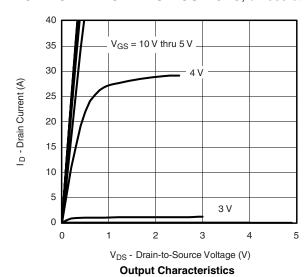
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage V <sub>GS(th</sub>		$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1.0		3.0	٧		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA		
Zana Oata Wallana Busin Oursel		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	μΑ		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5			
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α		
	D	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A		0.009	0.011	Ω		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 9.8 \text{ A}$		0.012	0.0145			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 12 A		32		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 2.3 A, V <sub>GS</sub> = 0 V		0.76	1.1	V		
Dynamic <sup>b</sup>				•				
Input Capacitance	C <sub>iss</sub>			1580		pF nC		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		295				
Reverse Transfer Capacitance	C <sub>rss</sub>			140				
Total Gate Charge	Q <sub>g</sub> Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 12 \text{ A}$		13.2	20			
Total Gate Charge				25.4	38			
Gate-Source Charge		$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 12 \text{ A}$		5.3				
Gate-Drain Charge	$Q_{gd}$			4.3				
Gate Resistance	$R_g$		0.9	1.8	2.7	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>			13	20			
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, R_{L} = 15 \Omega$		10	15			
Turn-Off Delay Time	t <sub>d(off)</sub>	$\rm I_D\cong 1$ A, $\rm V_{GEN}=10$ V, $\rm R_g=6~\Omega$		33	50	ns		
Fall Time	t <sub>f</sub>			10	15			
Source-Drain Reverse Recovery Time t <sub>l</sub>		$I_F = 2.3 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		25	40			

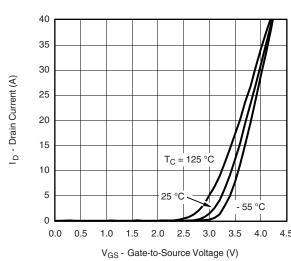
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



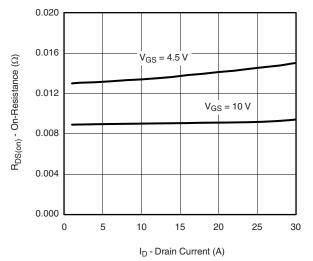


**Transfer Characteristics** 

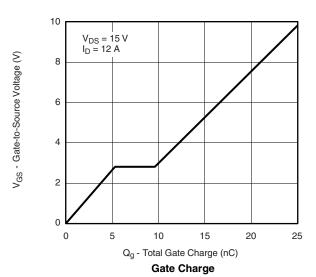


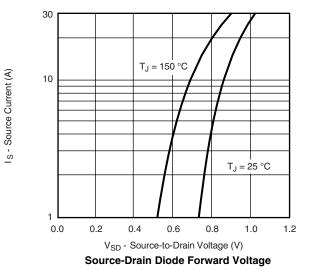
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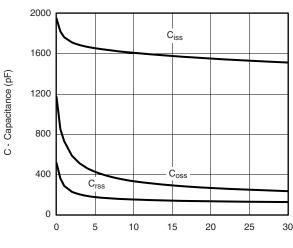
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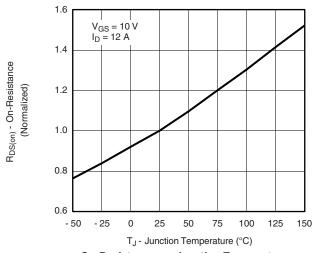
#### On-Resistance vs. Drain Current



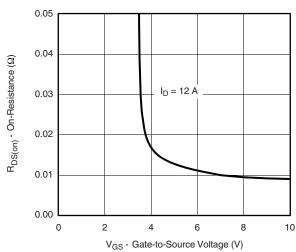




V<sub>DS</sub> - Drain-to-Source Voltage (V) **Capacitance** 



On-Resistance vs. Junction Temperature



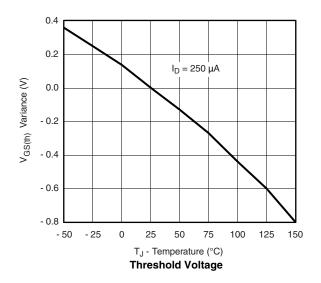
On-Resistance vs. Gate-to-Source Voltage

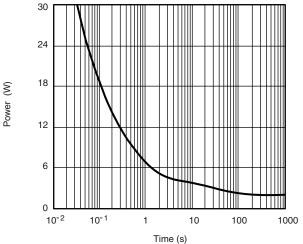
# **Si4688DY**

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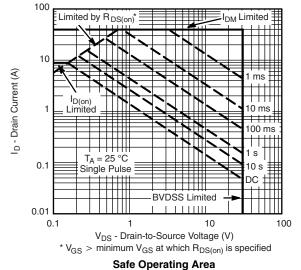
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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

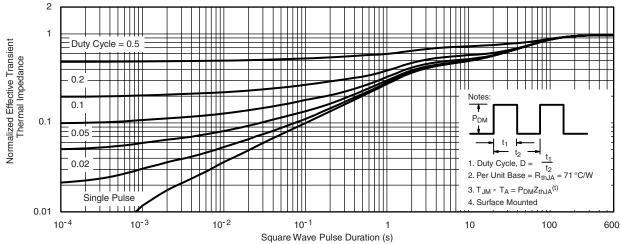




Single Pulse Power, Junction-to-Ambient





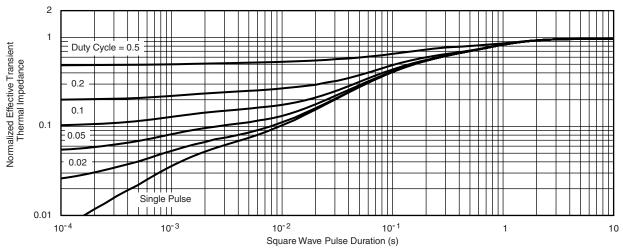


Normalized Thermal Transient Impedance, Junction-to-Ambient



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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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