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November 2014

TIP47 / TIP48 / TIP49 / TIP50 NPN Silicon Transistor

Features

- High-Voltage and Switching Applications
- High Sustaining Voltage: V_{CEO}(sus) = 250 V, 300 V, 350 V, 400 V
- 1 A Rated Collector Current



1.Base 2.Collector 3.Emitter

Ordering Information

Part Number	Top Mark	Package	Packing Method
TIP47	TIP47	TO-220 3L (Single Gauge)	Bulk
TIP47TU	TIP47	TO-220 3L (Single Gauge)	Rail
TIP48	TIP48	TO-220 3L (Single Gauge)	Bulk
TIP48TU	TIP48	TO-220 3L (Single Gauge)	Rail
TIP49	TIP49	TO-220 3L (Single Gauge)	Bulk
TIP50	TIP50	TO-220 3L (Single Gauge)	Bulk
TIP50TU	TIP50	TO-220 3L (Single Gauge)	Rail

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Value	Unit	
		TIP47	350		
V	Collector-Base Voltage	TIP48	400	V	
V_{CBO}		TIP49	450	V	
		TIP50	500		
	Collector-Emitter Voltage	TIP47	250		
V_{CEO}		TIP48	300	V	
		TIP49	350	V	
		TIP50	400		
V _{EBO}	Emitter-Base Voltage	5	V		
I _C	Collector Current (DC)	1	А		
I _{CP}	Collector Current (Pulse)	2	А		
Ι _Β	Base Current		0.6	А	
TJ	Junction Temperature	150	°C		
T _{STG}	Storage Temperature Range	- 65 to 150	°C		

Thermal Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symb	I Parameter	Value	Unit
Po	Collector Dissipation (T _C = 25°C)	40	W
I C	Collector Dissipation (T _A = 25°C)	2	VV

Electrical Characteristics

Values are at $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage ⁽¹⁾	TIP47	$I_C = 30 \text{ mA}, I_B = 0$	250			V
		TIP48		300			
		TIP49		350			
		TIP50		400			
	Collector Cut-Off Current	TIP47	$V_{CE} = 150 \text{ V}, I_{B} = 0$	(1	mA
I _{CEO}		TIP48	$V_{CE} = 200 \text{ V}, I_{B} = 0$			1	
		TIP49	$V_{CE} = 250 \text{ V}, I_{B} = 0$			1	
		TIP50	$V_{CE} = 300 \text{ V}, I_{B} = 0$			1	
	Collector Cut-Off Current	TIP47	$V_{CE} = 350 \text{ V}, V_{EB} = 0$			1	mA
		TIP48	$V_{CE} = 400 \text{ V}, V_{EB} = 0$			1	
I _{CES}		TIP49	$V_{CE} = 450 \text{ V}, V_{EB} = 0$			1	
		TIP50	$V_{CE} = 500 \text{ V}, V_{EB} = 0$			1	
I _{EBO}	Emitter Cut-Off Current		$V_{BE} = 5 \text{ V, } I_{C} = 0$			1	mA
h _{FE}	DC Current Gain ⁽¹⁾		$V_{CE} = 10 \text{ V}, I_{C} = 0.3 \text{ A}$	30		150	
			$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ A}$	10			
V _{CE} (sat)	t) Collector-Emitter Saturation Voltage ⁽¹⁾		$I_C = 1 A, I_B = 0.2 A$			1	V
V _{BE} (on)	Base-Emitter On Voltage ⁽¹⁾		$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ A}$			1.5	V
f _T	Current Gain Bandwidth Product		V _{CE} = 10 V, I _C = 0.2 A, f = 1 MHz	10			MHz

Note:

1. Pulse test: pw \leq 300 μ s, duty cycle \leq 2%.

Typical Performance Characteristics

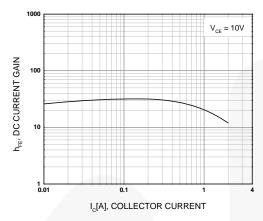


Figure 1. DC Current Gain

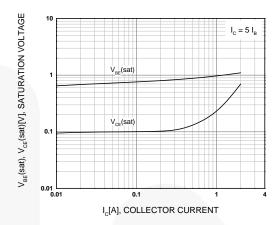


Figure 2. Collector-Emitter Saturation Voltage and Base-Emitter Saturation Voltage

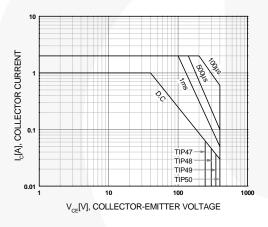


Figure 3. Safe Operating Area

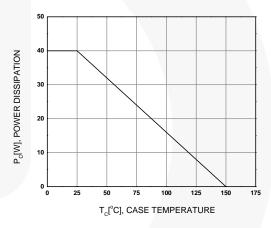
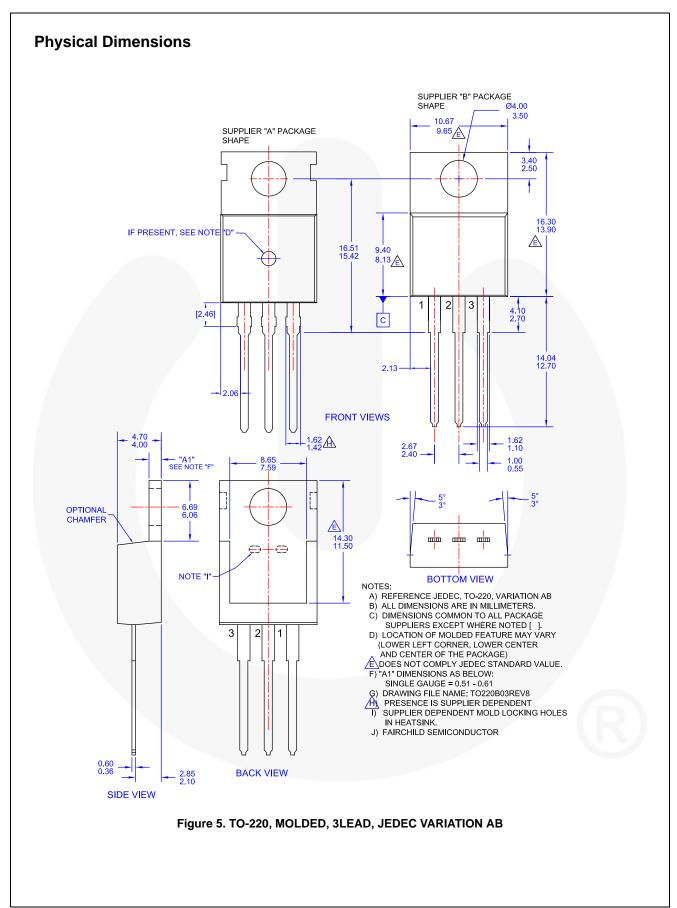


Figure 4. Power Derating







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