## **Silicon Power Transistors**

The MJW21195 and MJW21196 utilize Perforated Emitter technology and are specifically designed for high power audio output, disk head positioners and linear applications.

#### **Features**

- Total Harmonic Distortion Characterized
- High DC Current Gain h<sub>FE</sub> = 20 Min @ I<sub>C</sub> = 8 Adc
- Excellent Gain Linearity
- High SOA: 2.25 A, 80 V, 1 Second
- Pb-Free Packages are Available\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	250	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	400	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector-Emitter Voltage - 1.5 V	V <sub>CEX</sub>	400	Vdc
Collector Current - Continuous - Peak (Note 1)	I <sub>C</sub>	16 30	Adc
Base Current - Continuous	I <sub>B</sub>	5.0	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate Above 25°C	P <sub>D</sub>	200 1.43	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	0.7	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	40	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

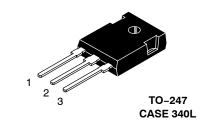
1. Pulse Test: Pulse Width = 5 μs, Duty Cycle ≤ 10%.



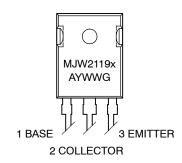
#### ON Semiconductor®

http://onsemi.com

# 16 AMPERES COMPLEMENTARY SILICON POWER TRANSISTORS 250 VOLTS, 200 WATTS



#### **MARKING DIAGRAM**



x = 5 or 6

A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

#### **ORDERING INFORMATION**

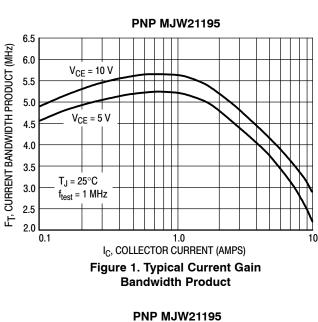
Device	Package	Shipping
MJW21195	TO-247	30 Units/Rail
MJW21195G	TO-247 (Pb-Free)	30 Units/Rail
MJW21196	TO-247	30 Units/Rail
MJW21196G	TO-247 (Pb-Free)	30 Units/Rail

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

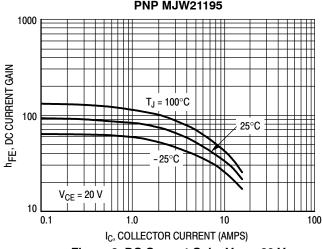
Characteristic		Symbol	Min	Typical	Max	Unit
OFF CHARACTERISTICS						•
Collector–Emitter Sustaining Voltage ( $I_C = 100 \text{ mAdc}, I_B = 0$ )		V <sub>CEO(sus)</sub>	250	-	_	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 200 Vdc, I <sub>B</sub> = 0)		I <sub>CEO</sub>	_	_	100	μAdc
Emitter Cutoff Current (V <sub>CE</sub> = 5 Vdc, I <sub>C</sub> = 0)		I <sub>EBO</sub>	_	_	50	μAdc
Collector Cutoff Current (V <sub>CE</sub> = 250 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc)		I <sub>CEX</sub>	_	_	50	μAdc
SECOND BREAKDOWN						•
Second Breakdown Collector Current with Base Forward Biaser (V <sub>CE</sub> = 50 Vdc, t = 1 s (non-repetitive) (V <sub>CE</sub> = 80 Vdc, t = 1 s (non-repetitive)	d	I <sub>S/b</sub>	4.0 2.25	- -	- -	Adc
ON CHARACTERISTICS						
DC Current Gain ( $I_C = 8$ Adc, $V_{CE} = 5$ Vdc) ( $I_C = 16$ Adc, $I_B = 5$ Adc)		h <sub>FE</sub>	20 8	- -	80 -	
Base–Emitter On Voltage (I <sub>C</sub> = 8 Adc, V <sub>CE</sub> = 5 Vdc)		V <sub>BE(on)</sub>	_	-	2.0	Vdc
Collector–Emitter Saturation Voltage ( $I_C = 8$ Adc, $I_B = 0.8$ Adc) ( $I_C = 16$ Adc, $I_B = 3.2$ Adc)		V <sub>CE(sat)</sub>	_ _	- -	1.0 3	Vdc
DYNAMIC CHARACTERISTICS						•
Total Harmonic Distortion at the Output $V_{RMS} = 28.3 \text{ V}, f = 1 \text{ kHz}, P_{LOAD} = 100 \text{ W}_{RMS}$	h <sub>FE</sub> unmatched	T <sub>HD</sub>	_	0.8	_	%
(Matched pair h <sub>FE</sub> = 50 @ 5 A/5 V)	h <sub>FE</sub> matched		-	0.08	-	
Current Gain Bandwidth Product (I <sub>C</sub> = 1 Adc, V <sub>CE</sub> = 10 Vdc, f <sub>test</sub> = 1 MHz)		f <sub>T</sub>	4	-	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f <sub>test</sub> = 1 MHz)		C <sub>ob</sub>	-	-	500	pF

#### **TYPICAL CHARACTERISTICS**



**NPN MJW21196** 7.5  $\mathsf{F}_\mathsf{T}$ , CURRENT BANDWIDTH PRODUCT (MHz) 7.0 V<sub>CE</sub> = 10 V 6.5 6.0 5.5 5.0 4.5 4.0 3.5 3.0  $T_J=25^{\circ}C$ 2.5  $f_{test} = 1 \text{ MHz}$ 2.0 1.5 1.0 1.0 I<sub>C</sub>, COLLECTOR CURRENT (AMPS) 0.1 10

Figure 2. Typical Current Gain Bandwidth Product



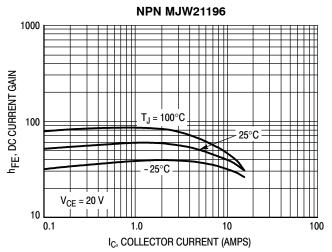
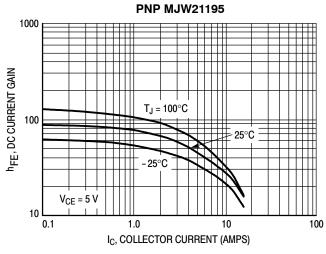


Figure 3. DC Current Gain, V<sub>CE</sub> = 20 V

Figure 4. DC Current Gain, V<sub>CE</sub> = 20 V



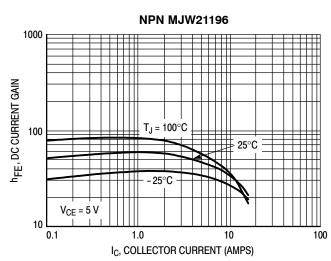


Figure 5. DC Current Gain, V<sub>CE</sub> = 5 V

Figure 6. DC Current Gain, V<sub>CE</sub> = 5 V

#### **TYPICAL CHARACTERISTICS**

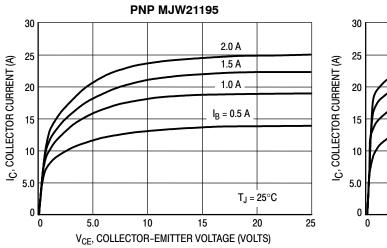


Figure 7. Typical Output Characteristics

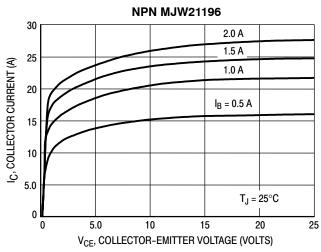


Figure 8. Typical Output Characteristics

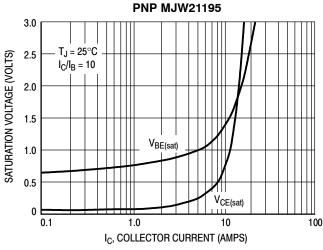


Figure 9. Typical Saturation Voltages

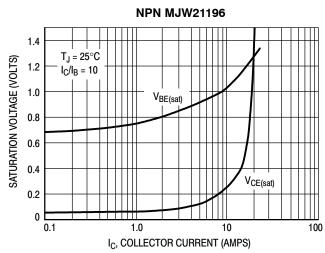


Figure 10. Typical Saturation Voltages

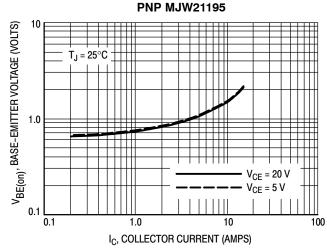


Figure 11. Typical Base-Emitter Voltage

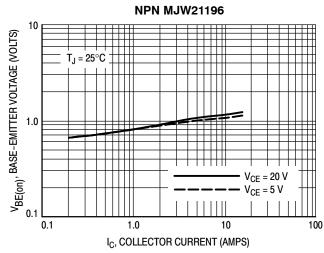


Figure 12. Typical Base-Emitter Voltage

There are two limitations on the power handling ability of a transistor; average junction temperature and secondary breakdown. Safe operating area curves indicate I<sub>C</sub> - V<sub>CE</sub> limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 13 is based on  $T_{J(pk)} = 150$ °C;  $T_C$  is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power than can be handled to values less than the limitations imposed by second breakdown.

#### **TYPICAL CHARACTERISTICS**

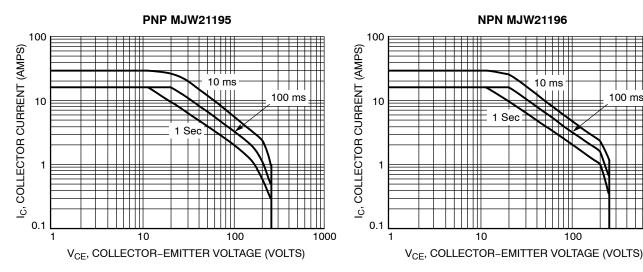


Figure 13. Active Region Safe Operating Area

Figure 14. Active Region Safe Operating Area

100

100 ms

1000

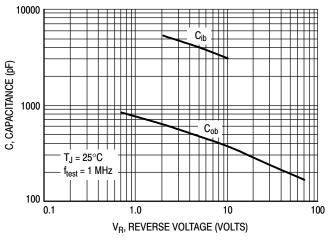


Figure 15. MJW21195 Typical Capacitance

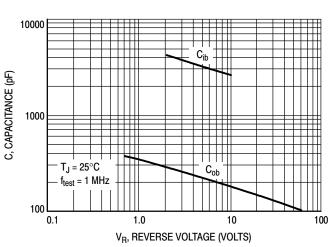


Figure 16. MJW21196 Typical Capacitance

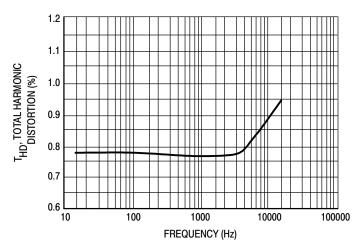


Figure 17. Typical Total Harmonic Distortion

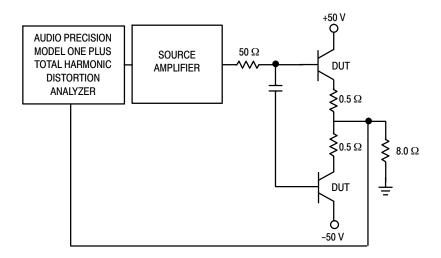
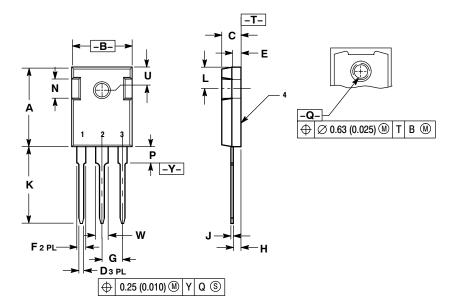


Figure 18. Total Harmonic Distortion Test Circuit

#### PACKAGE DIMENSIONS

TO-247 CASE 340L-02 ISSUE E



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	20.32	21.08	0.800	8.30	
В	15.75	16.26	0.620	0.640	
С	4.70	5.30	0.185	0.209	
D	1.00	1.40	0.040	0.055	
Ε	1.90	2.60	0.075	0.102	
F	1.65	2.13	0.065	0.084	
G	5.45 BSC		0.215 BSC		
Н	1.50	2.49	0.059	0.098	
J	0.40	0.80	0.016	0.031	
K	19.81	20.83	0.780	0.820	
L	5.40	6.20	0.212	0.244	
N	4.32	5.49	0.170	0.216	
P		4.50	-	0.177	
Q	3.55	3.65	0.140	0.144	
U	6.15	6.15 BSC 0.242 BSC		BSC	
W	2.87	3.12	0.113	0.123	

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