



60V SELF-PROTECTED LOW-SIDE INTELLIFET MOSFET SWITCH

Product Summary

- Continuous Drain Source Voltage: V_{DS} = 60V
- On-State Resistance: 500mΩ
- Max Nominal Load Current (V_{IN} = 5V): 1.1A
- Min Nominal Load Current (V_{IN} = 5V): 0.7A
- Clamping Energy: 550mJ

Description

The BSP75N is a self-protected, low-side MOSFET. It features monolithic overtemperature, overcurrent, overvoltage (active clamp), and ESD protected logic-level functionality. It is intended as a general purpose switch.

Applications

- Especially Suited for Loads With High Inrush Current, Such as Lamps and Motors
- All Types of Resistive, Inductive, and Capacitive Loads in Switching Applications
- µC Compatible Power Switch for 12V and 24V DC Applications
- Automotive Rated
- Replaces Electromechanical Relays and Discrete Circuits
- Linear Mode Capability—Current-Limiting Protection Circuitry is
 Designed to Deactivate at Low V_{DS} to not Compromise the Load
 Current During Normal Operation. Maximum DC Operating
 Current is Therefore Determined by Thermal Capability of the
 Package/Board Combination Rather Than by Protection
 Circuitry, Which Does not Compromise the Product's Ability to
 Self-Protect at Low V_{DS}.

Features and Benefits

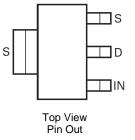
- Short-Circuit Protection With Auto Restart
- Overvoltage Protection (Active Clamp)
- Thermal Shutdown with Auto Restart
- Overcurrent Protection
- Input Protection (ESD)
- Load-Dump Protection (Actively Protects Load)
- Logic-Level Input
- High Continuous Current Rating
- Lead-Free Finish; RoHS Compliant (Note 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SOT223 (Type DN)
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish @3
- Weight: 0.112 grams (Approximate)







Ordering Information (Note 4)

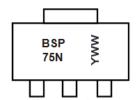
Product	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
BSP75NTA	BSP75N	7	12	1000 Units

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, see https://www.diodes.com/design/support/packaging/diodes-packaging/.
- 5. The tab is connected to the source pin and must be electrically isolated from the drain pin. Connection of significant copper to the drain pin is recommended for best thermal performance.

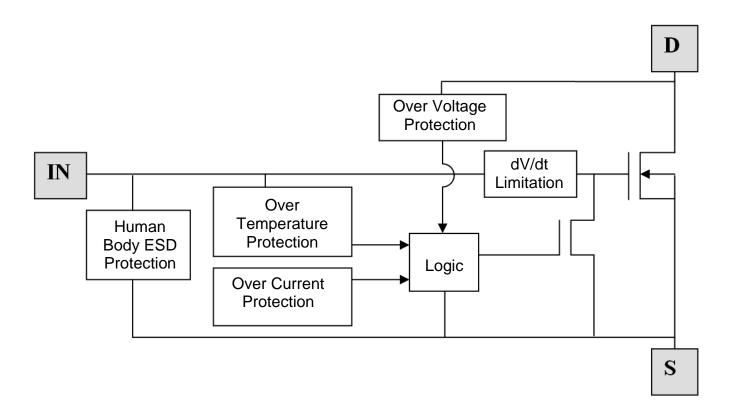


Marking Information



BSP75N = Product Type Marking Code YWW = Date Code Marking Y or \overline{Y} = Last Digit of Year (ex: 8 = 2018) WW or \overline{WW} = Week Code (01 to 53)

Functional Block Diagram





Absolute Maximum Ratings (@T_A = +25°C, unless otherwise stated.)

Parameter	Symbol	Limit	Unit
Continuous Drain-Source Voltage	V _{DS}	60	V
Drain-Source Voltage for Short Circuit Protection $V_{IN} = 5V$	V _{DS(SC)}	36	V
Drain-Source Voltage for Short Circuit Protection V _{IN} = 10V	V _{DS(SC)}	20	V
Continuous Input Voltage	V _{IN}	-0.2 to 10	V
Peak Input Voltage	V _{IN}	-0.2 to 20	V
Operating Temperature Range	TJ	-40 to +150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C
Power Dissipation at T _A = +25°C (Note 6)	P _D	1.5	W
Power Dissipation at T _A = +25°C (Note 8)	P _D	0.6	W
Continuous Drain Current @ V _{IN} =10V; T _A = +25°C (Note 6)	I _D	1.3	Α
Continuous Drain Current @ V _{IN} =5V; T _A = +25°C (Note 6)	I _D	1.1	Α
Continuous Drain Current @ V _{IN} =5V; T _A = +25°C (Note 8)	I _D	0.7	Α
Continuous Source Current (Body Diode) (Note 6)	Is	2.0	Α
Pulsed Source Current (Body Diode) (Note 7)	Is	3.3	Α
Unclamped Single Pulse Inductive Energy	Eas	550	mJ
Load Dump Protection	VLOAD_DUMP	80	V
Electrostatic Discharge (Human Body Model)	V _{ESD}	4000	V
DIN Humidity Category, DIN 40 040	_	E	_
IEC Climatic Category, DIN IEC 68-1	_	40/150/56	_

Thermal Resistance

Parameter	Symbol	Limit	Unit
Junction to Ambient (Note 6)	$R_{\Theta JA}$	83	°C/W
Junction to Ambient (Note 7)	$R_{\Theta JA}$	45	°C/W
Junction to Ambient (Note 8)	$R_{\Theta JA}$	208	°C/W

Notes:

^{6.} For a device surface mounted on 25mm × 25mm × 1.6mm FR-4 board with a high coverage of single-sided 2oz weight copper. Allocation of 6cm² copper 33% to source tab and 66% to drain pin with source tab and drain pin electrically isolated.

^{7.} For a device surface mounted on FR-4 board as (a) and measured at t < = 10s.

8. For a device surface mounted on FR-4 board with the minimum copper required for electrical connections.



Electrical Characteristics (@T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Min	Тур	Max	Unit	Conditions
Static Characteristics						
Drain-Source Clamp Voltage	V _{DS(AZ)}	60	70	75	V	I _D =10mA
Off State Drain Current	I _{DSS}	_	0.1	3	μΑ	V _{DS} =12V, V _{IN} =0V
Off State Drain Current	I _{DSS}	_	3	15	μΑ	V _{DS} =32V, V _{IN} =0V
Input Threshold Voltage (Note 9)	V _{IN(TH)}	1	2.1	_	V	V _{DS} =V _{GS} , I _D =1mA
Input Current	I _{IN}	_	0.7	1.2	mA	V _{IN} =5V
Input Current	I _{IN}	_	1.5	2.7	mA	V _{IN} =7V
Input Current	I _{IN}	_	4	7	mA	V _{IN} =10V
Static Drain-Source On-State Resistance	R _{DS(ON)}		520	675	mΩ	V _{IN} =5V, I _D =0.7A
Static Drain-Source On-State Resistance	R _{DS(ON)}	_	385	550	mΩ	V _{IN} =10V, I _D =0.7A
Current Limit (Note 10)	I _{D(LIM)}	0.7	1.0	1.5	Α	V _{IN} =5V, V _{DS} >5V
Current Limit (Note 10)	I _{D(LIM)}	1	1.8	2.3	Α	V _{IN} =10V, V _{DS} >5V
Dynamic Characteristics						
Turn-On Time (V _{IN} to 90% I _D)	t _{ON}	-	3		μs	$R_L=22\Omega$, $V_{IN}=0$ to 10V, $V_{DD}=12V$
Turn-Off time (V _{IN} to 90% I _D)	t _{OFF}	1	13	1	μs	R_L =22 Ω , V_{IN} =10V to 0V, V_{DD} =12V
Slew Rate On (70 to 50% V _{DD})	-dV _{DS} /dt _{ON}	_	8	_	V/µs	$R_L=22\Omega$, $V_{IN}=0$ to 10V, $V_{DD}=12V$
Slew Rate Off (50 to 70% V _{DD})	dV _{DS} /dt _{ON}	_	3.2	_	V/µs	$R_L=22\Omega$, $V_{IN}=10V$ to $0V$, $V_{DD}=12V$
Protection Functions (Note 11)						
Minimum Input Voltage for Over Temperature Protection	V _{PROT}	4.5	_	_	V	_
Thermal Overload Trip Temperature	T _{JT}	+150	+175	_	°C	_
Thermal Hysteresis	_	_	+1	_	°C	_
Unclamped Single Pulse Inductive Energy T _J = +25°C	E _{AS}	550	_	_	mJ	I _{D(ISO)} =0.7A, V _{DD} =32V
Unclamped Single Pulse Inductive Energy T _J = +150°C	E _{AS}	200	_	_	mJ	I _{D(ISO)} =0.7A, V _{DD} =32V
Inverse Diode						
Source Drain Voltage	V_{SD}	_	_	1	V	V _{IN} =0V, -I _D =1.4A

Notes:

^{9.} Protection features may operate outside spec for V_{IN} < 4.5V.

^{10.} The drain current is limited to a reduced value when V_{DS} exceeds a safe level.
11. Integrated protection functions are designed to prevent IC destruction under fault conditions described in the datasheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous, repetitive operation.



Application Information

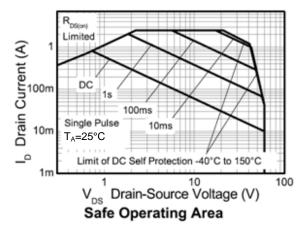
The current-limit protection circuitry is designed to deactivate at low V_{DS} to prevent the load current from being unnecessarily restricted during normal operation. The design max DC operating current is therefore determined by the thermal capability of the package/board combination rather than by the protection circuitry (see Typical Output Characteristics graphs). This does not compromise the products ability to self-protect at low VDS.

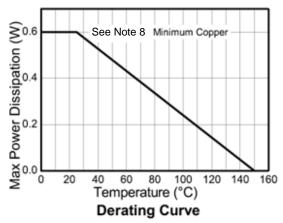
The overtemperature protection circuit trips at a minimum of +150°C, so the available package dissipation reduces as the maximum required ambient temperature increases. This leads to the following maximum recommended continuous operating currents.

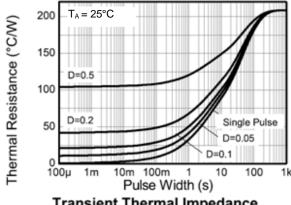
Minimum Copper Area Characteristics

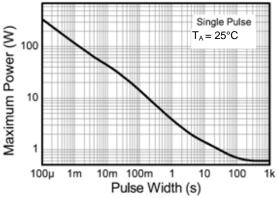
For minimum copper condition as described in Note 8.

Max Ambient Temperature T _A	Maximum Continuous Current			
_	V _{IN} = 5V	V _{IN} = 10V		
+25°C at V _{IN} = 5V	720mA	840mA		
+70°C at V _{IN} = 5V	575mA	670mA		
+85°C at V _{IN} = 5V	520mA	605mA		
+125°C at V _{IN} = 5V	320mA	375mA		









Transient Thermal Impedance

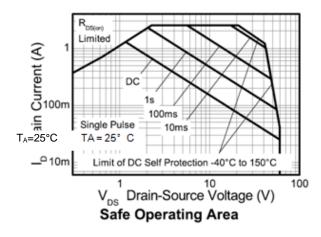
Pulse Power Dissipation

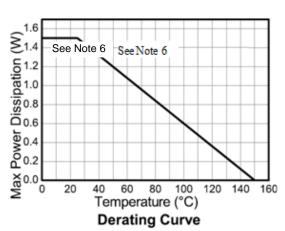


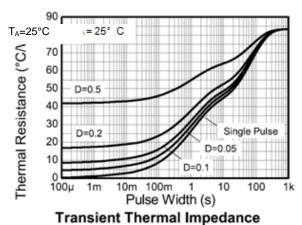
Large Copper Area Characteristics

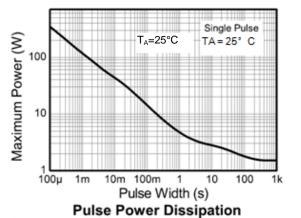
For large copper area as described in Note 6.

Max Ambient Temperature T _A	Maximum Continuous Current /mA	
_	V _{IN} = 5V	V _{IN} = 10V
+25°C at V _{IN} = 5V	1140mA	1325mA
+70°C at V _{IN} = 5V	915mA	1060mA
+85°C at V _{IN} = 5V	825mA	955mA
+125°C at V _{IN} = 5V	510mA	590mA



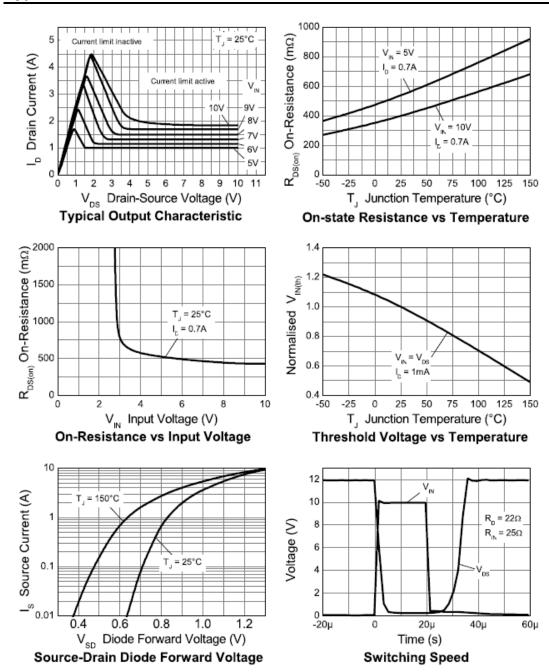








Typical Characteristics

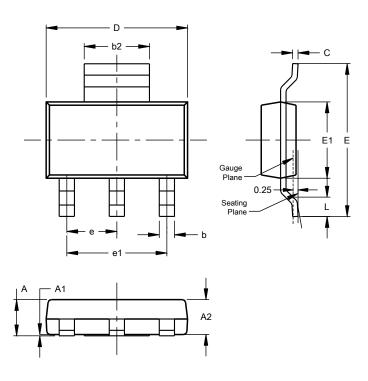




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)

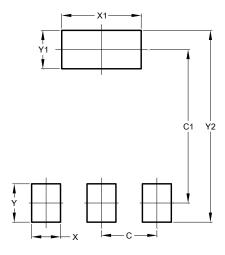


SOT223 (Type DN)				
Dim	Min	Max	Тур	
Α		1.70	-	
A1	0.01	0.15		
A2	1.50	1.68	1.60	
b	0.60	0.80	0.70	
b2	2.90	3.10		
С	0.20	0.32		
D	6.30	6.70	-	
Е	6.70	7.30		
E1	3.30	3.70		
е			2.30	
e1			4.60	
L	0.85			
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)	
С	2.30	
C1	6.40	
Х	1.20	
X1	3.30	
Υ	1.60	
Y1	1.60	
Y2	8.00	



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