



### 60V SOT223 N-channel enhancement mode MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	
60V	0.08 @ V <sub>GS</sub> = 10V	5.3	
	0.15 @ V <sub>GS</sub> = 4.5V	2.8	

### **Description and Applications**

This MOSFET features a unique structure combining the benefits of low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

- DC-DC Converters
- · Power Management Functions
- Disconnect Switches
- Motor Control

### **Features and Benefits**

- Low On-Resistance
- · Fast Switching Speed
- · Low Threshold
- Low Gate Drive
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

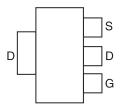
### **Mechanical Data**

- Case: SOT223
- Case Material: Molded Plastic.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.112 grams (Approximate)

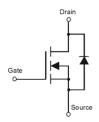




Top View



Pin Out - Top View



**Equivalent Circuit** 

### **Ordering Information** (Note 4)

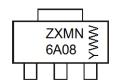
Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN6A08GTA	ZXMN6A08	7	12	1,000
ZXMN6A08GTC	ZXMN6A08	13	12	4,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**

SOT223



ZXMN6A08 =Product Type Marking Code YWW = Date Code Marking Y or Y = Last Digit of Year (ex: 5 = 2015) WW or WW = Week Code (01 - 53)



### **Absolute Maximum Ratings**

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V <sub>DSS</sub>	60	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
	T <sub>A</sub> = +25 °C (Note 6)		5.3	Α
Continuous Drain Current V <sub>GS</sub> = 10V	T <sub>A</sub> = +70 °C (Note 6)	I <sub>D</sub>	4.2	Α
	T <sub>A</sub> = +25 °C (Note 5)		3.8	Α
Pulsed Drain Current (Note 7)		I <sub>DM</sub>	20	Α
Continuous Source Current (body diode)( Note 6)		I <sub>S</sub>	2.1	Α
Pulsed Source Current (body diode)( Note 7)		I <sub>SM</sub>	20	Α
Power Dissipation at T <sub>A</sub> = +25 °C (Note 5) Linear Derating Factor		P <sub>D</sub>	2 16	W mW/℃
Power Dissipation at $T_A = +25$ °C (Note 6) Linear Derating Factor		P <sub>D</sub>	3.9 31	W mW/℃
Linear Derating Factor		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	.€

## Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Junction to Ambient (Note 5)	$R_{\theta JA}$	62.5	°C/W
Junction to Ambient (Note 6)	$R_{\theta JA}$	32	°C/W

## **Electrical Characteristics** (@T<sub>A</sub> = +25 °C, unless otherwise specified.)

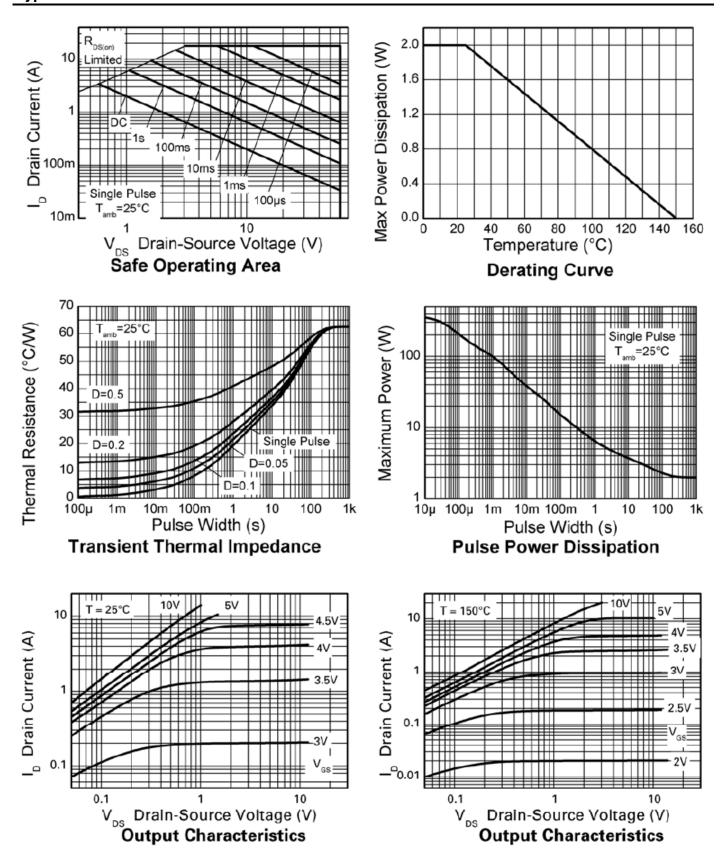
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	-	0.5	μΑ	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	ı	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1	-	-	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-State Resistance (Note 8)		-	1	0.08	Ω	$V_{GS} = 10V, I_D = 4.8A$	
Static Dialit-Source Oil-State Nesistance (Note 8)	R <sub>DS</sub> (ON)	1	1	0.15	Ω	$V_{GS} = 4.5V, I_D = 4.2A$	
Forward Transconductance (Notes 8 &10)	g <sub>fs</sub>	_	6.6	Ī	S	$V_{DS} = 15V, I_D = 4.8A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C <sub>iss</sub>	_	459	_	pF		
Output Capacitance	Coss	_	44.2	-	pF	$V_{DS} = 40V, V_{GS} = 0V,$ - f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	24.1	-	pF	T = TIVIHZ	
Turn-On Delay Time (Note 9)	t <sub>D(on)</sub>	_	2.6	_	ns		
Turn-On Rise Time (Note 9)	t <sub>r</sub>	-	2.1	_	ns	$V_{DD} = 30V, I_{D}=1.5A$	
Turn-Off Delay Time (Note 9)	t <sub>D(off)</sub>	_	12.3	_	ns	$RG \cong 6.0\Omega$ , $V_{GS}=10V$	
Turn-Off Fall Time (Note 9)	t <sub>f</sub>	_	4.6	1	ns		
Gate Charge (Note 9)	Qg	1	4.0	1	nC	$V_{DS}$ = 30V, $V_{GS}$ = 5V $I_{D}$ = 1.4A	
Total Gate Charge (Note 9)	Qg	_	5.8	_	nC	V 20V V 10V	
Gate-Source Charge (Note 9)	Qgs	_	1.4	-	nC	$V_{DS} = 30V, V_{GS} = 10V$	
Gate Drain Charge (Note 9)	Qgd	_	1.9	-	nC	I <sub>D</sub> = 1.4A	
SOURCE-DRAIN DIODE							
Diode Forward Voltage (Note 8)	V <sub>SD</sub>	1	0.88	1.2	V	Tj=+25°C, I <sub>S</sub> = 4A, V <sub>GS</sub> =0V	
Reverse Recovery Time (Note 10)	trr	_	19.2	-	ns	Tj=+25℃, I <sub>S</sub> = 1.4A,	
Reverse Recovery Charge (Note 10)	Qrr	_	30.3	_	nC	di/dt=100A/μs	

5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
6. For a device surface mounted on FR4 PCB measured at t <= 10 sec.
7. Repetitive rating - 25mm x 25mm FR4 PCB, D=0.02, pulse width 300\_s - pulse width limited by maximum junction temperature.
8. Measured under pulsed conditions. Pulse width <= 300\_s; duty cycle <=2%. Notes:

- 9. Switching characteristics are independent of operating junction temperature.
- 10. For design aid only, not subject to production testing.

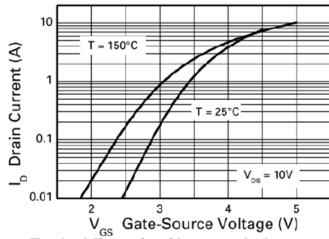


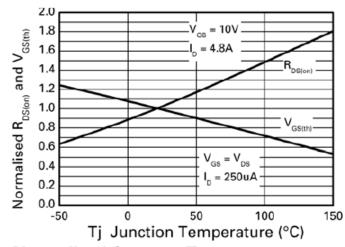
### **Typical Characteristics**





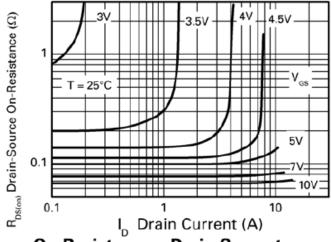
## Typical Characteristics (continued)

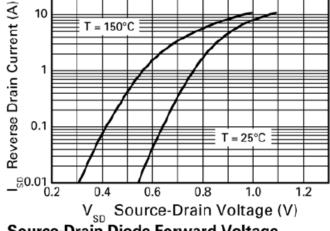




**Typical Transfer Characteristics** 

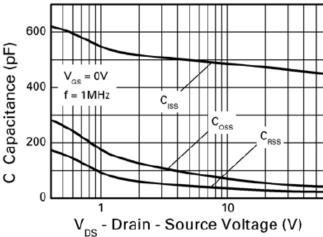


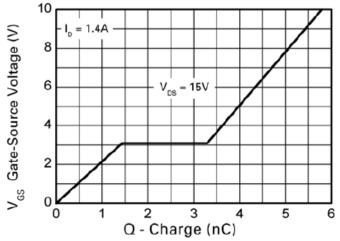




**On-Resistance v Drain Current** 

Source-Drain Diode Forward Voltage

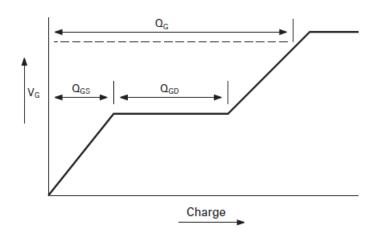




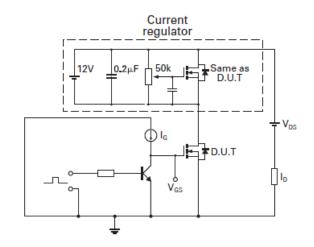
Gate-Source Voltage v Gate Charge Capacitance v Drain-Source Voltage



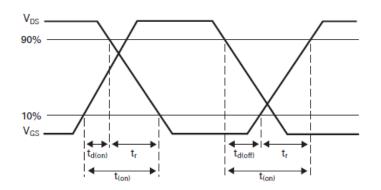
### **Test Circuits**



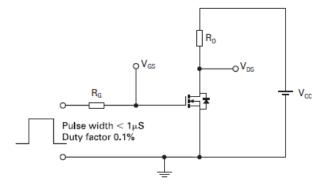
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

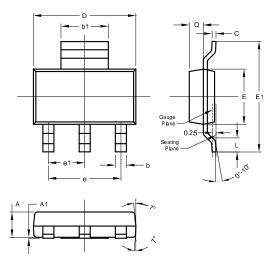


Switching time test circuit



### **Package Outline Dimensions**

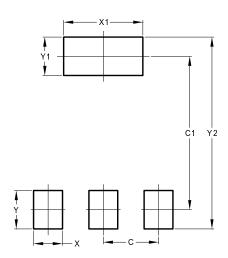
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
<b>A</b> 1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
E	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Υ	1.60
Y1	1.60
Y2	8.00

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