

PI90LV044, PI90LVB044

LVDS Dual 2x2 Crosspoint/Repeater Switch

Features

- Dual 2x2 Crosspoint/Repeater Switch
- Meets or Exceeds the Requirements of ANSI TIA/EIA-644-1995
- Designed for Signaling Rates up to 650 Mbit/s (325Mhz)
- Operates from a single 3.3V Supply: -40°C to 85°C
- Low-Voltage Differential Signaling with Output Voltages of ±350mV into:
 - 100Ω load (PI90LV044)
 - 50Ω load Bus LVDS Signaling (PI90LVB044)
- Accepts ±350mV differential inputs
- Wide common mode input range: 0.2V to 2.7V
- Output drivers are high impedance when disabled or when $V_{CC} \leq 1.5 V$
- Inputs are open, short, and terminated fail safe
- Propagation Delay Time: 3.5ns
- ESD protection is 10kV on bus pins
- Bus Pins are High Impedance when disabled or with V_{CC} less than 1.5V
- TTL Inputs are 5V Tolerant
- Power Dissipation at 400 Mbit/s of 250mW
- Packaging (Pb-free & Green available):
 28-pin TSSOP (L)

Description

The PI90LV044 and PI90LVB044 are monolithic dual 2x2 asynchronous crosspoint/repeater switches. The crosspoint function is based on a multiplexer tree architecture. Each 2x2 switch can be considered as a pair of 2:1 multiplexers that share the same inputs. The signal path through each switch is fully differential with minimal propagation delay. The signal path is unregistered, so no clock is required for the data inputs. The signal line drivers and receivers use Low Voltage Differential Signaling (LVDS) to achieve signaling rates as high as 650Mbps.

The LVDS standard provides a minimum differential output voltage magnitude of 247 mV into a 100 Ω load and receipt of 100 mV signals with up to 1V of ground potential difference between a transmitter and receiver. The PI90LVB044 doubles the output drive current to achieve LVDS levels with a 50 ohm load.

The intended application of these devices is for loop-through and redundant channel switching for both point-to-point baseband (PI90LV044) and multipoint (PI90LVB044) data transmissions over controlled impedance media.

Pin Configuration







MUX Truth Table

Input		Out	Eurotion	
S3, S1	S2, S0	1Y/1Z - 3Y/3Z	2T/2Z - 4Y/4Z	Function
0	0	1A/1B - 3A/3B	1A/1B - 3A/3B	Splitter
0	1	2A/2B - 4A/4B	2A/2B - 4A/4B	Splitter
1	0	1A/1B - 3A/3B	1A/1B - 3A/3B	Router
1	1	2A/2B - 4A/4B	2A/2B - 4A/4B	Router

Note:

1. Setting *n*DE to 0 will set Ouput nY/nZ to High Impedance.



Figure 1. Possible Signal Routing



Absolute Maximum Ratings Over Operating Free-Air Temperature⁽¹⁾

Supply Voltage Range, V _{CC} ⁽¹⁾
Voltage Range (DE, S0, S1)0.5 to 6V
Input Voltage Range, VI (A or B)–0.5V to V _{CC} + 0.5V
Electrostatic Discharge: A, B, Y, Z, and GND ⁽²⁾ Class 3, A: 16kV, B:600V
All PinsClass 3, A: 7kV, B:500V
Storage Temperature Range65°C to 150°C
Lead Temperature 1, 6 mm (1/16 inch) from case for 10 seconds

Notes:

- 1. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to Absolute-Maximum-Rated conditions for extended periods may affect device reliability.
- 2. All voltage values, except differential I/O bus voltages, are with respect to ground terminal.
- 3. Tested in accordance with MIL-STD-883C Method 3015.7

Recommended Operating Conditions

		Min.	Nom.	Max.	Units
Supply Voltage, V _{CC}	3.0	3.3	3.6	V	
High-Level input voltage, V _{IH}	S1 - S3, 1DE - 4DE	2			
Low-Level input voltage, V _{IL}				0.8	
Magnitude of Differential Input Voltage	0.1		0.6		
Common Mode input voltage, V _{IC} (see figure 2)		$\frac{ V_{ID} }{2}$		$2.4 - \frac{ V_{ID} }{2}$	
				V _{CC} - 0.8	
Operating free-air Temperature, T _A	-40		85	°C	

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Figure 2. Common-Mode Input Voltage vs. Differential Voltage

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Units
V _{ITH+}	Positive going differential input voltage threshold	VCM = 1.2V			100	mV
V _{ITH} -	Negative going differential input voltage threshold	v C I v I = 1.2 v	-100			
I.	Input ourroat (A or D inputo)	VI = 0V	-2		-20	
II	Input current (A or B inputs)	VI = 2.4V	-1.2			μA
I _{I (OFF)}	Power-off input current (A or B inputs)	VCC = 0V			20	

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Symbol	Parameter		Test Condition	Min.	Typ. ⁽¹⁾	Max.	Units
V _{OD}	Differential Output voltage magnitude Change in differential Output voltage magnitude between logic states			247	440	590	
ΔV_{OD}			-	-50		50	mV
V _{OC(SS)}	Steady-state common-mode ou	tput voltage	$R_{L} = 100\Omega (LV)$ $R_{L} = 50\Omega (LVB)$	1.062		1.375	V
$\Delta V_{OC(SS)}$	Change in steady-state common-mode output voltage between logic states			-50	3	50	mV
V _{OC(PP)}	Peak-to-peak common-mode of	utput voltage				150	
			No load		16	24	
т	Summer Current	$R_L = 100\Omega (LV)$		26	40]	
I _{CC}	Supply Current		$R_{\rm L} = 50\Omega (\rm LVB)$		42	54	mA
		All Channels Disabled		6	12	1	
T	III - 1 - 1 1	DE	V. – 5			40	nA
I _{IH}	High level input current	S1, S2, S3, S4	$V_{\rm IH} = 5$			-3	μA
т	T	DE	$V_{IL} = 0.8V$			-20	nA
I _{IL}	Low level input current	S1, S2, S3, S4				10	μA
I _{OS}	Short circuit output current		V_{OY} or $V_{OZ} = 0V$, $V_{OD} = 0V$			-10	mA
I _{OZ} High impedence output current			$V_{OD} = 600 \text{mV}$		1.5	±25	
			V _O - 0V or V _{CC}		1.5	±25	nA
I _{O(OFF)}	Power off output current		$V_{\rm CC} = 0V, V_{\rm O} = 3.6V$		1.5	±40	1
					3		
C _{IN}	Input Capacitance		S0 - S3, 1DE - 4DE		8		pF

Note:

1. All typical values are at 25°C and with a 3.3 supply

Differential Receiver to Driver Switching Characteristics Over Recommended Operating Conditions (unless otherwise noted)

Symbol	Paran	Test Condition	Min.	Typ. ⁽¹⁾	Max.	Units	
t _{PLH}	Differential propagation delay, low-to-high				4.0	6.0	
t _{PHL}	Differential propagation delay, l	nigh-to-low]		4.0	6.0	
t _{sk(p)}	Pulse skew ($ t_{PHL} = t_{PLH} $)]		0.25	0.3	
4	Transition low to high	LV044	$C_L = 10 pF$		1.0	1.5	
t _r	Transition, low-to-high	LVB044			0.8	1.3	
	Transition, high-to-low	LV044	-		1.0	1.5	ns
t _f		LVB044			0.8	1.3	
t _{PHZ}	Propagation delay time, high-level-to-high-impedence output				4.0	10	
t _{PLZ}	Propagation delay time, low-level-to-high-impedance output				4.3	10	
t _{PZH}	Propagation delay time, high-impedence to high-level output				3.0	10	
t _{PZL}	Propagation delay time, high-impdeence tolow-level output				2.0	10	
t _{PHL} _R1_Dx					-	95	
t _{PLH} _R1_Dx				-	95		
t _{PHL} R2_Dx	Channel-tochannel skew, receiver to driver ⁽²⁾				-	95	ps
t _{PLH} _R2_Dx]			-	95		

Notes:

1. All typical values are at 25°C and with a 3.3 supply

2. These parametric values are measured over supply voltage and temperature ranges recommended for the device



Parameter Measurement Information



Figure 3. Test Circuit and Voltage Definitions for the Differential Output Signal



Figure 4. Test Circuit and Definitions for the Driver Common-Mode Output Voltage





Figure 5. Differential Receiver to Driver Propagation Delay and Driver Transition Time Waveforms





Figure 6. Enable and Disable Timing Circuit



Typical Characteristics





Packaging Mechanical: 28-Pin TSSOP (L)



Note:

· For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php

Ordering Information

Ordering Code	Package Code	Package Type
PI90LV044LE	L	Pb-free & Green, 28-pin 170-mil TSSOP
PI90LVB044LE	L	Pb-free & Green, 28-pin 170-mil TSSOP

Notes:

• Thermal characteristics can be found on the company web site at www.pericom.com/packaging/

- E = Pb-free and Green
- Adding X suffix = Tape/Reel

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