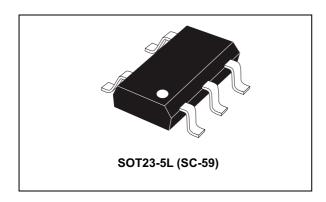


### Low capacitance TVS for high speed lines such as xDSL

Datasheet - production data



#### **Features**

- High surge capability to comply with GR-1089 and ITU-T K20/21
- Keeps its peak power capability up to T<sub>i</sub> max
- Stand-off voltage: 5 V, 8 V, 10 V
- Low capacitance device: C<sub>tvp</sub> = 3 pF
- RoHS package
- Low leakage current: 0.5 µA at 25 °C

#### Complies with the following standards

- Telcordia GR-1089
  - 2.5 kV 2/10  $\mu s$  500 A 2/10  $\mu s$
  - AC power fault tests
- ITU-T K20/21/45
  - 6 kV 10/700 μs 150 A 5/310 μs
  - power induction tests
  - power contact tests
- IEC 61000-4-2, level 4
  - 15 kV (air discharge)
  - 8 kV (contact discharge
- IEC 61000-4-5, level 3
  - ±2 kV, 42  $\Omega$
- MIL STD 883G-Method 3015-7: Class 3
  - 8 kV (human body model)

#### **Description**

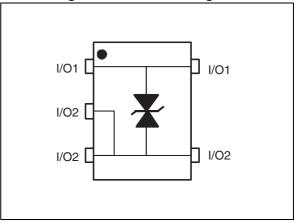
DSL02 is designed to protect DSL line drivers against surges defined in worldwide telecommunication standards. This device protects line drivers of various systems such as ADSL and VDSL. The low capacitance makes it suitable from ADSL to VDSL data rates.

DSL02 is able to survive severe conditions even when used with downgraded or oscillating gas tube.

DSL02 is also suitable to be used on other lines when IEC61000-4-5 surge capability is required.

DSL02 is packaged in a SOT23-5L.

Figure 1. Functional diagram



Characteristics DSL02

#### 1 Characteristics

Table 1. Absolute ratings (T<sub>amb</sub> = 25 °C)

Symbol		Value	Unit	
V <sub>PP</sub>	Peak pulse voltage	IEC 61000-4-2, contact discharge	30	kV
I <sub>pp</sub>	Peak pulse current	8/20 µs	30	Α
T <sub>stg</sub> T <sub>j</sub>	Storage temperature range Operating junction temperat	-55 to 150 -40 to 125	ů Ĉ	
TL	Maximum temperature for s	260	°C	

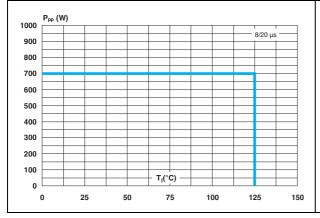
Table 2. Electrical characteristics (T<sub>amb</sub> = 25 °C)

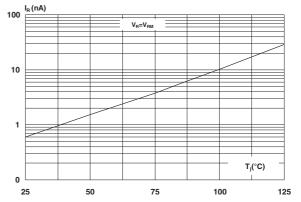
Order code	I <sub>RM</sub> @	I <sub>RM</sub> @ V <sub>RM</sub> V <sub>E</sub>		V <sub>BR</sub> @ I <sub>BR</sub>		V <sub>CL</sub> @ I <sub>PP</sub> (8/20 μs)		С	
Order code	Max. μA	v	Min. V	mA	Max. V	A	Тур. pF <sup>(1)</sup>	Max. pF <sup>(1)</sup>	Typ. pF <sup>(2)</sup>
DSL02-005SC5	0.5	5	6	1	18	24	3	5	0.3
DSL02-008SC5	0.5	8	10	1	22	24	3	5	0.3
DSL02-010SC5	0.5	10	11	1	24	24	3	5	0.3

<sup>1.</sup> Test conditions:  $V_R = 2 \text{ V bias}$ ,  $V_{RMS} = 1 \text{ V}$ , F = 1 MHz

Figure 2. Peak pulse power dissipation versus initial junction temperature (typical values, 8/20µs)

Figure 3. Leakage current versus junction temperature (typical values)



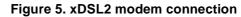


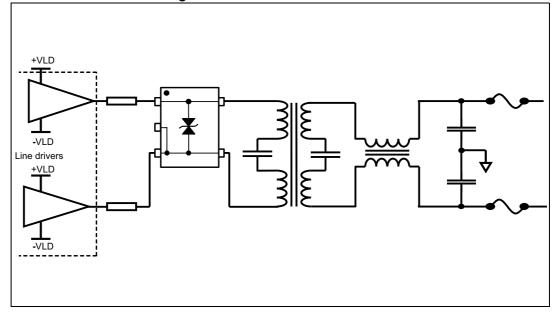
<sup>2.</sup> Measured between 1 V and  $V_{RM}$ 

DSL02 Characteristics

C (pF) 3.0 For lower voltages, maximum voltage applied is limited to V  $_{\rm RM}$ F=1 MHz  $V_{osc} = 1V_{RMS}$ T<sub>j</sub>=25 °C 2.5 2.0 1.5 1.0 0.5  $V_R(V)$ 0.0 2 7 3 4 5 6 8

Figure 4. Junction capacitance versus reverse voltage applied (typical values)





Package information DSL02

## 2 Package information

- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

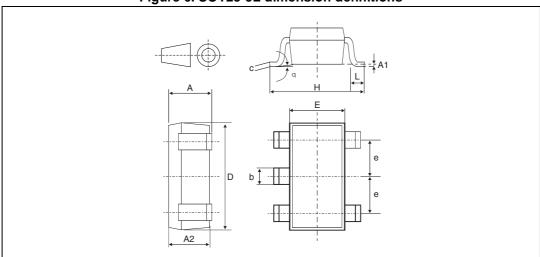


Figure 6. SOT23-5L dimension definitions

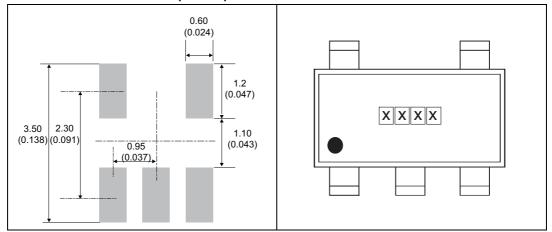
Table 3. SOT23-5L dimension values

	Dimensions								
Ref.	Ref. Millimeters				Inches				
	Min.	Тур.	Max.	Min.	Тур.	Max.			
Α	0.90		1.45	0.035		0.057			
A1	0		0.15	0		0.006			
A2	0.90		1.30	0.035		0.051			
b	0.30		0.50	0.012		0.020			
С	0.09		0.20	0.004		0.008			
D	2.80		3.05	0.11		0.118			
Е	1.50		1.75	0.059		0.069			
е		0.95			0.037				
Н	2.60		3.00	0.102		0.118			
L	0.30		0.60	0.012		0.024			
М	0°		10°	0°		10°			

DSL02 Package information

Figure 7. Footprint recommendations dimensions in mm (inches)

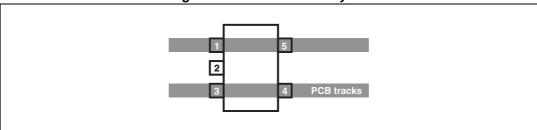
Figure 8. Marking layout



PCB recommendation DSL02

## 3 PCB recommendation

Figure 9. Recommended layout



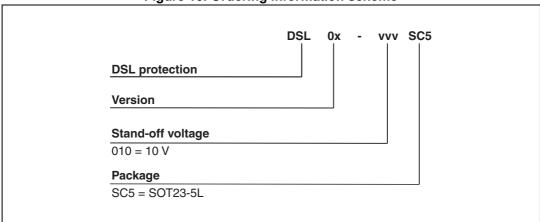
Concerning Figure 9:

- Pins 1 and 5 must be connected together.
- Pins 3 and 4 must be connected together.
- Pin 2 must not be connected

DSL02 Ordering information

# 4 Ordering information

Figure 10. Ordering information scheme



**Table 4. Ordering information** 

Order code	Marking	Package	Weight	Base qty	Delivery mode
DSL02-005SC5	YT05				
DSL02-008SC5	YT08	SOT23-5L	16 mg	3000	Tape and reel
DSL02-010SC5	YT10				

## 5 Revision history

**Table 5. Document revision history** 

Date	Revision	Changes	
30-Oct-2013	1	Initial release.	
03-Feb-2015	2	Updated Features and Description. Added Figure 5.	

#### IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2015 STMicroelectronics – All rights reserved



# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

### STMicroelectronics:

DSL02-005SC5 DSL02-008SC5 DSL02-010SC5