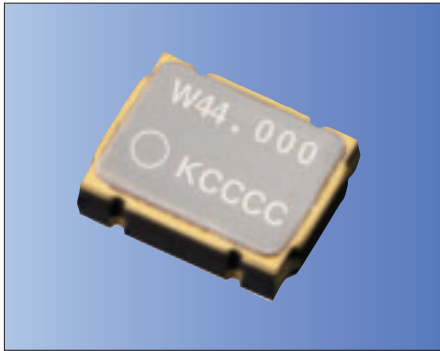


CMOS/ 3.3V/ 3.2×2.5mm



RoHS Compliant

**Features**

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage  $V_{CC} = 3.3V$
- $\pm 25 \times 10^{-6}$  available

**Table 1**

Freq. Tol. Code	Tolerance $\times 10^{-6}$	Operating Temperature Range (°C)	Note
0	$\pm 50$	-10 to +70	Standard specifications
S	$\pm 30$		
U	$\pm 25$		
F	$\pm 100$	-40 to +85	Please contact us for available frequencies.
G	$\pm 50$		
6	$\pm 50$	-40 to +105	

**How to Order**

**KC3225A 25.0000 C 3 □ E 00**  
 ① ② ③ ④ ⑤ ⑥ ⑦

- ① Series
- ② Output Frequency
- ③ Output Type (CMOS)
- ④ Supply Voltage (3.3V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ INH Function (45/ 55%, Stand-by)
- ⑦ Individual Specification (STD Specification is "00")

Packaging (Tape & Reel 2000 pcs./ reel)

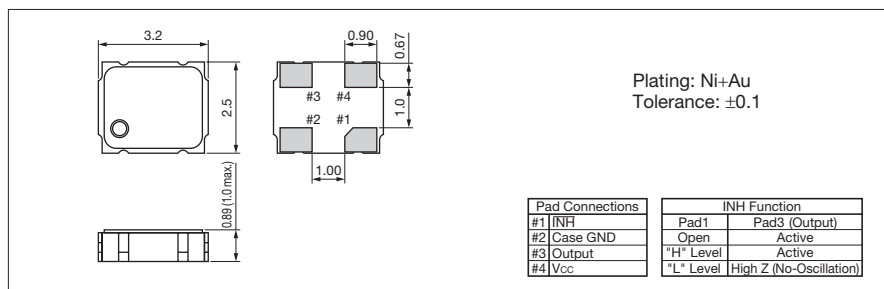
**Specifications**

Item	Symbol	Conditions	Min.	Max.	Units	
Output Frequency Range	$f_o$		1.5	125	MHz	
Frequency Tolerance	$f_{tol}$	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	Op. Temp.: -40 to +85°C	-100	+100	$\times 10^{-6}$
			Op. Temp.: -10 to +70°C/ -40 to +85°C/ -40 to +105°C	-50	+50	
			Op. Temp.: -10 to +70°C	-30	+30	
			Op. Temp.: -10 to +70°C	-25	+25	
Storage Temperature Range	$T_{stg}$		-55	+125	°C	
Operating Temperature Range	$T_{use}$	Standard Specifications	-10	+70	°C	
		Extend (Option)	-40	+85		
Max. Supply Voltage	—		-40	+105		
Supply Voltage	$V_{CC}$	Freq. Tol.Code: 0, S, F	+2.97	+3.63	V	
		Freq. Tol.Code: U, G, 6	+3.14	+3.46	V	
Current Consumption (Maximum Loaded)	$I_{CC}$	$1.5 \leq f_o \leq 26MHz$	—	6	mA	
		$26 < f_o \leq 50MHz$	—	8		
		$50 < f_o \leq 67.5MHz$	—	12		
		$67.5 < f_o \leq 95MHz$	—	20		
		$95 < f_o \leq 125MHz$	—	25		
Stand-by Current	$I_{std}$		—	10	$\mu A$	
Symmetry	SYM	@50% $V_{CC}$	45	55	%	
Rise/ Fall Time (10% $V_{CC}$ to 90% $V_{CC}$ Maximum Loaded)	$t_r / t_f$	$1.5 \leq f_o \leq 67.5MHz$	—	5	ns	
		$67.5 < f_o \leq 125MHz$	—	3		
Low Level Output Voltage	$V_{OL}$	$I_{OL} = 4mA$	—	10% $V_{CC}$	V	
High Level Output Voltage	$V_{OH}$	$I_{OH} = -4mA$	90% $V_{CC}$	—	V	
CMOS Load	$L_{CMOS}$	CMOS Output	—	15	pF	
Input Voltage Range	$V_{IN}$		0	$V_{CC}$	V	
Low Level Input Voltage	$V_{IL}$		—	30% $V_{CC}$	V	
High Level Input Voltage	$V_{IH}$		70% $V_{CC}$	—	V	
Disable Time	$t_{dis}$		—	150	ns	
Enable Time	$t_{ena}$		—	5	ms	
Start-up Time	$t_{str}$	@Minimum operating voltage to be 0 sec.	—	10	ms	
1 Sigma Jitter	$J_{Sigma}$	Measured with Wavecrest SIA-3000	$1.5 \leq f_o \leq 60MHz$	—	8	ps
			$60 < f_o \leq 125MHz$	—	5	
Peak to Peak Jitter	$J_{PK-PK}$	Measured with Wavecrest SIA-3000	$1.5 \leq f_o \leq 60MHz$	—	80	ps
			$60 < f_o \leq 125MHz$	—	40	

Note: All electrical characteristics are defined at the maximum load and operating temperature range. Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

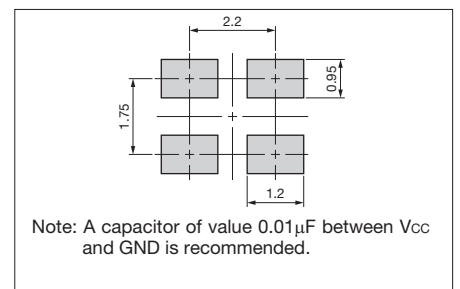
**Dimensions**

(Unit: mm)



**Recommended Land Pattern**

(Unit: mm)



# Mouser Electronics

Authorized Distributor

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