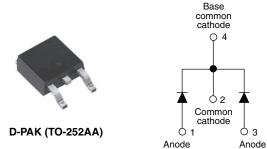


COMPLIANT

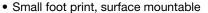
High Performance Schottky Rectifier, 2 x 6 A



PRODUCT SUMMARY						
Package	D-PAK (TO-252AA)					
I _{F(AV)}	2 x 6 A					
V_R	40 V					
V _F at I _F	0.48 V					
I _{RM}	40 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	9 mJ					

FEATURES

- Popular D-PAK outline
- Center tap configuration



- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-12CWQ04FNPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	12	A						
V _{RRM}		40	V						
I _{FSM}	t _p = 5 µs sine	550	А						
V _F	6 A _{pk} , T _J = 125 °C (per leg)	0.48	V						
TJ	Range	-55 to +150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-12CWQ04FNPbF	UNITS					
Maximum DC reverse voltage	V_{R}	40						
Maximum working peak reverse voltage	V _{RWM}	40	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDI	TEST CONDITIONS				
Maximum average per leg			50 % duty avalo at T ₂ = 134 °C	6	А			
See fig. 5	per device	$I_{F(AV)}$ 50 % duty cycle at T_C = 134 °C, rectangular waveform		, rectangular wavelonn	12	A		
Maximum peak one cycle non-repetitive surge current See fig. 7		5 μs sine or 3 μs rect. pulse Following any rated load condition and with		550	Α			
		I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	90			
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1.5 A, L = 8 mH		9	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.2	Α		



ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		6 A	- T _{.1} = 25 °C	0.53	V			
Maximum forward	V (1)	12 A	11 = 23 0	0.68				
voltage drop per leg See fig. 1	_	6 A	- T _{.I} = 125 °C	0.48				
G		12 A	- 1j = 125 C	0.64				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3	- mA			
See fig. 2		T _J = 125 °C	V _R = nateu v _R	40				
Threshold voltage	V _{F(TO)}	T. – T. movimum		0.28	V			
Forward slope resistance	r _t	$T_J = T_J$ maximum		25.58	mΩ			
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal ran	405	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 r	5.0	nH				

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and srorage temperature range		T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C			
Maximum thermal resistance,	per leg	D	DC operation	3.0	°C/W			
junction to case	per device	R_{thJC}	See fig. 4	1.5	C/ VV			
Approximate weight				0.3	g			
				0.01	OZ.			
Marking device			Case style D-PAK (similar to TO-252AA)	12CW(Q04FN			

Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

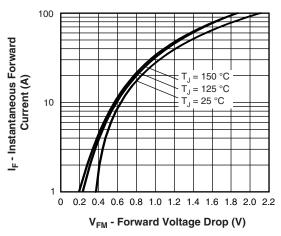


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

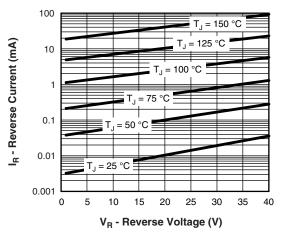


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

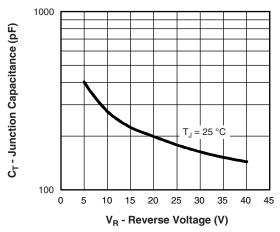


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

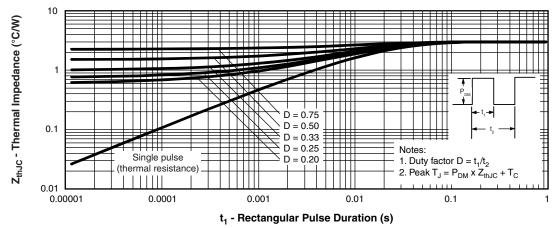


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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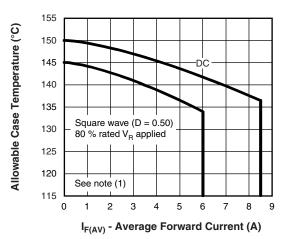


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

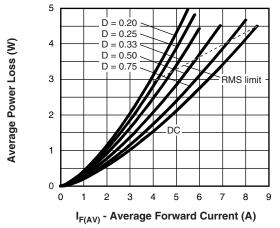


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

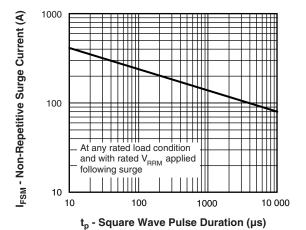


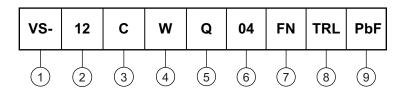
Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (12 A)

3 - Center tap configuration

Package identifier:

W = D-PAK

5 - Schottky "Q" series

6 - Voltage rating (04 = 40 V)

7 - FN = TO-252AA

None = tube (50 pieces)

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

9 - PbF = lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95059					
Packaging information	www.vishay.com/doc?95033					



NOTES

3

2

MAX.

0.410

0.070

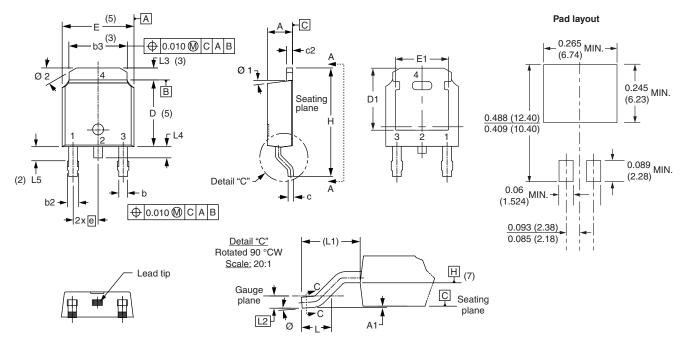
0.050

0.040

0.060

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



Ī	SYMBOL	MILLIM	METERS	INCHES		NOTES	CVMDOL	MILLIMETERS		INCHES		
		MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX
ſ	Α	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC
ſ	A1	-	0.13		0.005			Н	9.40	10.41	0.370	0.41
Ī	b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.07
Ī	b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.
ſ	b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC
Ī	С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.05
Ī	c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.04
ſ	D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.06
Ī	D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°
ſ	Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°
Ī	E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



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