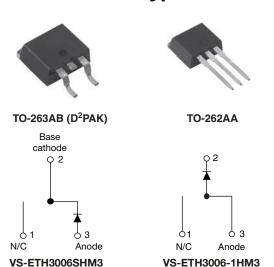


VS-ETH3006SHM3, VS-ETH3006-1HM3

Vishay Semiconductors

Hyperfast Rectifier, 30 A FRED Pt®



PRODUCT SUMMARY				
Package	TO-263AB (D ² PAK), TO-262AA			
I _{F(AV)}	30 A			
V _R	600 V			
V _F at I _F	1.4 V			
t _{rr} (typ.)	27 ns			
T _J max.	175 °C			
Diode variation	Single die			

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912







RoHS COMPLIANT HALOGEN

FREE

DESCRIPTION / APPLICATIONS

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Repetitive peak reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 95 °C	30	А
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	180	A
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-		
Forward voltage	Forward voltage V _F	I _F = 30 A	-	2.0	2.65	V	
Forward voitage		I _F = 30 A, T _J = 150 °C	-	1.4	1.8		
Povorno logicado gurront		$V_R = V_R$ rated	-	0.02	30		
Reverse leakage current I _R		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	50	300	μA	
Junction capacitance	C _T	V _R = 600 V	-	20	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH	



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	$I_F = 1 \text{ A, } dI_F/dt = 50 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		-	26	35		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	26	-	ns
		T _J = 125 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	=	70	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	3.5	-	A nC
		T _J = 125 °C		=	7.6	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		=	50	-	
		T _J = 125 °C		-	280	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	0.95	1.4	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Madding daving		Case style TO-263AB (D ² PAK)	ETH3006SH			
Marking device		Case style TO-262AA		ETH3006-1H		

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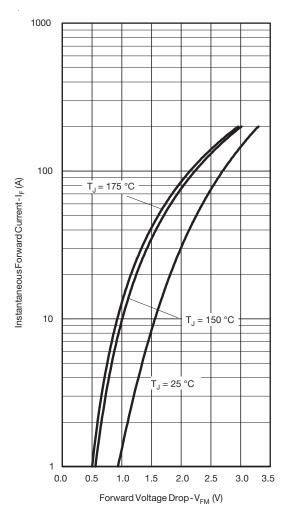


Fig. 1 - Typical Forward Voltage Drop Characteristics

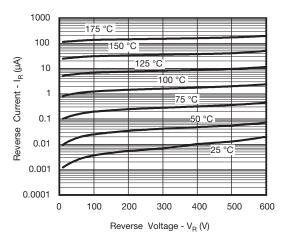


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

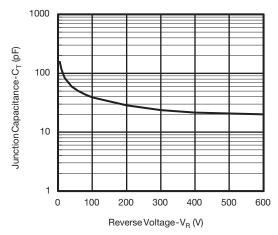


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

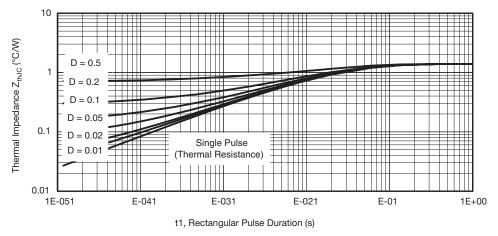


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

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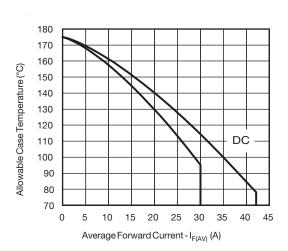


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

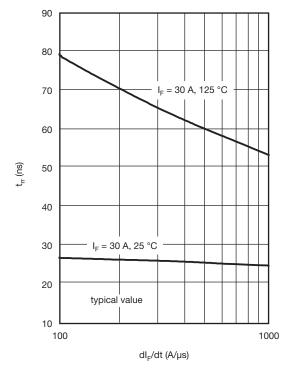


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

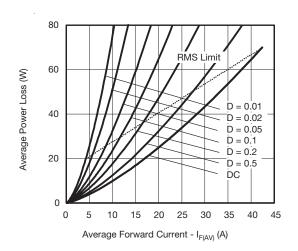


Fig. 6 - Forward Power Loss Characteristics

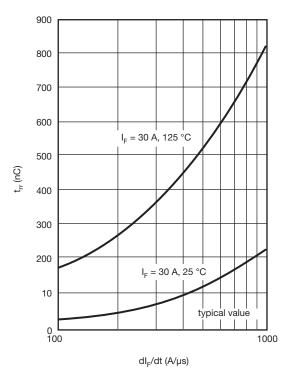


Fig. 8 - Typical Stored Charge vs. dl_F/dt

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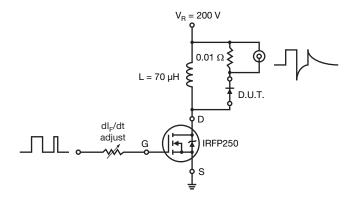
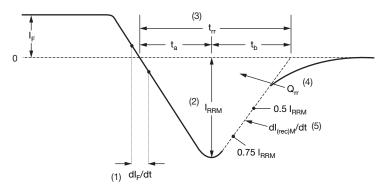


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

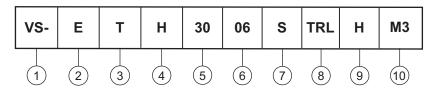
Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Circuit configuration

E = single diode

3 - T = TO-220

4 - H = hyperfast recovery time

5 - Current code (30 = 30 A)

6 - Voltage code (06 = 600 V)

7 - • S = D²PAK

- • -1 = TO-262

8 - • None = tube

- • TRL = tape and reel (left oriented, for D²PAK package)

- • TRR = tape and reel (right oriented, for D²PAK package)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-ETH3006SHM3	50	1000	Antistatic plastic tube		
VS-ETH3006-1HM3	50	1000	Antistatic plastic tube		
VS-ETH3006STRRHM3	800	800	13" diameter reel		
VS-ETH3006STRLHM3	800	800	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046		
Difficusions	TO-262AA	www.vishay.com/doc?95419		
Down moulcing information	TO-263AB (D ² PAK)	www.vishay.com/doc?95444		
Part marking information	TO-262AA	www.vishay.com/doc?95443		
Packaging information	TO-263AB (D ² PAK)	www.vishay.com/doc?95032		



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