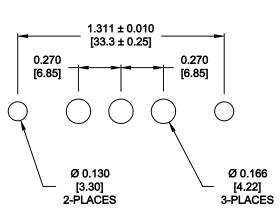
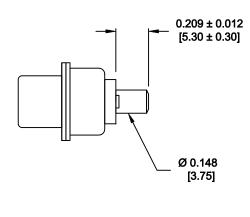


NOTE: POWER CONTACTS COME PRE-LOADED INTO INSULATORS



RECOMMENDED PCB LAYOUT TOLERANCE ± 0.002 [0.05]



DWG NO.

POWER CONTACTS



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	DRAWN:		DATE:					
M. SIGMON		07-17-13						
	CHECKED:		DATE:	X				
	SCALE:	SHEET	OF		REV			
	N.T.S.		2	2	0			

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