

Voltage Regulator

TLE 4274 / 3.3V;2.5V



Features

- Output voltage: 3.3 V/2.5 V ± 4%
- Current capability 400 mA
- Very low current consumption
- Short-circuit proof
- Reverse polarity proof
- Suitable for use in automotive electronics
- Green Product (RoHS compliant)
- AEC Qualified

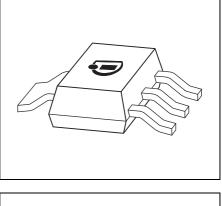
Functional Description

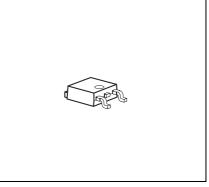
The TLE 4274/3.3V;2.5V is a voltage regulator available in a SOT223 and TO252 package. The IC regulates an input voltage up to 40 V to $V_{\text{Qrated}} = 3.3 \text{ V/2.5 V}$. The maximum output current is 400 mA. The IC is shortcircuit proof and has a shutdown circuit protecting it against overtemperature. The TLE 4274 is also available as 5 V, 8.5 V and 10 V version. Please refer to the data sheet TLE 4274.

Dimensioning Information on External Components

The input capacitor $C_{\rm I}$ is necessary for compensating line influences. Using a resistor of approx. 1 Ω in series with $C_{\rm I}$, the oscillating of input inductivity and input capacitance can be damped. The output capacitor $C_{\rm Q}$ is necessary for the stability of the regulation circuit. Stability is guaranteed for capacities $C_{\rm Q} \ge 10 \ \mu\text{F}$ with an ESR of $\le 2.5 \ \Omega$ within the operating temperature range.

Туре	Package
TLE 4274 GSV33	PG-SOT223-4
TLE 4274 DV33	PG-TO252-3-11
TLE 4274 GSV25	PG-SOT223-4







Circuit Description

The control amplifier compares a reference voltage to a voltage that is proportional to the output voltage and drives the base of the series transistor via a buffer. Saturation control as a function of the load current prevents any oversaturation of the power element. The IC also includes a number of internal circuits for protection against:

- Overload
- Overtemperature
- Reverse polarity

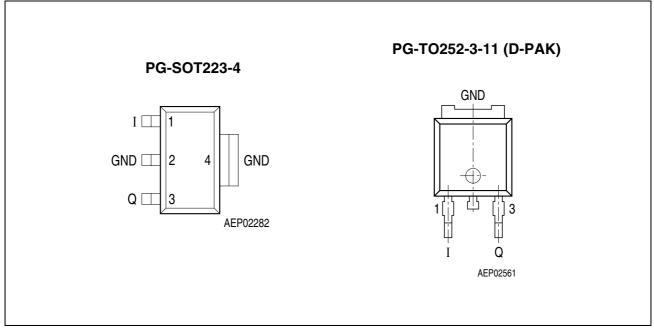
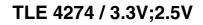


Figure 1 Pin Configuration (top view)

Table 1	Pin Definitions and Functions				
Pin No.	Symbol	Function			
1	I	Input; block to ground directly at the IC with a ceramic capacitor.			
2, 4	GND	Ground; PG-TO252-3-11: internally connected to heatsink			
3	Q	Output; block to ground with capacitor $C_Q \ge 10 \ \mu\text{F}$, ESR $\le 2.5 \ \Omega$			





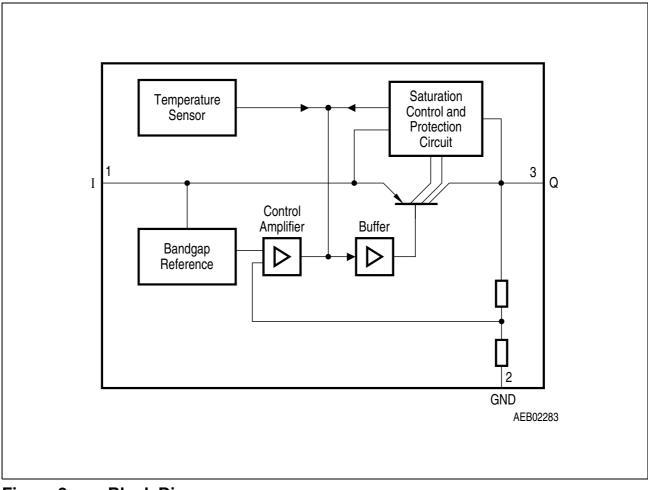


Figure 2 Block Diagram



Table 2Absolute Maximum Ratings

 $T_{\rm j}$ = -40 to 150 °C

Parameter	Symbol	Limit Values		Unit	Test Condition
		Min.	Max.		
Input					- -
Voltage	$V_{\rm I}$	-42	45	V	_
Current	I	-	-	_	Internally limited
Output	· · · · · · · · · · · · · · · · · · ·				
Voltage	VQ	-1.0	40	V	-
Current	IQ	-	-	_	Internally limited
Ground		·			
Current	I_{GND}	-	100	mA	-
Temperature					
Junction temperature	Tj	-	150	°C	-
Storage temperature	T_{stg}	-50	150	°C	_

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.

Table 3Operating Range

Parameter	Symbol	Limit Values		Unit	Remarks
		Min.	Max.	_	
Input voltage	V_1	4.7	40	V	_
Junction temperature	T _j	-40	150	°C	_
Thermal Resistance		•	•	•	
Junction ambient	$R_{ m thja}$	_	100	K/W	SOT223 ¹⁾
Junction ambient	R _{thja}	-	70	K/W	TO252 ²⁾
Junction case	R _{thjc}	-	25	K/W	SOT223
Junction case	R _{thic}	_	4	K/W	TO252

1) Soldered in, 1 cm² copper area at pin 4, FR4

2) Soldered in, minimal footprint, FR4



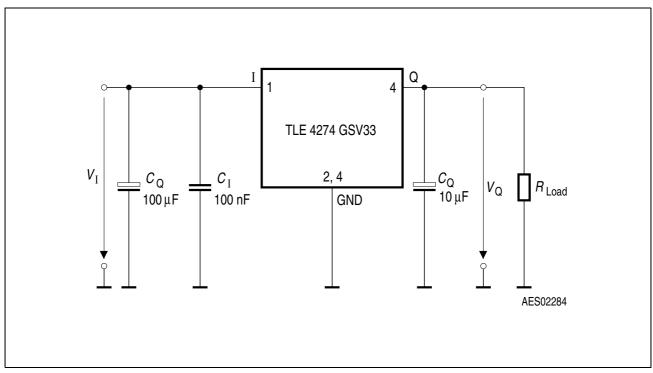
Table 4Characteristics

 $V_{\rm I}$ = 6 V; -40 $^{\rm o}{\rm C}$ < $T_{\rm j}$ < 150 $^{\rm o}{\rm C}$ (unless otherwise specified)

Parameter	Symbol	Limit Values			Unit	Measuring Condition
		Min.	Тур.	Max.		
Output voltage V33-Version	V _Q	3.17	3.3	3.44	V	5 mA < $I_{\rm Q}$ < 400 mA 4.7 V < $V_{\rm I}$ < 28 V
Output voltage V33-Version	V _Q	3.17	3.3	3.44	V	5 mA < $I_{\rm Q}$ < 200 mA 4.7 V < $V_{\rm I}$ < 40 V
Output voltage V25-Version	V _Q	2.4	2.5	2.6	V	5 mA < I_Q < 400 mA 4.7 V < V_1 < 28 V
Output voltage V25-Version	V _Q	2.4	2.5	2.6	V	$5 \text{ mA} < I_Q < 200 \text{ mA}$ 4.7 V < V_1 < 40 V
Output current limitation ¹⁾	IQ	400	600	-	mA	_
Current consumption; $I_q = I_1 - I_Q$	I _q	-	100	220	μA	$I_{\rm Q} = 1 {\rm mA}$
Current consumption; $I_q = I_1 - I_Q$	I _q	-	8	15	mA	I _Q = 250 mA
$\overline{\text{Current consumption;}}$ $I_{q} = I_{l} - I_{Q}$	I _q	-	20	30	mA	$I_{\rm Q} = 400 {\rm mA}$
Drop voltage ¹⁾ V33-Version	V_{dr}	-	0.7	1.2	V	$I_{\rm Q} = 300 \text{ mA}$ $V_{\rm dr} = V_{\rm I} - V_{\rm Q}$
Drop voltage ¹⁾ V25-Version	V _{dr}	_	1.0	2.0	V	$I_{\rm Q} = 300 \text{ mA}$ $V_{\rm dr} = V_{\rm I} - V_{\rm Q}$
Load regulation	ΔV_{Q}	-	40	70	mV	$I_{\rm Q}$ = 5 mA to 300 mA; $V_{\rm I}$ = 6 V
Line regulation	ΔV_{Q}	_	10	25	mV	$\Delta V_{\rm I} = 12 \text{ V to } 32 \text{ V}$ $I_{\rm Q} = 5 \text{ mA}$
Power supply ripple rejection	PSRR	-	60	-	dB	$f_{\rm r}$ = 100 Hz; $V_{\rm r}$ = 0.5 Vpp
Temperature output voltage drift	dV_Q/dT	-	0.5	-	mV/K	_

1) Measured when the output voltage V_{Q} has dropped 100 mV from the nominal value obtained at V_{I} = 6 V.







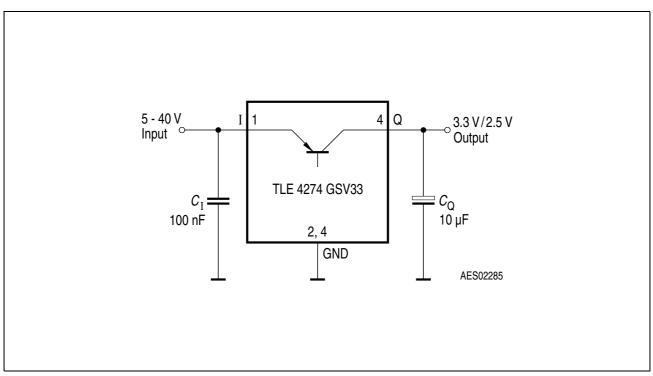
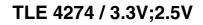


Figure 4 Application Circuit





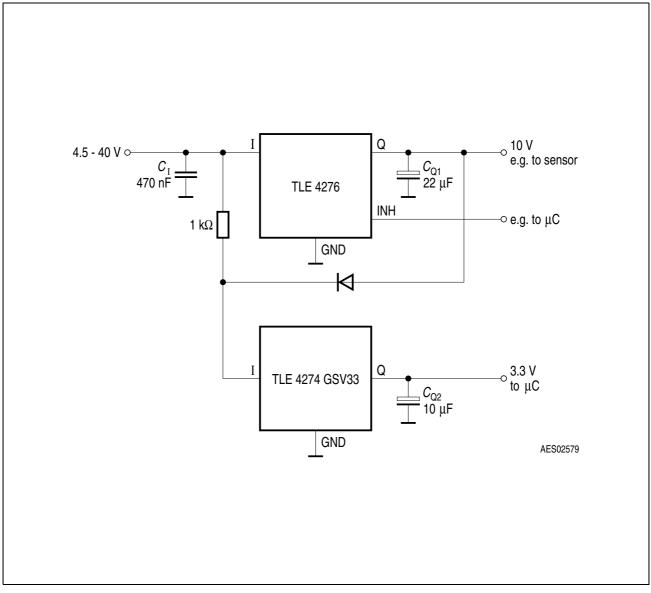
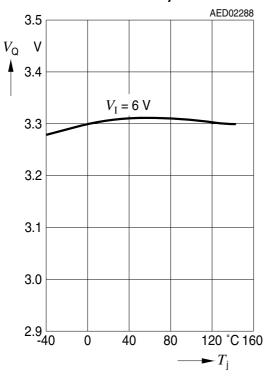


Figure 5 Application Example

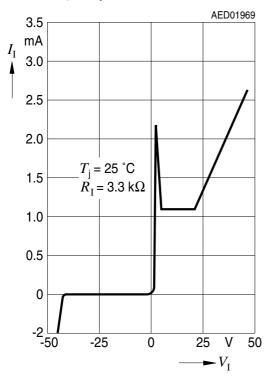


Typical Performance Characteristics

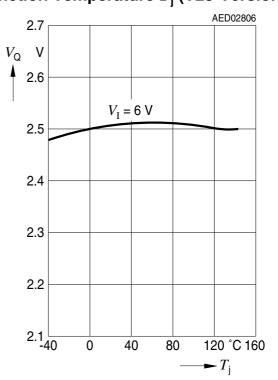
Output Voltage V_{Q} versus Junction Temperature T_{i} (V33-Version)



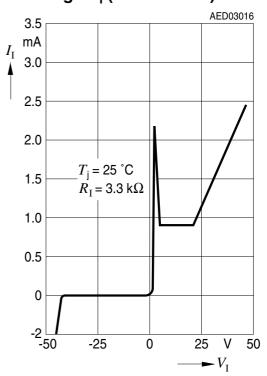
Input Current I_q versus Input Voltage V_1 (V33-Version)



Output Voltage V_{Q} versus Junction Temperature T_{i} (V25-Version)

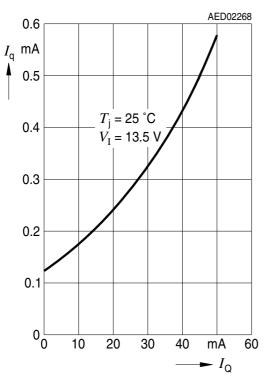


Input Current I_q versus Input Voltage V_1 (V25-Version)

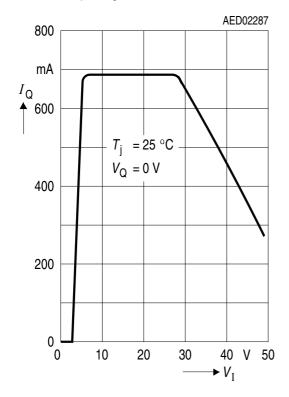




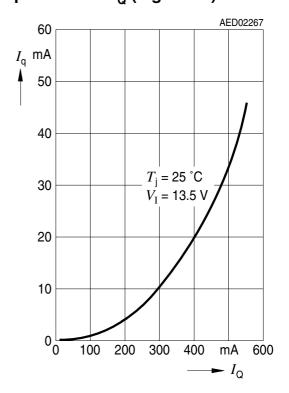
Current Consumption I_q versus Output Current I_Q (low load)



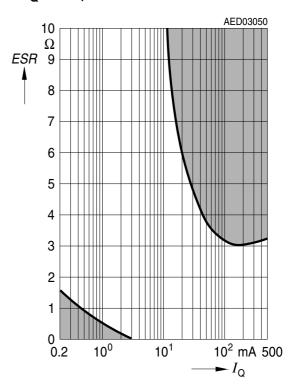
Output Current I_Q versus Input Voltage V_I



Current Consumption I_q versus Output Current I_Q (high load)



Region of Stability for C_{Q} = 10 μ F





Package Outlines

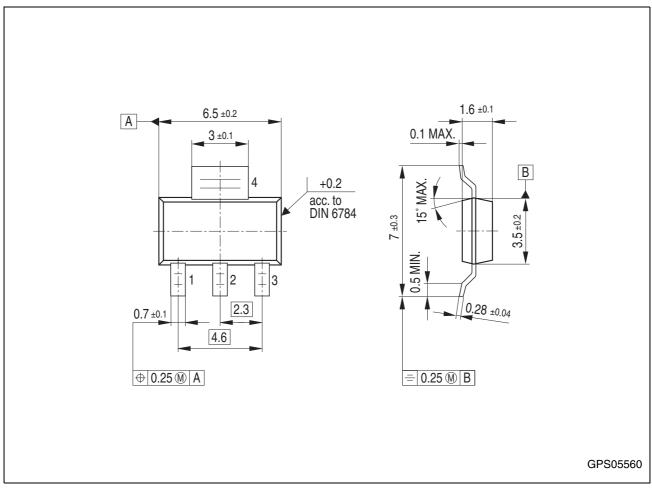


Figure 6 PG-SOT223-4 (Plastic Small Outline Transistor)

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

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SMD = Surface Mounted Device

Dimensions in mm



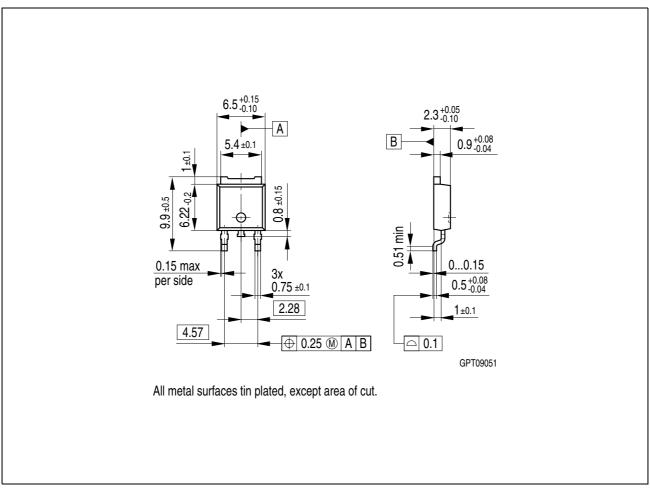


Figure 7 PG-T0252-3-11 (Plastic Transistor Single Outline)

Green Product (RoHS compliant)

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SMD = Surface Mounted Device

Dimensions in mm



Revision History

Version	Date	Changes
Rev. 2.3	2008-03-10	Simplified package name to PG-SOT223-4. No modification of released product.
Rev. 2.2	2007-03-20	Initial version of RoHS-compliant derivate of TLE 4274 / 3.3V;2.5V Page 1: AEC certified statement added Page 1 and Page 10: RoHS compliance statement and Green product feature added Page 1 and Page 10: Package changed to RoHS compliant version Legal Disclaimer updated

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