3.2 Watt Plastic Surface Mount POWERMITE[®] Package

This complete new line of 3.2 Watt Zener Diodes are offered in highly efficient micro miniature, space saving surface mount with its unique heat sink design. The POWERMITE package has the same thermal performance as the SMA while being 50% smaller in footprint area and delivering one of the lowest height profiles (1.1 mm) in the industry. Because of its small size, it is ideal for use in cellular phones, portable devices, business machines and many other industrial/consumer applications.

Features

- Zener Breakdown Voltage: 6.2 47 V
- DC Power Dissipation: 3.2 W with Tab 1 (Cathode) @ 75°C
- Low Leakage $< 5 \mu A$
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- Low Profile Maximum Height of 1.1 mm
- Integral Heat Sink/Locking Tabs
- Full Metallic Bottom Eliminates Flux Entrapment
- Small Footprint Footprint Area of 8.45 mm²
- Supplied in 12 mm Tape and Reel
- Lead Orientation in Tape: Cathode (Short) Lead to Sprocket Holes
- POWERMITE is JEDEC Registered as DO-216AA
- Cathode Indicated by Polarity Band
- These Devices are Pb-Free and are RoHS Compliant

Mechanical Characteristics

CASE: Void-free, transfer-molded, thermosetting plastic **FINISH:** All external surfaces are corrosion resistant and leads are readily solderable

MOUNTING POSITION: Any

MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES:

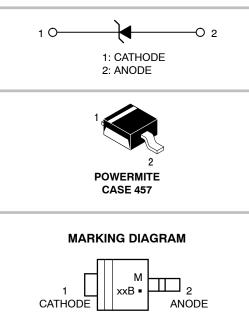
260°C for 10 Seconds

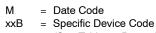


ON Semiconductor®

www.onsemi.com

PLASTIC SURFACE MOUNT 3.2 WATT ZENER DIODES 6.2 – 47 VOLTS





(See Table on Page 2)

= Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
1PMT59xxBT1G	POWERMITE (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
DC Power Dissipation @ T _A = 25°C (Note 1) Derate above 25°C Thermal Resistance, Junction-to-Ambient	P _D R _{θJA}	500 4.0 248	mW mW/°C °C/W
Thermal Resistance, Junction-to-Lead (Anode)	$R_{ heta Janode}$	35	°C/W
Maximum DC Power Dissipation (Note 2) Thermal Resistance from Junction-to-Tab (Cathode)	P_D $R_{ hetaJcathode}$	3.2 23	W °C/W
Operating and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C

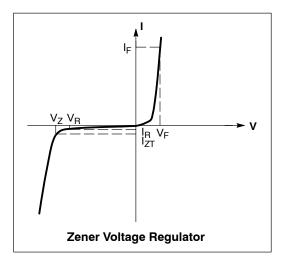
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Mounted with recommended minimum pad size, PC board FR-4.

2. At Tab (Cathode) temperature, $T_{tab} = 75^{\circ}C$

ELECTRICAL CHARACTERISTICS (T _L = 25°C unless
otherwise noted, V _F = 1.5 V Max. @ I _F = 200 mAdc for all types)

Symbol	Parameter			
VZ	Reverse Zener Voltage @ I _{ZT}			
I _{ZT}	Reverse Current			
Z _{ZT}	Maximum Zener Impedance @ I _{ZT}			
I _{ZK}	Reverse Current			
Z _{ZK}	Maximum Zener Impedance @ I _{ZK}			
I _R	Reverse Leakage Current @ V _R			
V _R	V _R Reverse Voltage			
١ _F	Forward Current			
V _F	Forward Voltage @ I _F			



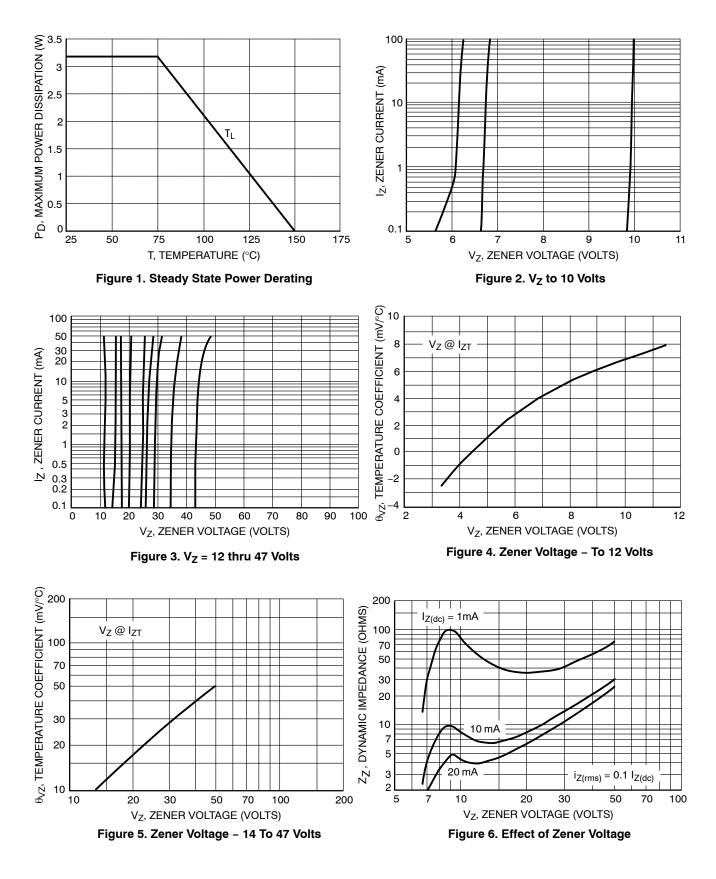
ELECTRICAL CHARACTERISTICS (T_L = 30°C unless otherwise noted, V_F = 1.25 Volts @ 200 mA)

		Zener	Voltage (N	lote 3)				Z _{ZT} @ I _{ZT}	Z _{ZK} @ I _{ZK}	
	Device	Vz	@ I _{ZT} (Vol	ts)	I _{ZT}	I _R @ V _R	VR	(Note 4)	(Note 4)	I _{ZK}
Device*	Marking	Min	Nom	Max	(mA)	(μΑ)	(V)	(Ω)	(Ω)	(mA)
1PMT5920BT1G	20B	5.89	6.2	6.51	60.5	5.0	4.0	2.0	200	1.0
1PMT5921BT1G	21B	6.46	6.8	7.14	55.1	5.0	5.2	2.5	200	1.0
1PMT5924BT1G	24B	8.64	9.1	9.56	41.2	5.0	7.0	4.0	500	0.5
1PMT5927BT1G	27B	11.4	12	12.6	31.2	1.0	9.1	6.5	550	0.25
1PMT5929BT1G	29B	14.25	15	15.75	25	1.0	11.4	9.0	600	0.25
1PMT5933BT1G	33B	20.9	22	23.1	17	1.0	16.7	17.5	650	0.25
1PMT5934BT1G	34B	22.8	24	25.2	15.6	1.0	18.2	19	700	0.25
1PMT5935BT1G	35B	25.65	27	28.35	13.9	1.0	20.6	23	700	0.25
1PMT5941BT1G	41B	44.65	47	49.35	8.0	1.0	35.8	67	1000	0.25

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product

Zener voltage is measured with the device junction in thermal equilibrium with an ambient temperature of 25°C.
Zener Impedance Derivation Z_{ZT} and Z_{ZK} are measured by dividing the AC voltage drop across the device by the AC current applied. The specified limits are for I_Z(ac) = 0.1 I_Z(dc) with the ac frequency = 60 Hz.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

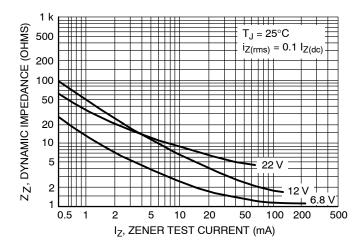
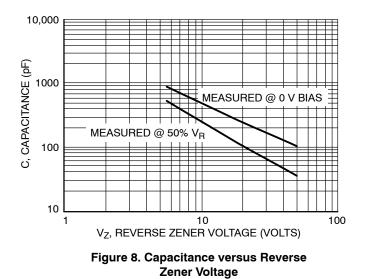


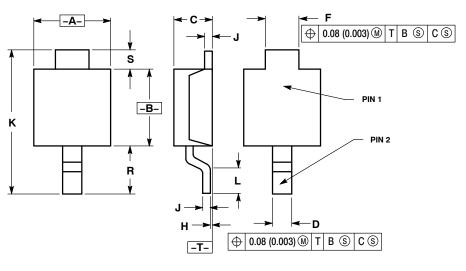
Figure 7. Effect of Zener Current



PACKAGE DIMENSIONS

POWERMITE®

CASE 457-04 **ISSUE F**

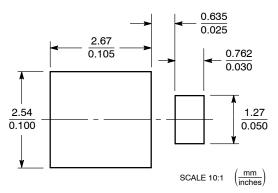


NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

CONTROLLING DIMENSION: MILLIMETER 2 CUNTROLLING DIMENSION: MILLIMETER. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.

	MILLIN	IETERS	INCHES		
DIM	MIN	MIN MAX		MAX	
Α	1.75	2.05	0.069	0.081	
В	1.75	2.18	0.069	0.086	
С	0.85	1.15	0.033	0.045	
D	0.40	0.69	0.016	0.027	
F	0.70	1.00	0.028	0.039	
Н	-0.05	+0.10	-0.002	+0.004	
J	0.10	0.25	0.004	0.010	
Κ	3.60	3.90	0.142	0.154	
L	0.50	0.80	0.020	0.031	
R	1.20	1.50	0.047	0.059	
S	0.50	REF	0.019 REF		

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

POWERMITE is a registered trademark of and used under a license from Microsemi Corporation.

ON Semiconductor and 💷 are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. Coverage may be accessed at www.onsemi.com/site/pont/atent_Marking.pon. ON semiconductor reserves the right to make changes winnout further notice to any products nerein. ON semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights or the rights of others. ON Semiconductor products are not designed intended or suthering of the rupe ner or withing a comparent is literation are any license and any semiconductor and the rupe or any license and or suthering device or any circuits a comparent is patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

ON Semiconductor Website: www.onsemi.com Order Literature: http://www.onsemi.com/orderlit

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

For additional information, please contact your local

Sales Representative