



Parameter	Rating	Units
Blocking Voltage	60	V
Load Current	1	$A_{rms} / A_{DC}$
On-Resistance (max)	0.6	$\Omega$
LED Current to Operate	2	mA

### Features

- Low On-Resistance (0.6 $\Omega$ )
- Low Control Current (2mA)
- 3750V<sub>rms</sub> Input/Output Isolation
- 100% Solid State
- Low Drive Power Requirements
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Surface Mount Version
- Small 8-Pin Package
- Tape & Reel available
- Flammability Rating UL 94 V-0

### Applications

- Security
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Medical Equipment-Patient/Equipment Isolation
- Aerospace
- Industrial Controls

### Description

LBA710 is a 60V, 1A, 0.6 $\Omega$  dual Solid State Relay integrating independent single-pole normally open (1-Form-A) and single-pole normally closed (1-Form-B) relays into a single package. It features a superior combination of low on-resistance and 1A load current handling capability.

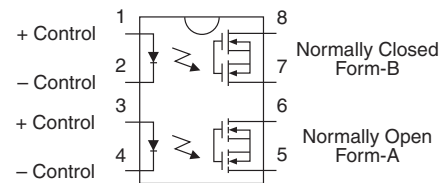
### Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component:  
TUV Certificate B 13 12 82667 003

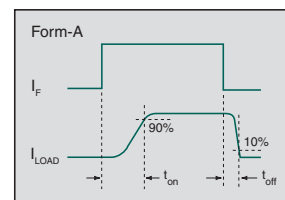
### Ordering Information

Part #	Description
LBA710	8-Pin DIP (50/Tube)
LBA710S	8-Pin Surface Mount (50/Tube)
LBA710STR	8-Pin Surface Mount (1000/Reel)

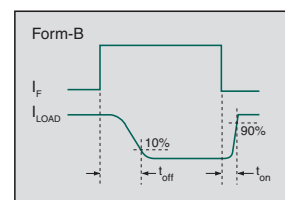
### Pin Configuration



### Switching Characteristics of Normally Open Devices



### Switching Characteristics of Normally Closed Devices



### Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	60	V <sub>P</sub>
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation <sup>1</sup>	150	mW
Total Power Dissipation <sup>2</sup>	800	mW
Isolation Voltage, Input to Output	3750	V <sub>rms</sub>
ESD Rating, Human Body Model	8	kV
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

<sup>1</sup> Derate linearly 1.33 mW / °C

<sup>2</sup> Derate linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

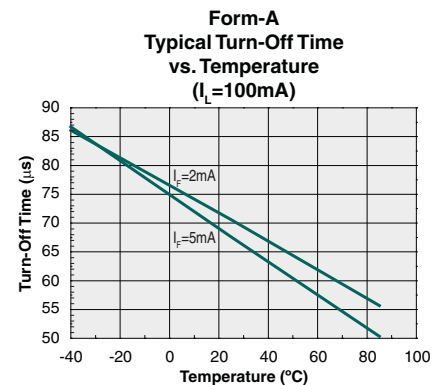
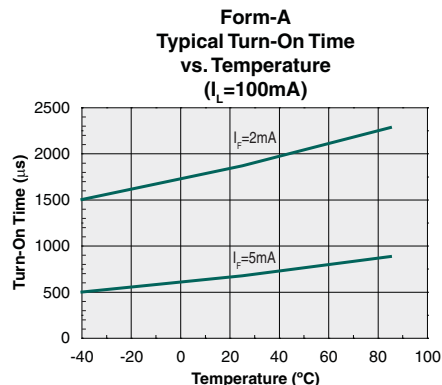
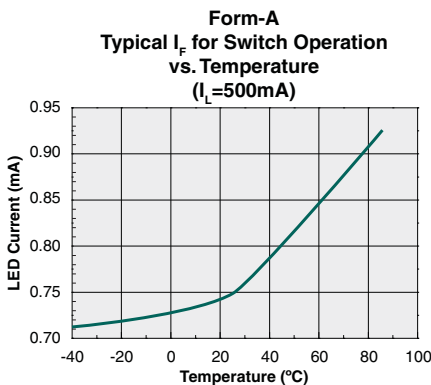
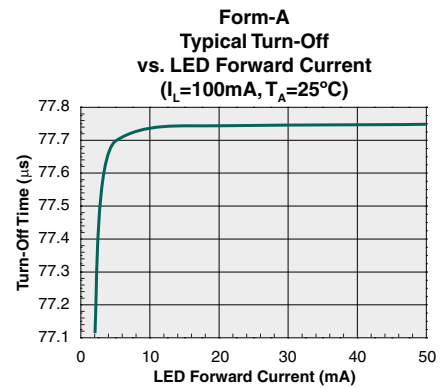
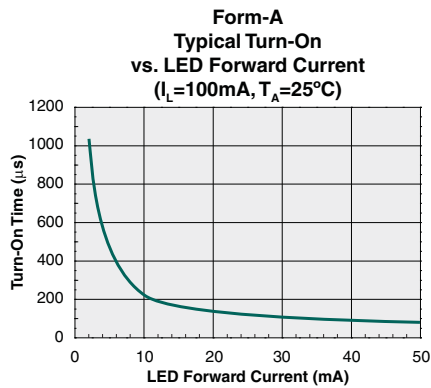
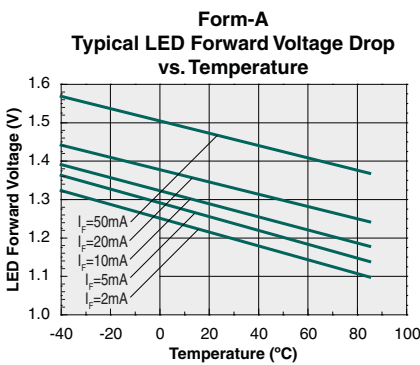
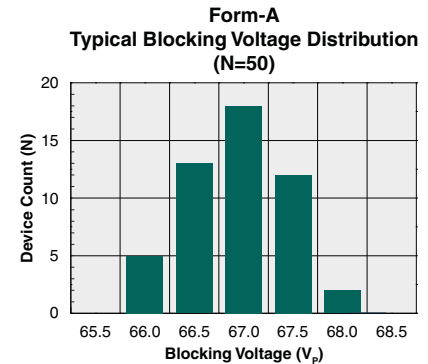
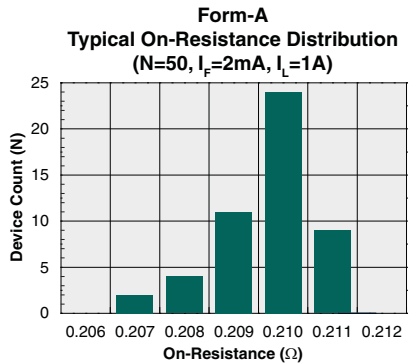
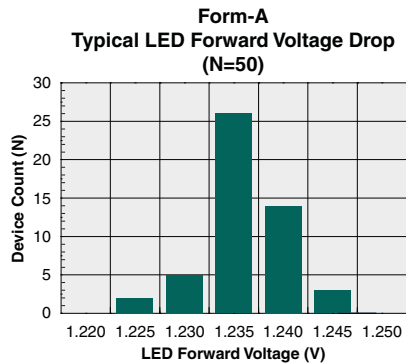
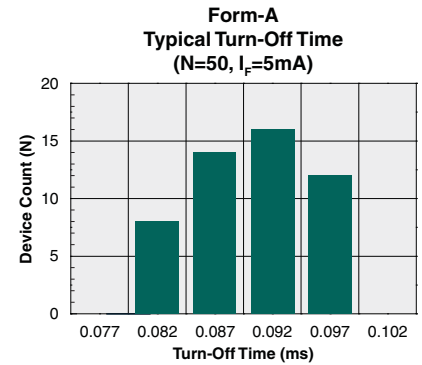
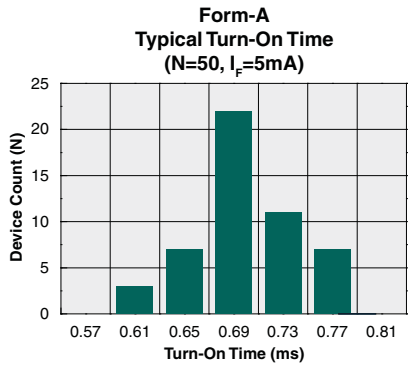
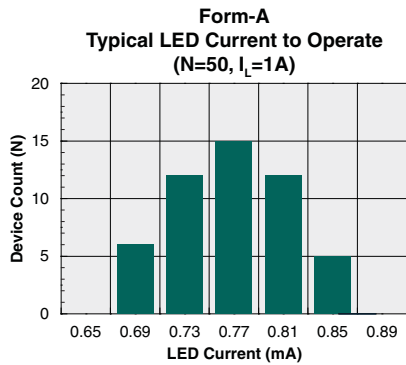
Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

### Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics: Form-A (Normally Open)</b>						
Load Current						
Continuous	I <sub>F</sub> =2mA	I <sub>L</sub>	-	-	1	A <sub>rms</sub> / A <sub>DC</sub>
Peak	I <sub>F</sub> =2mA, t ≤ 10ms	I <sub>LPK</sub>	-	-	±5	A <sub>P</sub>
On-Resistance	I <sub>F</sub> =2mA, I <sub>L</sub> =1A	R <sub>ON</sub>	-	0.21	0.6	Ω
Switching Speeds						
Turn-On Output (Activate)	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	0.7	5	ms
Turn-Off Output (Deactivate)		t <sub>off</sub>	-	0.09	5	
Off-State Leakage Current	I <sub>F</sub> =0mA, V <sub>L</sub> =60V	I <sub>LEAK</sub>	-	-	1	μA
Output Capacitance	I <sub>F</sub> =0mA, V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	44	-	pF
<b>Output Characteristics: Form-B (Normally Closed)</b>						
Load Current						
Continuous	I <sub>F</sub> =0mA	I <sub>L</sub>	-	-	1	A <sub>rms</sub> / A <sub>DC</sub>
Peak	I <sub>F</sub> =0mA, t ≤ 10ms	I <sub>LPK</sub>	-	-	±5	A <sub>P</sub>
On-Resistance	I <sub>F</sub> =0mA, I <sub>L</sub> =1A	R <sub>ON</sub>	-	0.39	0.6	Ω
Switching Speeds						
Turn-On Output (Deactivate)	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	0.63	5	ms
Turn-Off Output (Activate)		t <sub>off</sub>	-	1.5	5	
Off-State Leakage Current	I <sub>F</sub> =2mA, V <sub>L</sub> =60V	I <sub>LEAK</sub>	-	-	1	μA
Output Capacitance	I <sub>F</sub> =2mA, V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	125	-	pF
<b>Input Characteristics: Form-A and Form-B</b>						
Input Control Current to Activate	I <sub>L</sub> =1A	I <sub>F</sub>	-	-	2	mA
Input Control Current to Deactivate	-	I <sub>F</sub>	0.1	-	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA
<b>Common Characteristics: Form-A and Form-B</b>						
Capacitance, Input to Output	V <sub>IO</sub> =0V, f=1MHz	C <sub>IO</sub>	-	3	-	pF

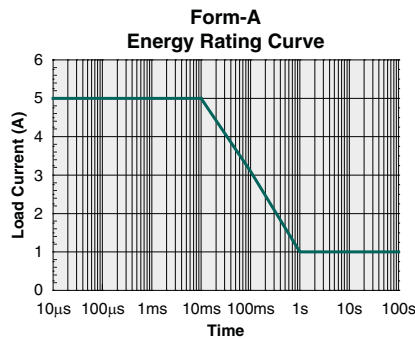
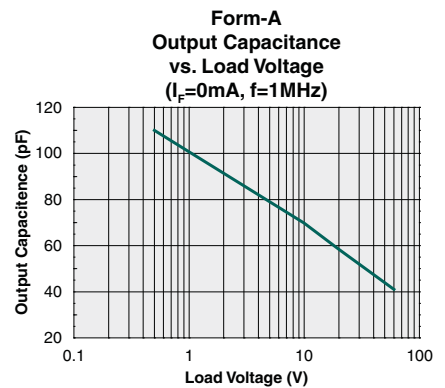
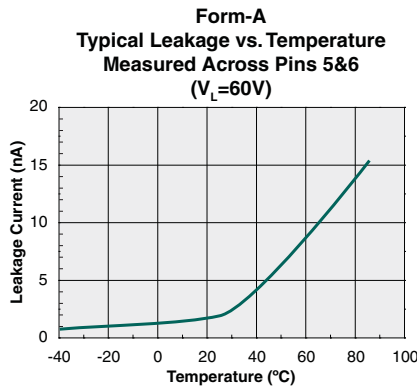
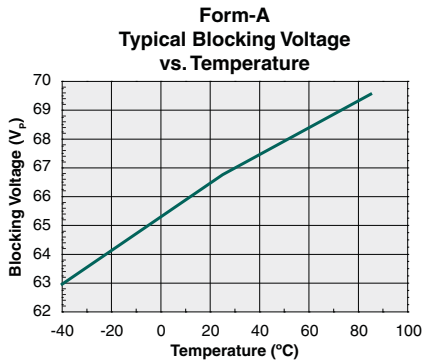
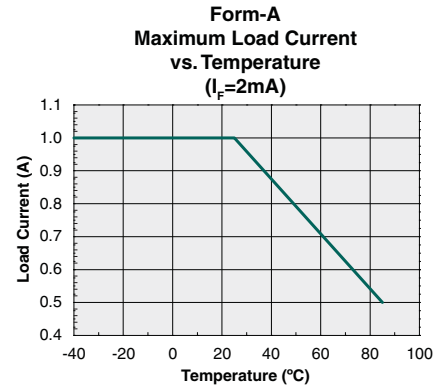
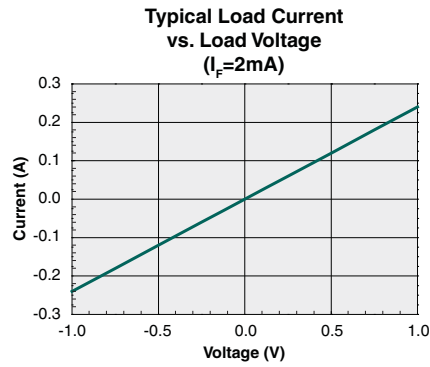
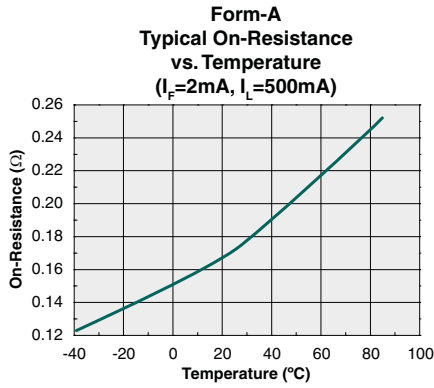
\*NOTE: If both poles operate simultaneously, then load current must be derated so as not to exceed the package total power dissipation value.

Form-A PERFORMANCE DATA\*



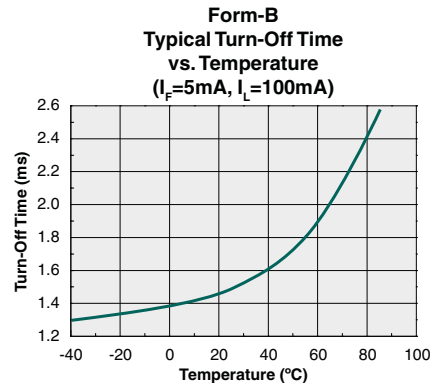
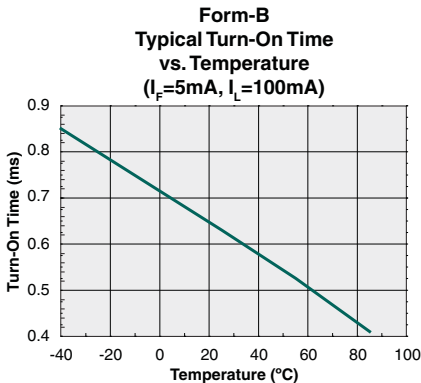
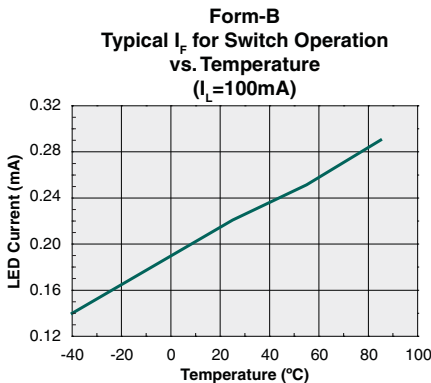
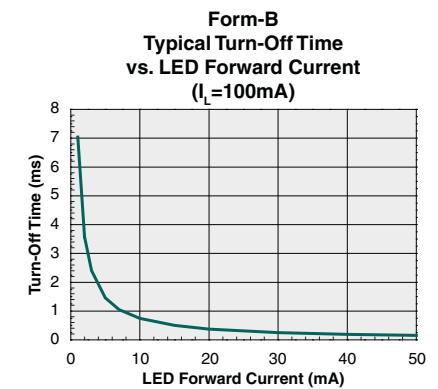
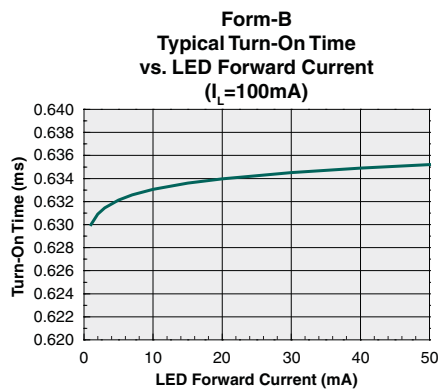
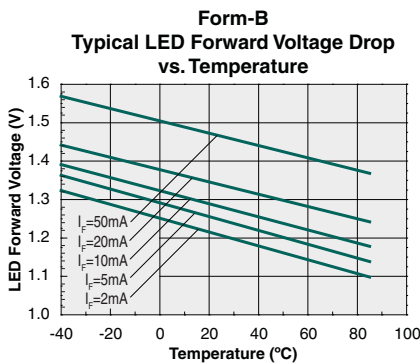
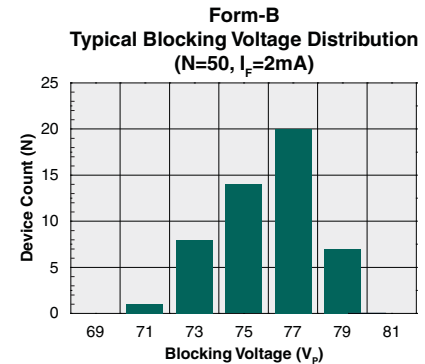
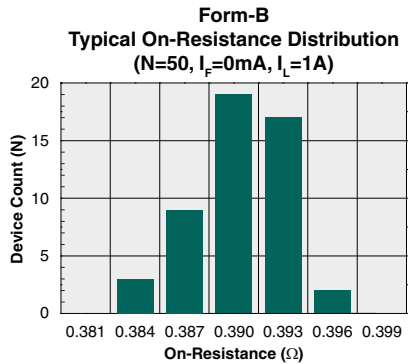
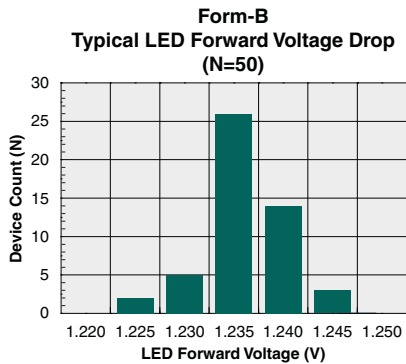
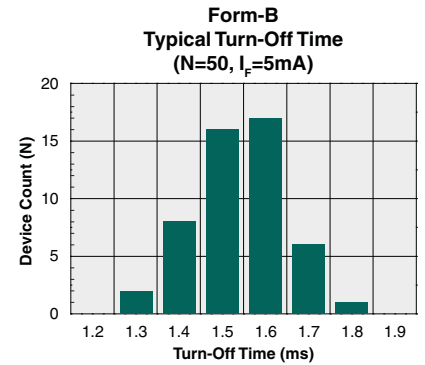
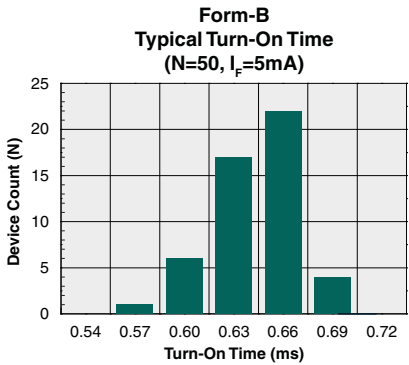
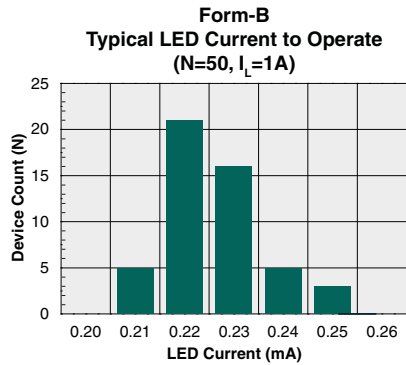
\*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C. For guaranteed parameters not indicated in the written specifications, please contact our Application Department.

**Form-A PERFORMANCE DATA (continued)\***



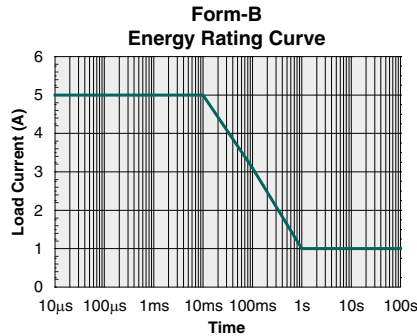
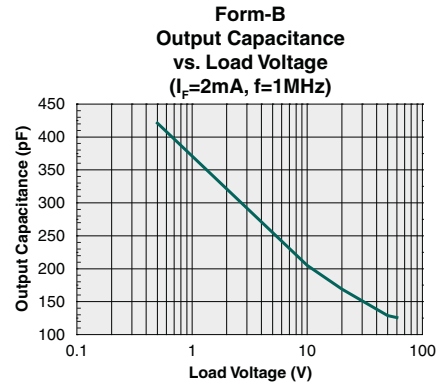
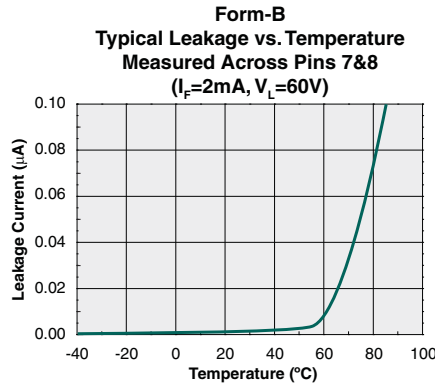
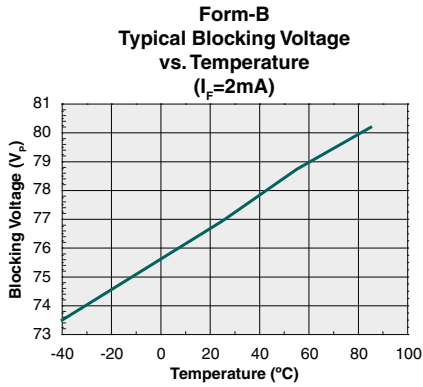
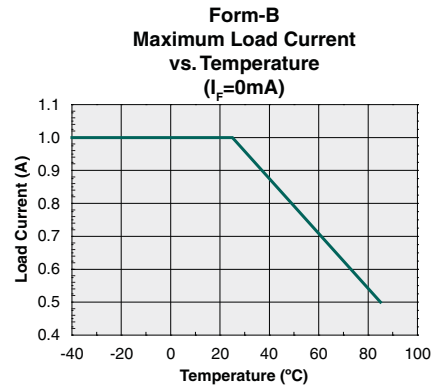
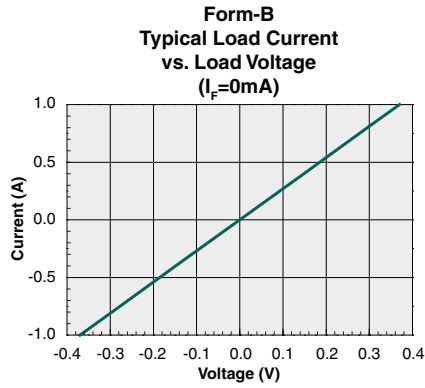
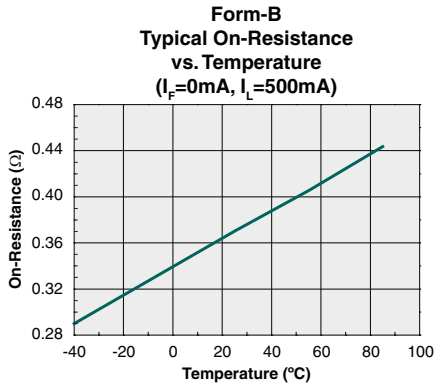
\*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.  
For guaranteed parameters not indicated in the written specifications, please contact our Application Department.

Form-B PERFORMANCE DATA\*



\*Unless otherwise noted, data presented in these graphs is typical of device operation at 25 $^{\circ}C$ .  
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**Form-B PERFORMANCE DATA (continued)\***



\*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.  
For guaranteed parameters not indicated in the written specifications, please contact our Application Department.

## Manufacturing Information

### Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
LBA710 / LBA710S	MSL 1

### ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

### Soldering Profile

Provided in the table below is the Classification Temperature ( $T_c$ ) of this product and the maximum dwell time the body temperature of this device may be ( $T_c - 5$ )°C or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

Device	Classification Temperature ( $T_c$ )	Dwell Time ( $t_p$ )	Max Reflow Cycles
LBA710 / LBA710S	250°C	30 seconds	3

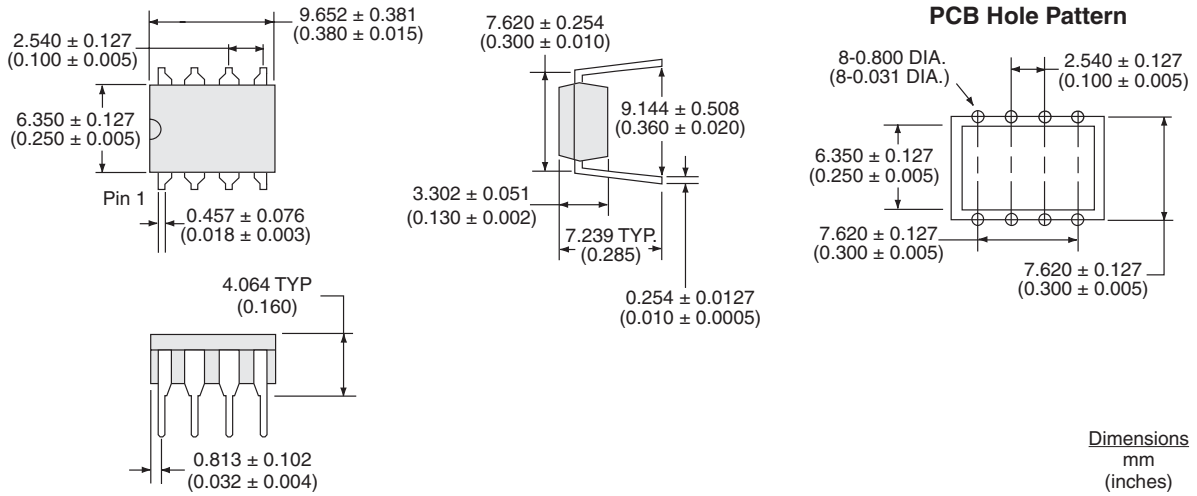
### Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.

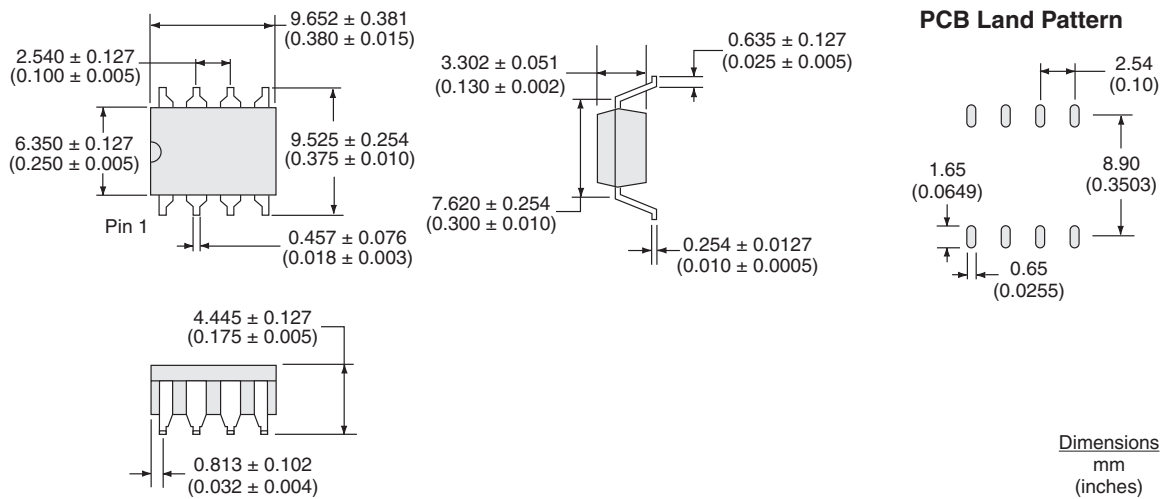


**Mechanical Dimensions**

**LBA710**

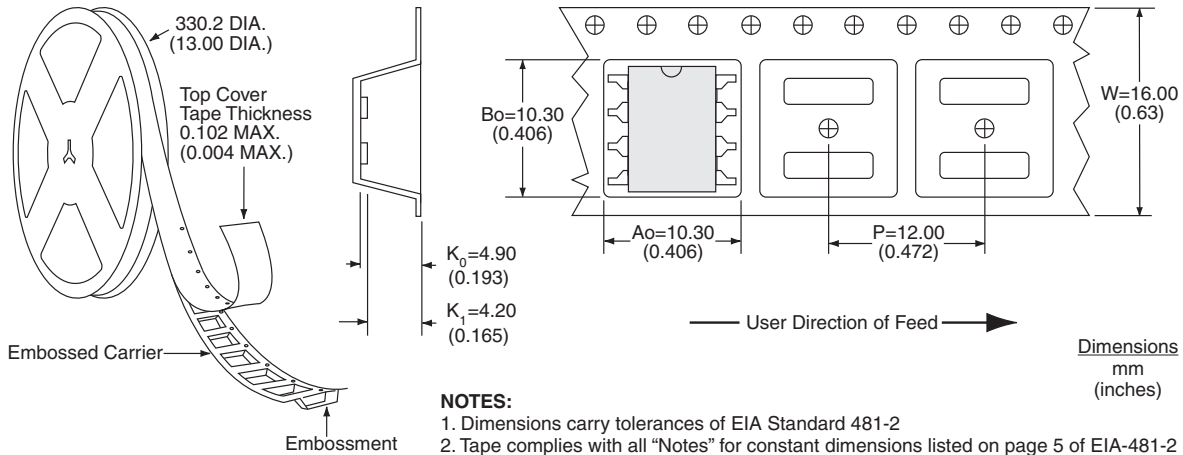


**LBA710S**





**LBA710STR Tape & Reel**



**For additional information please visit our website at: [www.ixysic.com](http://www.ixysic.com)**

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