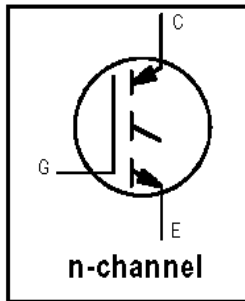


IRG4PF50WPbF

INSULATED GATE BIPOLAR TRANSISTOR

Features

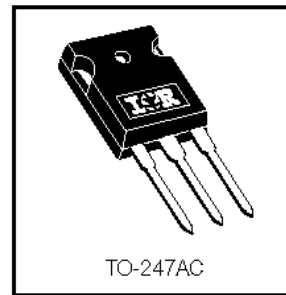
- Optimized for use in Welding and Switch-Mode Power Supply applications
- Industry benchmark switching losses improve efficiency of all power supply topologies
- 50% reduction of E_{off} parameter
- Low IGBT conduction losses
- Latest technology IGBT design offers tighter parameter distribution coupled with exceptional reliability
- Lead-Free



$V_{CES} = 900V$
$V_{CE(on)} \text{ typ.} = 2.25V$
@ $V_{GE} = 15V, I_C = 28A$

Benefits

- Lower switching losses allow more cost-effective operation and hence efficient replacement of larger-die MOSFETs up to 100kHz
- Of particular benefit in single-ended converters and Power Supplies 150W and higher
- Reduction in critical E_{off} parameter due to minimal minority-carrier recombination coupled with low on-state losses allow maximum flexibility in device application



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Breakdown Voltage	900	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	51	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	28	
I_{CM}	Pulsed Collector Current $\text{\textcircled{1}}$	204	
I_{LM}	Clamped Inductive Load Current $\text{\textcircled{2}}$	204	
V_{GE}	Gate-to-Emitter Voltage	± 20	V
E_{ARV}	Reverse Voltage Avalanche Energy $\text{\textcircled{3}}$	186	mJ
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	200	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	78	
T_J	Operating Junction and	-55 to + 150	$^\circ C$
T_{STG}	Storage Temperature Range		
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm from case)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	0.64	$^\circ C/W$
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.24	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	40	
Wt	Weight	6 (0.21)	—	g (oz)

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)CES}	Collector-to-Emitter Breakdown Voltage	900	—	—	V	V _{GE} = 0V, I _C = 250μA
V _{(BR)ECS}	Emitter-to-Collector Breakdown Voltage ④	18	—	—	V	V _{GE} = 0V, I _C = 1.0A
ΔV _{(BR)CES/ΔT_J}	Temperature Coeff. of Breakdown Voltage	—	0.295	—	V/°C	V _{GE} = 0V, I _C = 3.5mA
V _{CE(ON)}	Collector-to-Emitter Saturation Voltage	—	2.25	2.7	V	I _C = 28A, V _{GE} = 15V
		—	2.74	—		I _C = 60A, V _{GE} = 15V
		—	2.12	—		I _C = 28A, T _J = 150°C
V _{GE(th)}	Gate Threshold Voltage	3.0	—	6.0		V _{CE} = V _{GE} , I _C = 250μA
ΔV _{GE(th)/ΔT_J}	Temperature Coeff. of Threshold Voltage	—	-13	—	mV/°C	V _{CE} = V _{GE} , I _C = 1.0mA
g _{fe}	Forward Transconductance ⑤	26	39	—	S	V _{CE} ≥ 15V, I _C = 28A
I _{CES}	Zero Gate Voltage Collector Current	—	—	500	μA	V _{GE} = 0V, V _{CE} = 900V
		—	—	2.0		V _{GE} = 0V, V _{CE} = 10V, T _J = 25°C
		—	—	5.0	mA	V _{GE} = 0V, V _{CE} = 900V, T _J = 150°C
I _{GES}	Gate-to-Emitter Leakage Current	—	—	±100	nA	V _{GE} = ±20V

Switching Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q _g	Total Gate Charge (turn-on)	—	160	240	nC	I _C = 28A V _{CC} = 400V V _{GE} = 15V See Fig. 8
Q _{ge}	Gate - Emitter Charge (turn-on)	—	19	29		
Q _{gc}	Gate - Collector Charge (turn-on)	—	53	80		
t _{d(on)}	Turn-On Delay Time	—	29	—	ns	T _J = 25°C I _C = 28A, V _{CC} = 720V V _{GE} = 15V, R _G = 5.0Ω Energy losses include "tail"
t _r	Rise Time	—	26	—		
t _{d(off)}	Turn-Off Delay Time	—	110	170		
t _f	Fall Time	—	150	220		
E _{on}	Turn-On Switching Loss	—	0.19	—	mJ	See Fig. 10, 11, 13, 14
E _{off}	Turn-Off Switching Loss	—	1.08	—		
E _{ts}	Total Switching Loss	—	1.25	1.7		
t _{d(on)}	Turn-On Delay Time	—	28	—	ns	T _J = 150°C, I _C = 28A, V _{CC} = 720V V _{GE} = 15V, R _G = 5.0Ω Energy losses include "tail"
t _r	Rise Time	—	26	—		
t _{d(off)}	Turn-Off Delay Time	—	280	—		
t _f	Fall Time	—	90	—		
E _{ts}	Total Switching Loss	—	3.45	—	mJ	See Fig. 13, 14
L _E	Internal Emitter Inductance	—	13	—	nH	Measured 5mm from package
C _{ies}	Input Capacitance	—	3300	—	pF	V _{GE} = 0V V _{CC} = 30V f = 1.0MHz See Fig. 7
C _{oes}	Output Capacitance	—	200	—		
C _{res}	Reverse Transfer Capacitance	—	45	—		

Notes:

- ① Repetitive rating; V_{GE} = 20V, pulse width limited by max. junction temperature. (See fig. 13b)
- ② V_{CC} = 80%(V_{CES}), V_{GE} = 20V, L = 10μH, R_G = 5.0Ω, (See fig. 13a)
- ③ Repetitive rating; pulse width limited by maximum junction temperature.
- ④ Pulse width ≤ 80μs; duty factor ≤ 0.1%.
- ⑤ Pulse width 5.0μs, single shot.

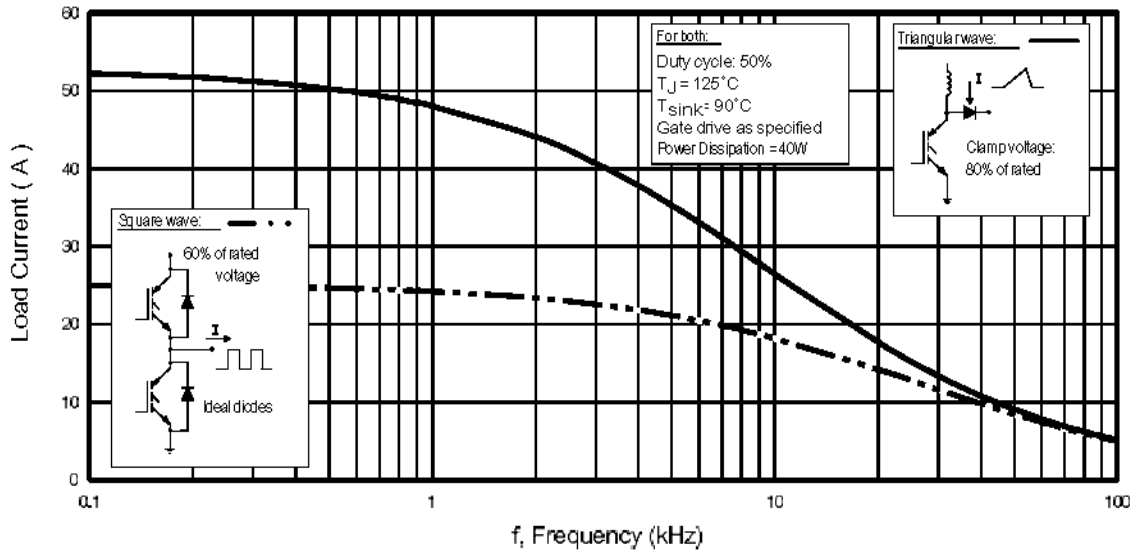


Fig. 1 - Typical Load Current vs. Frequency
(For square wave, $I = I_{RMS}$ of fundamental; for triangular wave, $I = I_{PK}$)

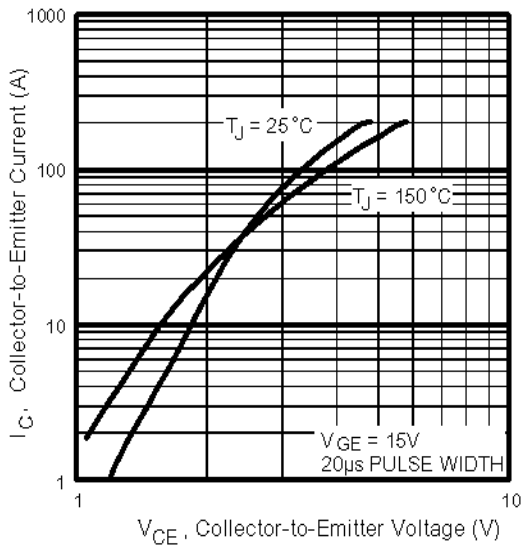


Fig. 2 - Typical Output Characteristics

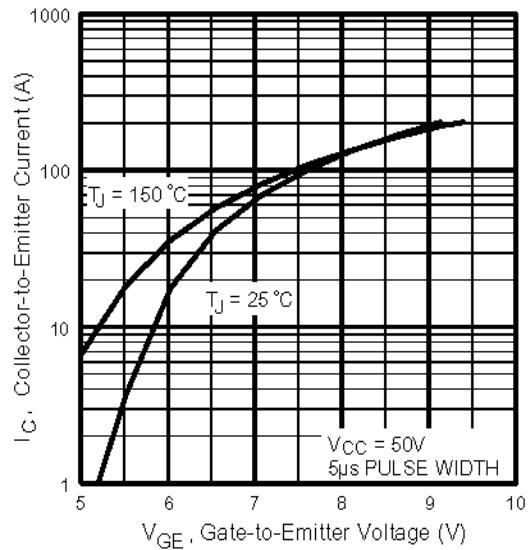


Fig. 3 - Typical Transfer Characteristics

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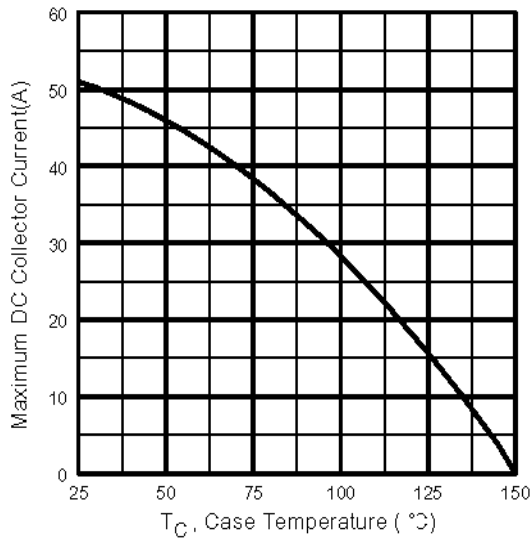


Fig. 4 - Maximum Collector Current vs. Case Temperature

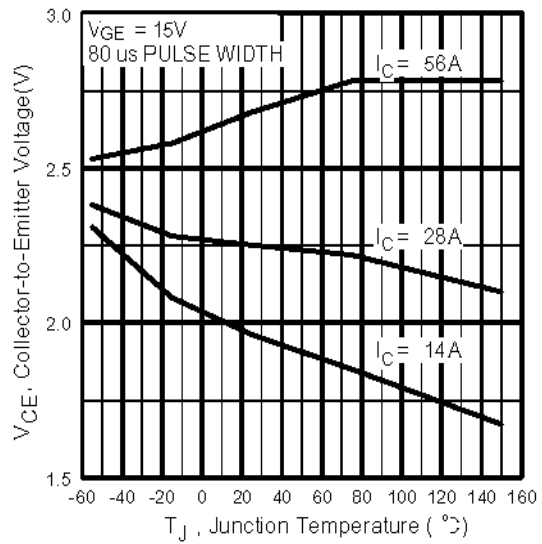


Fig. 5 - Collector-to-Emitter Voltage vs. Junction Temperature

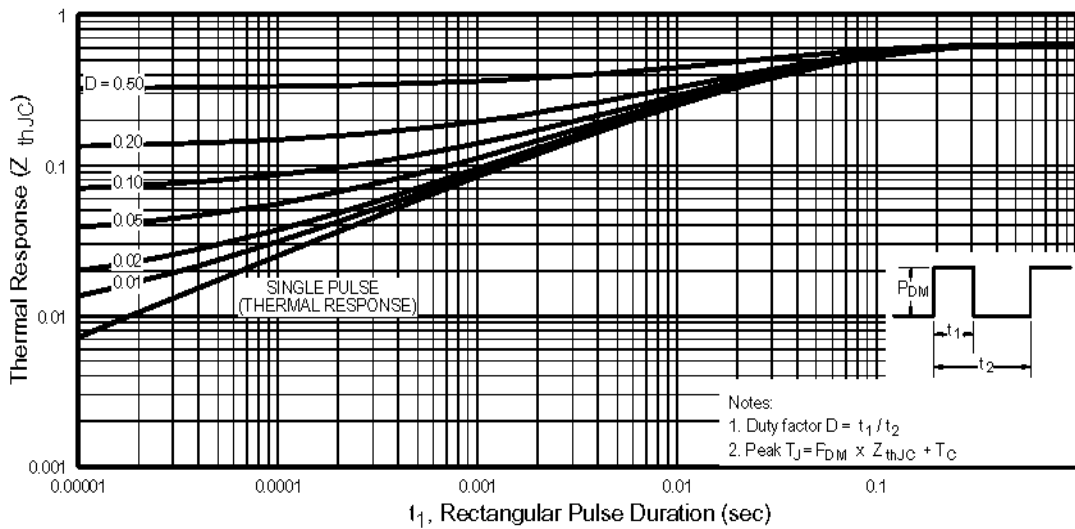


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

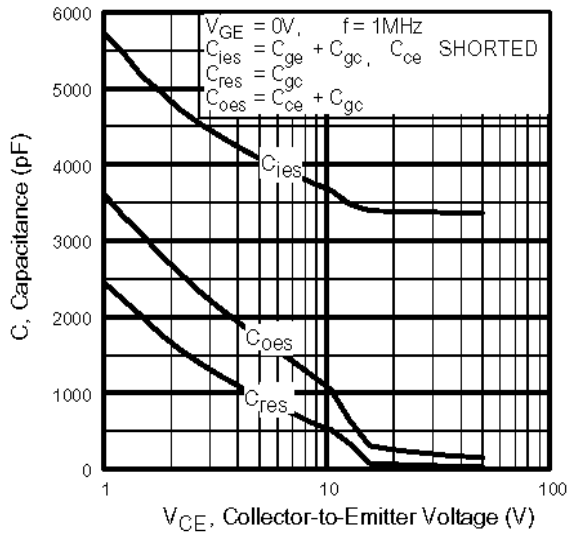


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

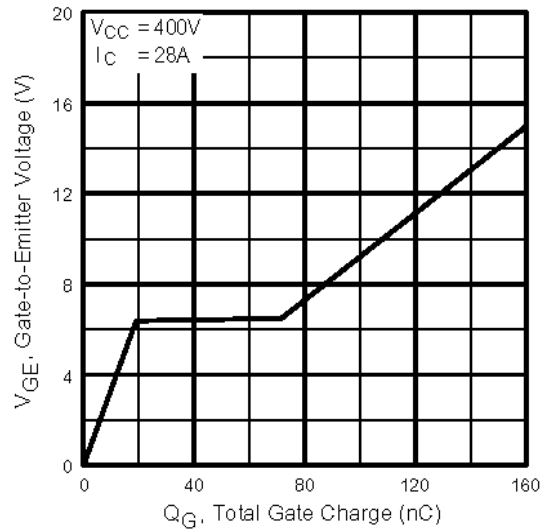


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

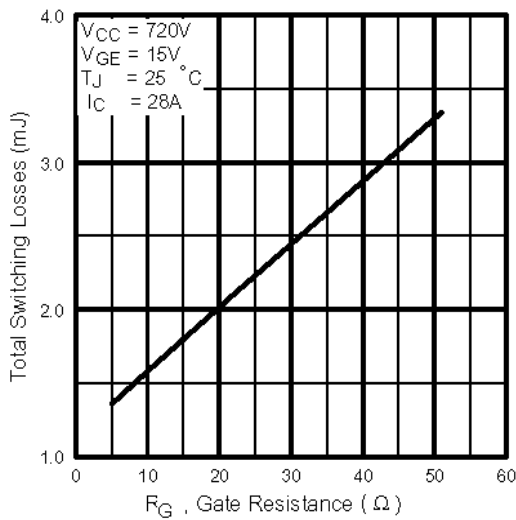


Fig. 9 - Typical Switching Losses vs. Gate Resistance

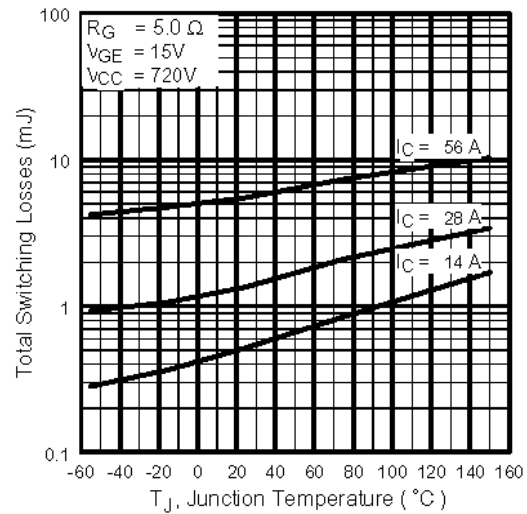


Fig. 10 - Typical Switching Losses vs. Junction Temperature

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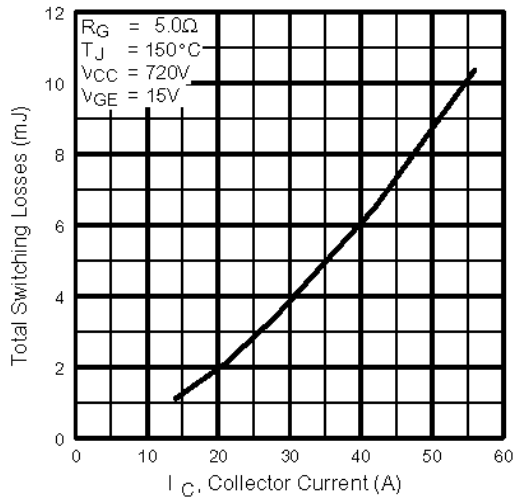


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

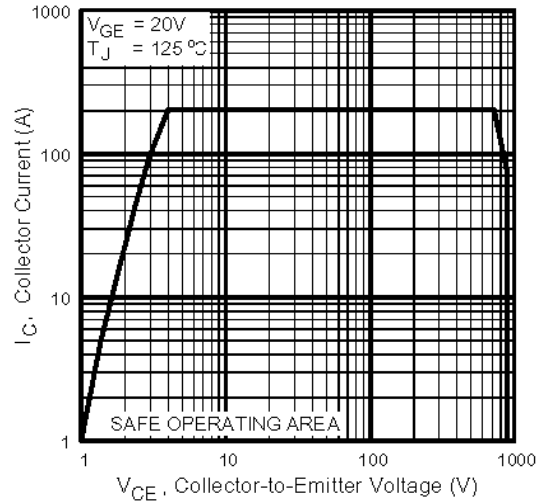
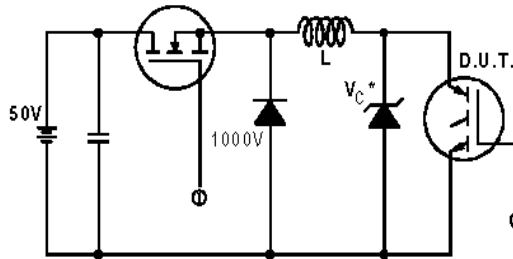


Fig. 12 - Turn-Off SOA



* Driver same type as D.U.T.; $V_c = 80\%$ of $V_{ce(max)}$
 * Note: Due to the 50V power supply, pulse width and inductor will increase to obtain rated I_d .

Fig. 13a - Clamped Inductive Load Test Circuit

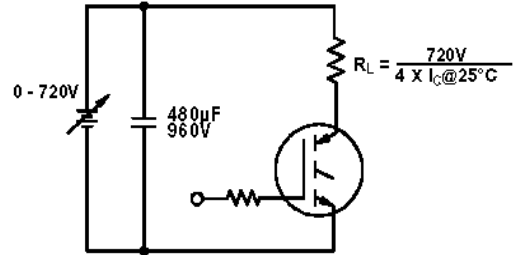


Fig. 13b - Pulsed Collector Current Test Circuit

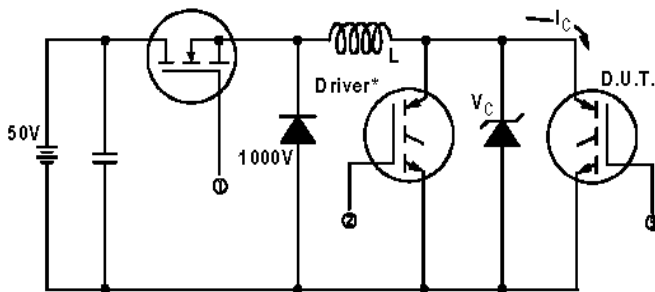


Fig. 14a - Switching Loss Test Circuit

* Driver same type as D.U.T., $V_c = 720V$

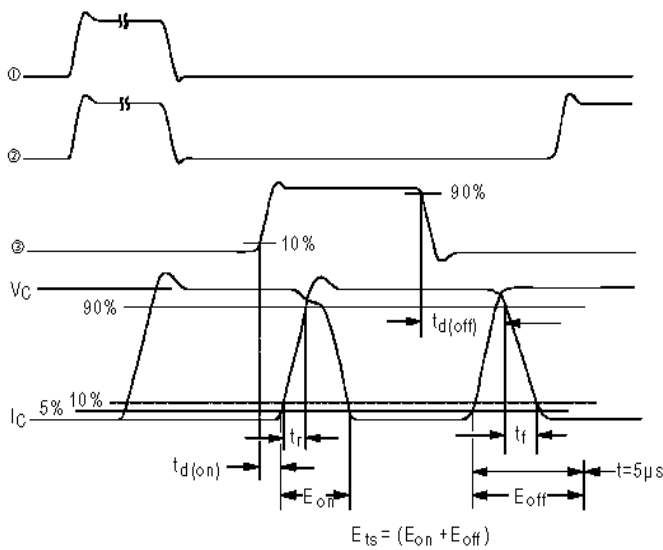


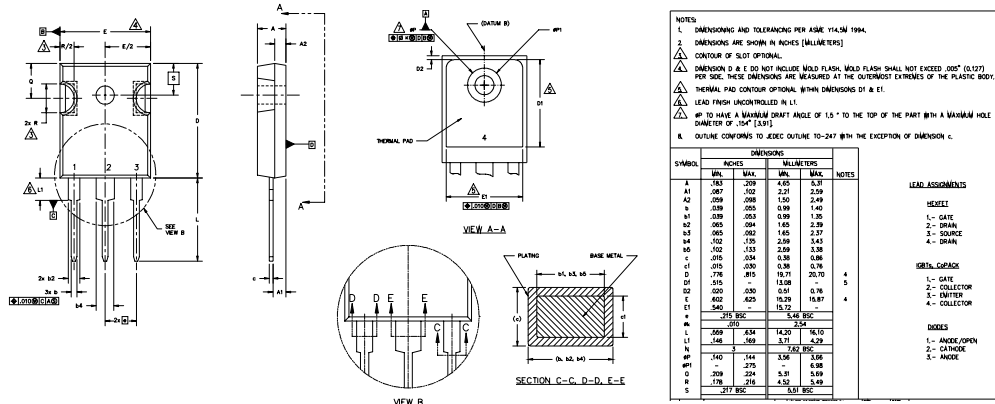
Fig. 14b - Switching Loss Waveforms

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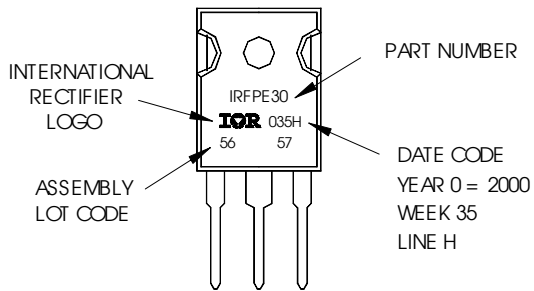
TO-247AC Package Outline

Dimensions are shown in millimeters (inches)



TO-247AC Part Marking Information

EXAMPLE: THIS IS AN IRFP30
 WITH ASSEMBLY
 LOT CODE 5657
 ASSEMBLED ON WW 35, 2000
 IN THE ASSEMBLY LINE "H"
Note: "P" in assembly line
 position indicates "Lead-Free"



Data and specifications subject to change without notice.

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 TAC Fax: (310) 252-7903

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