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## EGP20A - EGP20K 2.0 A Glass-Passivated High-Efficiency Rectifiers

### Features

- Glass-Passivated Cavity-Free Junction
- High Surge Current Capability
- Low Leakage Current
- Super-Fast Recovery Time for High Efficiency
- · Low Forward Voltage, High Current Capability

DO-15 Glass case

June 2013

COLOR BAND DENOTES CATHODE

### Absolute Maximum Ratings<sup>(1)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

| Symbol                            | Parameter   | Value      | Units |  |
|-----------------------------------|---|------------|-------|--|
| I <sub>F(AV)</sub>                | Average Rectified Current<br>.375 inch lead length at $T_A = 55^{\circ}C$                               | 2.0        | A     |  |
| I <sub>FSM</sub>                  | Peak Forward Surge Current<br>8.3 ms single half-sine-wave<br>Superimposed on rated load (JEDEC method) | 75         | A     |  |
| T <sub>J</sub> , T <sub>STG</sub> | Junction and Storage Temperature Range  | -65 to 150 | °C    |  |

### Note:

1. These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

### **Thermal Characteristics**

| P <sub>D</sub>        | Total Device Dissipation                | 3.13 | W    |
|-----------------------|---|------|------|
|                       | Derate above 25°C                       | 25   | mW°C |
| $R_{	extsf{	heta}JA}$ | Thermal Resistance, Junction to Ambient | 40   | °C/W |
| R <sub>θJL</sub>      | Thermal Resistance, Junction to Lead    | 15   | °C/W |

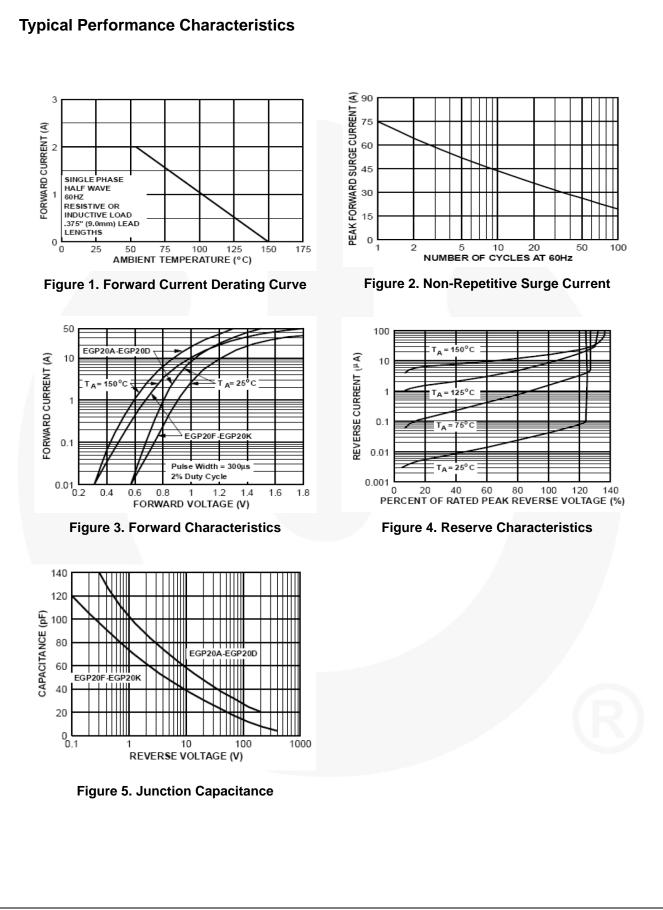
### **Electrical Characteristics**<sup>(2)</sup>

 $T_A = 25^{\circ}C$  unless otherwise noted.

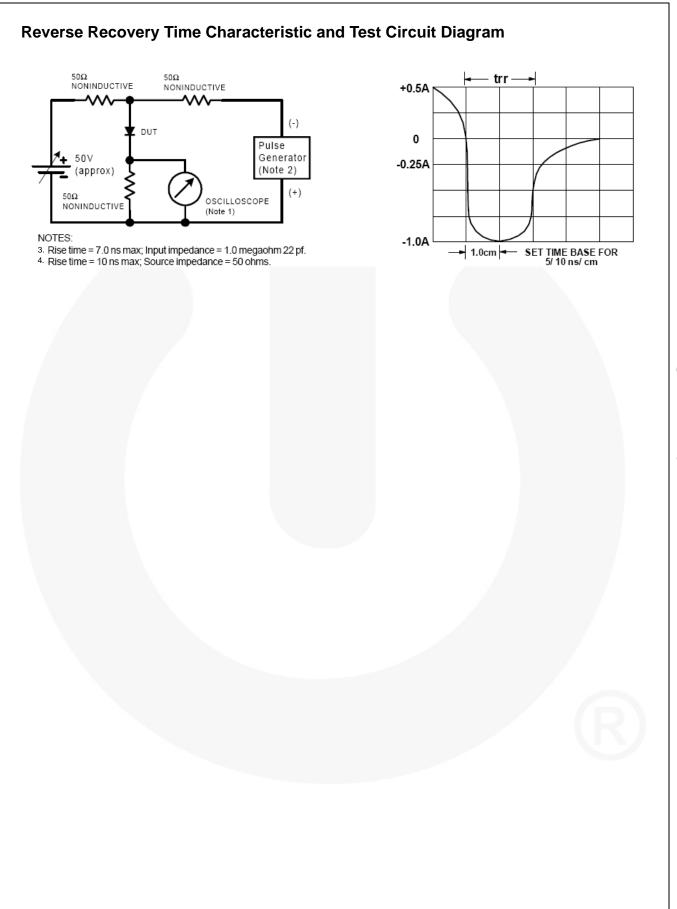
| Parameter   |                        | Device |       |     |     |      |     |      |       | Units |
|---|------------------------|--------|-------|-----|-----|------|-----|------|-------|-------|
| Faranie   | 20A                    | 20B    | 20C   | 20D | 20F | 20G  | 20J | 20K  | Units |       |
| Peak Repetitive Reverse Voltage   |                        | 50     | 100   | 150 | 200 | 300  | 400 | 600  | 800   | V     |
| Maximum RMS Voltage   |                        | 35     | 70    | 105 | 140 | 210  | 280 | 420  | 560   | V     |
| DC Reverse Voltage (Rated V <sub>R</sub> )                                      |                        | 50     | 100   | 150 | 200 | 300  | 400 | 600  | 800   | V     |
| Maximum Reverse   | $T_A = 25^{\circ}C$    |        | 5.0   |     |     |      |     |      |       | μΑ    |
| Current at Rated V <sub>R</sub>   | T <sub>A</sub> = 125°C |        | 100   |     |     |      |     |      | μΑ    |       |
| Maximum Reverse-Recovery Time $I_F = 0.5 A$ , $I_R = 1.0 A$ , $I_{rr} = 0.25 A$ |                        |        | 50 75 |     |     |      | 5   | ns   |       |       |
| Maximum Forward Voltage at 2.0 A  |                        |        | 0.95  |     |     | 1.25 |     | 1.70 |       | V     |
| Typical Junction Capacitance $V_R = 4.0 V$ , f = 1.0 MHz                        |                        |        | 70    |     |     | 45   |     |      | pF    |       |

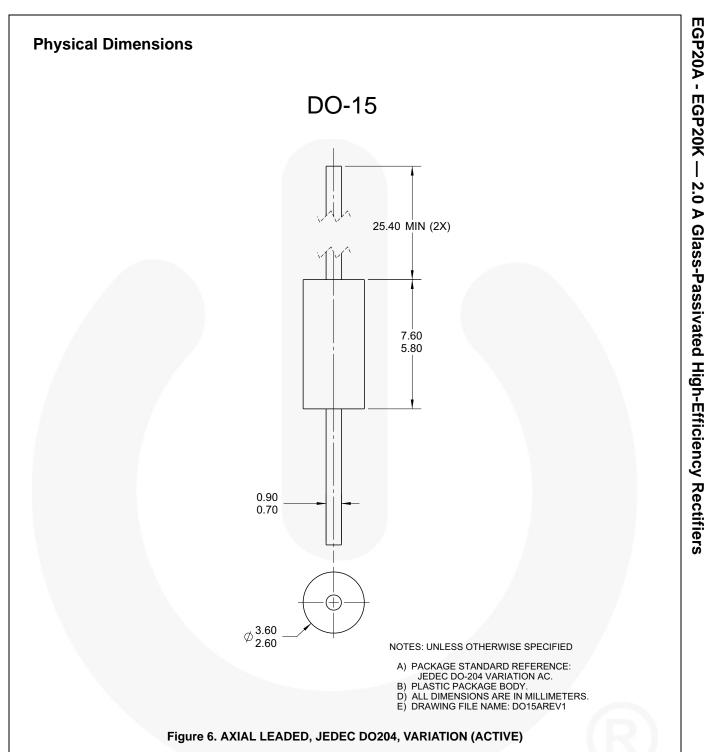
### Note:

**2.** Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2%.



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|--|-----------------------|--|
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| Preliminary First Production             |                       | Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild<br>Semiconductor reserves the right to make changes at any time without notice to improve design. |
| No Identification Needed Full Production |                       | Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.  |
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