TLP160G

<u>TOSHIBA</u>

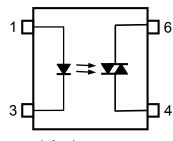
TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP160G

Triac Drive Unit: mm **Programmable Controllers AC-Output Module** Solid State Relay The TOSHIBA mini flat coupler TLP160G is a small outline coupler, suitable for surface mount assembly. The TLP160G consists of a photo triac, optically coupled to a gallium arsenide infrared emitting diode. Peak off-state voltage: 400 V (min) 0.5MIN Trigger LED current: 10 mA (max) 7.0 ± 0.4 2.54 On-state current: 70 mA (max) Isolation voltage: 2500 Vrms (min) 11-4C3 UL approved: UL1577, File No.E67349 cUL approved : CSA Component Acceptance Service No. 5A, File No.E67349 TOSHIBA 11-4C3 • Option (V4) VDE approved : EN60747-5-5 (Note1)

Weight: 0.09 g (typ.)

Pin Configurations (top view)



1. Anode

3. Cathode

4. Triac Terminal
 6. Triac Terminal

Trigger LED Current

-				
Classification	Trigger LED	Marting		
Classification (Note 1)	VT=3V,	Marking of Classification		
(Note I)	Min	Max	Classification	
(IFT5)	- //	5.0	T5	
(IFT7)	\sim \sim $-$	7.0	T5, T7	
Standard		10.0	T5, T7, blank	

Note 1: When a EN60747-5-5 approved type is needed, please designate "Option(V4)"

Note 1: (IFT5); TLP160G (IFT5) Note: Application type name for certification test, please use standard product type name, i.e. TLP160G(IFT5): TLP160G

Start of commercial production 1988-04

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit		
	Forward current		lF	50	mA	
	Forward current derating (Ta ≥ 53°C)		ΔI _F / °C	-0.7	mA / °C	
	Peak forward current (100µs pulse, 100 pps)		IFP	1	A	
LED	Reverse voltage		VR	5	v (
	Diode power dissipation		PD	100	mW	\bigcirc
	Diode power dissipation derating (Ta \geq 53°C)		$\Delta P_D /°C$	-1.4	mW/°C	\sim
	Junction temperature		Tj	125	°C	\mathcal{O}
	Off- state output terminal voltage		Vdrm	400	×	
	On-state RMS current	Ta=25°C	- I _{T(RMS)}	70	mA	
		Ta=70°C		40		
Ļ	On-state current derating (Ta ≥ 25°C) Peak on–state current (100µs pulse, 120 pps)		ΔI _T / °C	-0.67	mA / °C	
Detector			ITP	(2)	A	$\langle \mathcal{O} \rangle$
Det	Peak non-repetitive surge current (P _W =10ms)		Ітѕм	1.2	A <	$\langle \langle \rangle \rangle$
	Output power dissipation		Po	200	mW	
	Output power dissipation derating (Ta $\ge 25^{\circ}$ C)		ΔPo/°C	-2.0	mW/°C	\mathcal{D}
	Junction temperature		Tj	115	°°C	
Storage temperature range		Tstg	-55 to 125	Ŷ		
Operating temperature range		Topr	-40 to 100	ů		
Lead soldering temperature (10s)		T _{sol}	260) °C		
Isolatio	Isolation voltage (AC, 1 minute, R.H. ≤ 60%) (Note 1))) bvs	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	-	-	120	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	ITP	-	-	1	А
Operating temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

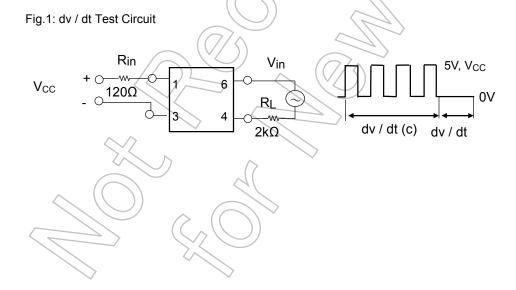
Note 1: Device considered a two terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

Electrical Characteristics (Ta = 25°C)

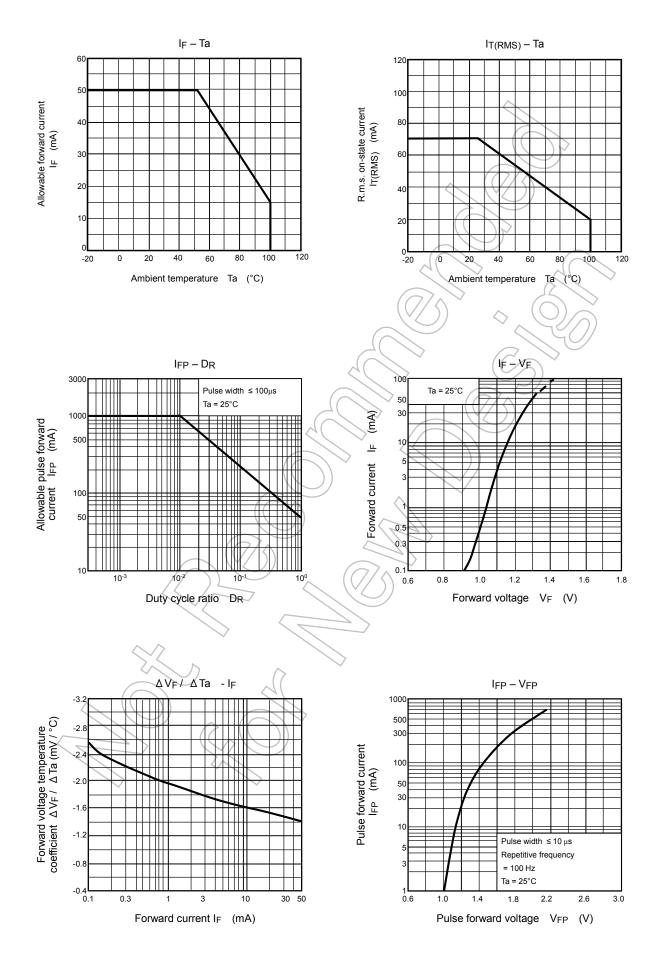
	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10mA	1.0	1.15	1.3	V
LED	Reverse current	IR	V _R = 5V	-	_	10	μA
	Capacitance	CT	VF = 0 V, f = 1 MHz	X	30		pF
	Peak off-state current	IDRM	V _{DRM} = 400 V		10	1000	nA
	Peak on-state voltage	Vтм	I _{TM} = 70 mA	K) 1.7	2.8	V
ctor	Holding current	Ін	(0		0.6	-	mA
Detector	Critical rate of rise of off-state voltage	dv / dt	V _{in} = 120 Vrms, Ta = 85 °C (Fig.1)	200	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	IT = 15 mA, Vin= 30 Vrms (Fig.1)	> _	0.2	_	V / µs

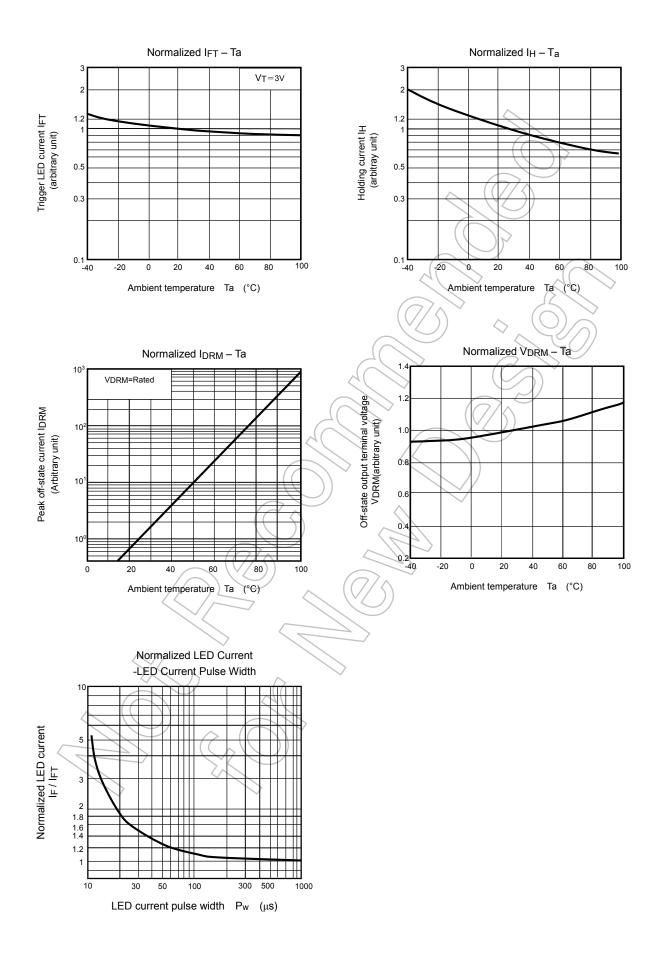
Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	IFT	V _T = 3V	\sim	5	10	mA
Capacitance input to output	Cs	V _S = 0 V, f = 1 MHz	62	0.8		pF
Isolation resistance	Rs	Vs = 500V, R.H. ≤ 60%	1×10 ¹²	10 ¹⁴	_	Ω
		AC, 60 s	2500	_	_	Vrms
Isolation voltage	BVs	AC, 1 s, in oil	2 –	5000		VIIIIS
		DC, 60 s, in oil	—	5000		Vdc
Turn–on time	ton	V _D = 6→4 V, R _L = 100Ω I _F = Rated I _{FT} ×1.5	_	30	100	μs



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