

High Performance Schottky Rectifier, 1 A



DO-214AC (SMA)

PRODUCT SUMMARY				
Package	DO-214AC (SMA)			
I _{F(AV)}	1 A			
V _R	100 V			
V _F at I _F	0.63 V			
I _{RM}	1 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single die			
E _{AS}	1.0 mJ			

FEATURES





Guard ring for enhanced ruggedness and long term reliability

ROHS COMPLIANT HALOGEN FREE

- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10MQ100-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1	А	
V _{RRM}		100	V	
I _{FSM}	t _p = 5 μs sine	120	А	
V _F	1.5 A _{pk} , T _J = 125 °C	0.68	V	
T _J	Range	-55 to +150	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-10MQ100-M3	UNITS
Maximum DC reverse voltage	V_R	100	V
Maximum working peak reverse voltage	V_{RWM}	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at T_L = 126 °C On PC board 9 mm ² island (0.013 mm thick copper pad are	. •	1.5	
See fig. 4	I _{F(AV)}	50% duty cycle at $T_L = 135 \degree C$ On PC board 9 mm ² island (0.013 mm thick copper pad are	. •	1	А
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	120	
non-repetitive surge current, $T_J = 25\ ^{\circ}\text{C}$ See fig. 6	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	30	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.5 \text{A}, L = 8 \text{mH}$		1.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 0.5		Α	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T _J = 25 °C	0.78	V
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1.5 A		0.85	
See fig. 1	V _{FM} (')	1 A	T _J = 125 °C	0.63	
		1.5 A		0.68	
Maximum reverse leakage current		T _J = 25 °C	V _R = Rated V _R	0.1	- mA
See fig. 2	I _{RM}	T _J = 125 °C		1	
Threshold voltage	V _{F(TO)}	$T_{J} = T_{J} \text{ maximum} $ 0.52 78.4		0.52	V
Forward slope resistance	r _t			mΩ	
Typical junction capacitance	C _T	V _R = 10 V _{DC} , T _J = 25 °C, test signal = 1 MHz 38		pF	
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0 n		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/μ		V/µs	

Note

 $^{^{(1)}}$ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	OZ.
Marking device		Case style SMA (similar D-64)	1,	J

Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

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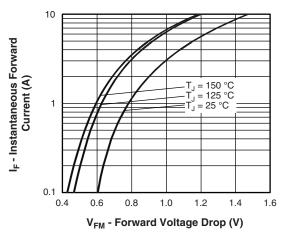


Fig. 1 - Maximum Forward Voltage Drop Characteristics

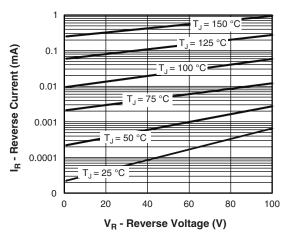


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

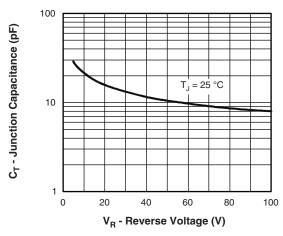
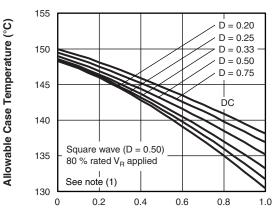
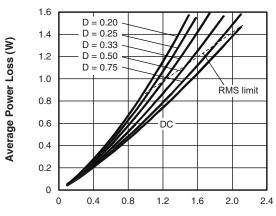


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



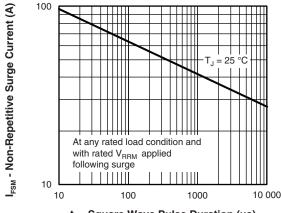
I_{F(AV)} - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature



I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



t_p - Square Wave Pulse Duration (μs)

Fig. 6 - Maximum Peak Surge Forward Current vs.
Pulse Duration

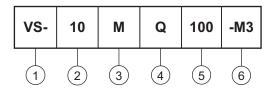
Note

(1) Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating

3 - M = SMA

4 - Q = Schottky "Q" series

5 - Voltage rating (100 = 100 V)

6 - Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-10MQ100-M3/5AT	5AT	7500	13" diameter plastic tape and reel		

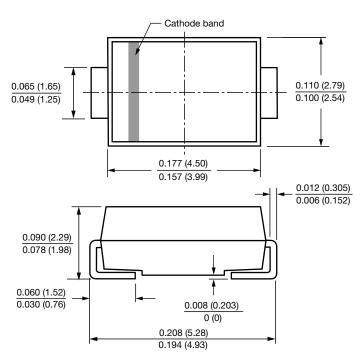
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95400		
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		



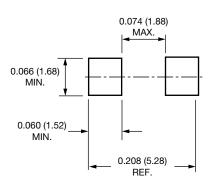
SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)



Mounting Pad Layout





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