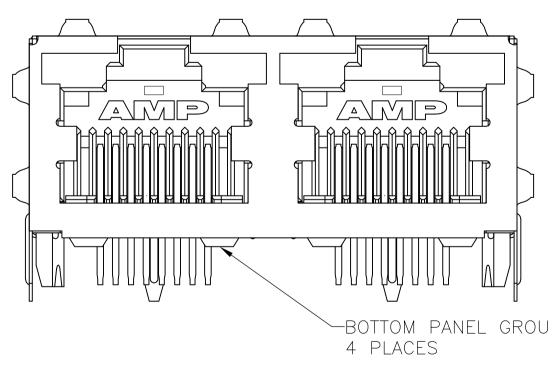


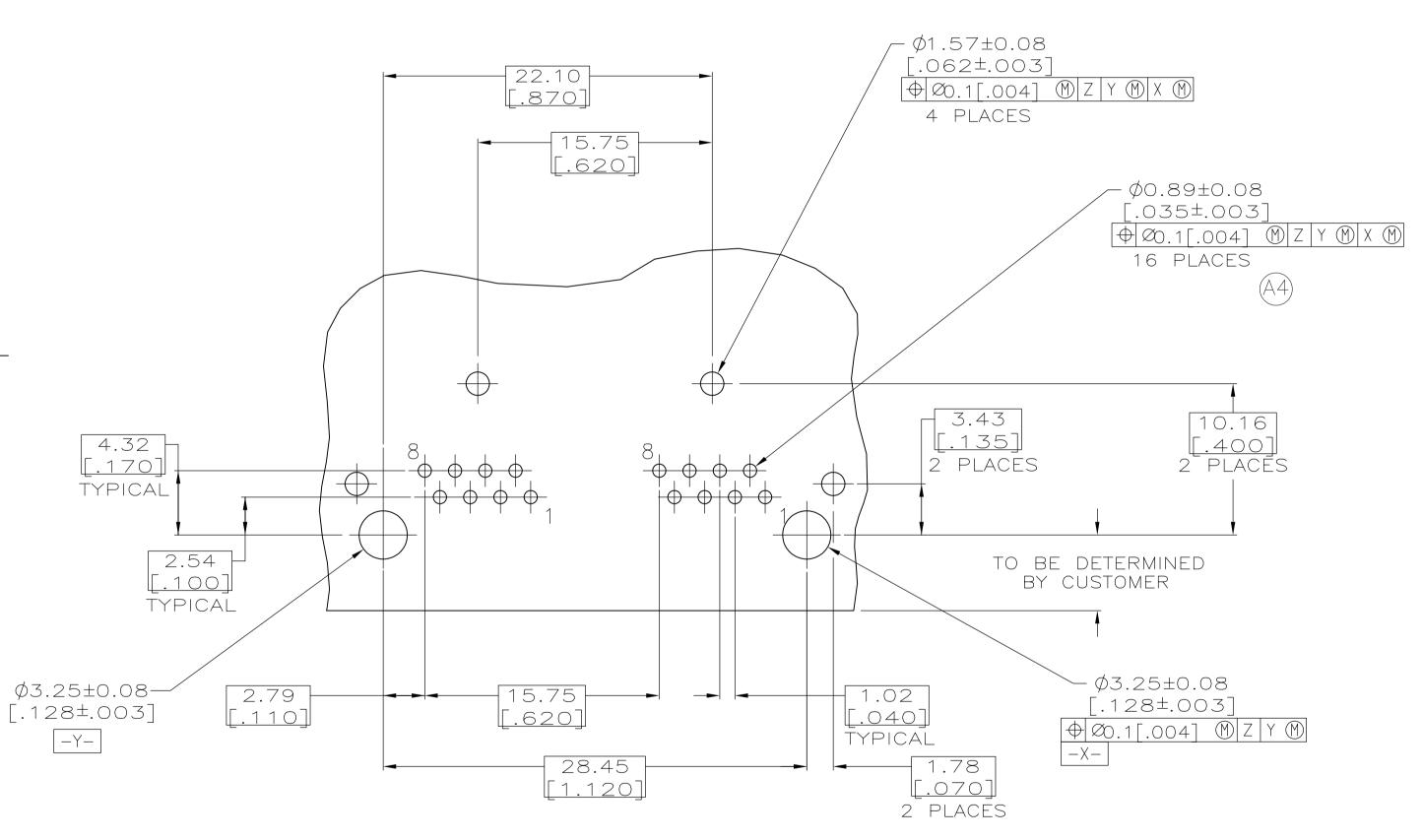
В

А

AMP 4805 REV 31MAR2000



PN 6368459-4 SAME AS PN 6368459-3 EXCEPT AS SHOWN WITH BOTTOM GROUND PANEL TAB



-BOTTOM PANEL GROUND TAB

## SUGGESTED PRINTED CIRCUIT BOARD LAYOUT (COMPONENT SIDE)

THIS DRAWING IS A DIMENSIONS: mm  $\bigcirc \sub$ iaterial SEE NOTE 1

		2					1			
	loc	dist 22	REVISIONS							
			Ρ	LTR		DESCRIPTION		DATE	DWN	APVD
				A4	ECR-10-0114	186		02JUN2010	KS	SY

- 1. MATERIAL: HOUSING HIGH TEMPERATURE THERMOPLASTIC, BLACK, UL94V-0; TERMINALS – 0.36[.014] THICK PHOS BRONZE PLATED WITH 3.81 $\mu$ m[.000150] MINIMUM THICK MATTE TIN IN SOLDER AREA. 1.27  $\mu$ m [.000050] MINIMUM GOLD IN LOCALIZED PLATE AREA. ENTIRE TERMINAL PLATED WITH 1.27  $\mu m$ [.000050] MINIMUM THICK NICKEL. SHIELD – 0.196[.0077] THICK COPPER ZINC ALLOY PREPLATED WITH  $1.27 \mu m$ [.000050] MINIMUM SATIN NICKEL WITH 2.03 $\mu$ m[.000080] MINIMUM TIN POST DIPPED ON PCB GROUND TABS.
- 2. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIIONS PART 68, SUBPART F.
- $\triangle$  suggested panel opening dimensions.
- $\triangle$  suggested clearance between top of connector AND TOP PANEL OPENING.

В

A THE PART IS PRELIMINARY.

<u>۸</u>						
W BOTTOM PANE	GROUND TAB 3.68 [.145	6368459-4				
✓S w∕o bottom pane	GROUND TAB 3.68 [.145	6368459-3				
W/O BOTTOM PANE	GROUND TAB 3.05 [.120	6368459-1				
PANELL GRO	UND TAB DIM. A	PART NUMBER				
CONTROLLED DOCUMENT. D.L.A.MAYER CHK 17.		Tyco Electronics Corporation <b>Electronics</b> Harrisburg, Pa 17105–3608				
TOLERANCES UNLESS OTHERWISE SPECIFIED: J.WESTMAN   0 PLC ± - APVD 17.   1 PLC ± - PRODUCT SPEC   2 PLC ± 0.25[.01] 108-1163-   3 PLC ± 0.13[.005] APPLICATION SPEC	INVERTED MODULA					
4 PLC ± - 114-2154 ANGLES ± - 114-2154 FINISH WEIGHT -	size cage code drawing no A 1 00779 <b>C=</b> 63684	459 RESTRICTED TO				
CUSTOMER DRAW	NG SCALE	4:1 SHEET OF REV A:1 A4				

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TE Connectivity: 6368459-1