

BFU520Y

Dual NPN wideband silicon RF transistor Rev. 1 — 20 February 2014

Product data sheet

Product profile

1.1 General description

Dual NPN silicon RF transistor for high speed, low noise applications in a plastic, 6-pin SOT363 package.

The BFU520Y is part of the BFU5 family of transistors, suitable for small signal to medium power applications up to 2 GHz.

1.2 Features and benefits

- Low noise, high breakdown RF transistor
- AEC-Q101 qualified
- Minimum noise figure (NF_{min}) = 0.65 dB at 900 MHz
- Maximum stable gain 19 dB at 900 MHz
- 11 GHz f_T silicon technology

1.3 Applications

- Applications requiring high supply voltages and high breakdown voltages
- Broadband differential amplifiers up to 2 GHz
- Low noise amplifiers for ISM applications
- ISM band oscillators

1.4 Quick reference data

Quick reference data

T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CB}	collector-base voltage	open emitter		-	-	24	V
V _{CE}	collector-emitter voltage	open base		-	-	12	V
		shorted base		-	-	24	V
V_{EB}	emitter-base voltage	open collector		-	-	2	V
I _C	collector current			-	5	30	mΑ
P _{tot}	total power dissipation	T _{sp} ≤ 87 °C	<u>[1]</u>	-	-	450	mW
h _{FE}	DC current gain	$I_C = 5 \text{ mA}; V_{CE} = 8 \text{ V}$		60	95	200	
C _c	collector capacitance	$V_{CB} = 8 \text{ V}; f = 1 \text{ MHz}$		-	0.48	-	pF
f _T	transition frequency	$I_C = 10 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $f = 900 \text{ MHz}$		-	10	-	GHz



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Table 1. Quick reference data ...continued

T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$G_{p(max)}$	maximum power gain	$I_C = 5 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $f = 900 \text{ MHz}$	[2] -	19	-	dB
NF_{min}	minimum noise figure	I_C = 1 mA; V_{CE} = 8 V; f = 900 MHz; Γ_S = Γ_{opt}	-	0.65	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	I_C = 10 mA; V_{CE} = 8 V; Z_S = Z_L = 50 Ω ; f = 900 MHz	-	7.0	-	dBm

- [1] T_{sp} is the temperature at the solder point of the collector lead.
- [2] If K > 1 then $G_{p(max)}$ is the maximum power gain. If K < 1 then $G_{p(max)} = MSG$.

2. Pinning information

Table 2. Discrete pinning

Pin	Description	Simplified outline	Graphic symbol
1	base1	∏6 ∏5 ∏4	
2	emitter1	F	6 3 J J
3	collector2		1 — 4 —
4	base2	H ₁ H ₂ H ₃	 2 5
5	emitter2		aaa-010460
6	collector1		

3. Ordering information

Table 3. Ordering information

Type number	Package	e	
	Name	Description	Version
BFU520Y	-	plastic surface-mounted package; 6 leads	SOT363

4. Marking

Table 4. Marking

Type number	Marking	Description		
BFU520Y WB*		* = t : made in Malaysia		
		* = w : made in China		

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5. Design support

Table 5. Available design support

Download from the BFU520Y product information page on http://www.nxp.com.

Support item	Available	Remarks
Device models for Agilent EEsof EDA ADS	yes	Based on Mextram device model.
SPICE model	yes	Based on Gummel-Poon device model.
S-parameters	yes	
Noise parameters	yes	
Solder pattern	yes	

6. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CB}	collector-base voltage	open emitter	-	30	V
V_{CE}	collector-emitter voltage	open base	-	16	V
		shorted base	-	30	V
V_{EB}	emitter-base voltage	open collector	-	3	V
I _C	collector current		-	50	mA
T_{stg}	storage temperature		-65	+150	°C
V _{ESD}	electrostatic discharge voltage	Human Body Model (HBM) According to JEDEC standard 22-A114E	-	±150	V
		Charged Device Model (CDM) According to JEDEC standard 22-C101B	-	±2	kV

7. Recommended operating conditions

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CB}	collector-base voltage	open emitter	-	-	24	V
V_{CE}	collector-emitter voltage	open base	-	-	12	V
		shorted base	-	-	24	V
V_{EB}	emitter-base voltage	open collector	-	-	2	V
Ic	collector current		-	-	30	mA
Pi	input power	$Z_S = 50 \Omega$	-	-	10	dBm
Tj	junction temperature		-40	-	+150	°C
P _{tot}	total power dissipation	$T_{sp} \le 87 ^{\circ}C$	<u>[1]</u> _	-	450	mW

^[1] T_{sp} is the temperature at the solder point of the collector lead.

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8. Thermal characteristics

Table 8. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[<u>1]</u> 140	K/W

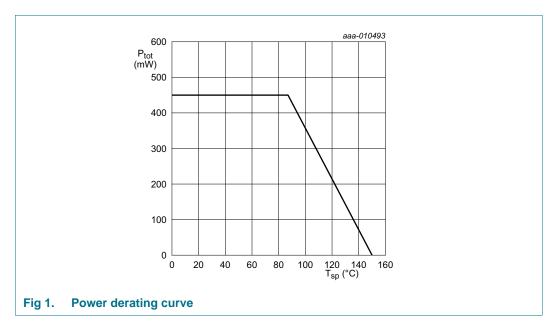
[1] T_{sp} is the temperature at the solder point of the collector lead.

 T_{sp} has the following relation to the ambient temperature $T_{amb}\!:$

 $T_{sp} = T_{amb} + P \times R_{th(sp-a)}$

With P being the power dissipation and $R_{th(sp-a)}$ being the thermal resistance between the solder point and ambient. $R_{th(sp-a)}$ is determined by the heat transfer properties in the application.

The heat transfer properties are set by the application board materials, the board layout and the environment e.g. housing.



9. Characteristics

Table 9. Characteristics

T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100 \text{ nA}; I_E = 0 \text{ mA}$	24	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 150 \text{ nA}; I_B = 0 \text{ mA}$	12	-	-	V
I_{C}	collector current		-	5	30	mΑ
I_{CBO}	collector-base cut-off current	$I_E = 0 \text{ mA}; V_{CB} = 8 \text{ V}$	-	<1	-	nΑ
h _{FE}	DC current gain	$I_C = 5 \text{ mA}; V_{CE} = 8 \text{ V}$	60	95	200	
C _e	emitter capacitance	$V_{EB} = 0.5 \text{ V}; f = 1 \text{ MHz}$	-	0.64	-	pF
C_{re}	feedback capacitance	$V_{CE} = 8 \text{ V}; f = 1 \text{ MHz}$	-	0.30	-	pF
C _c	collector capacitance	$V_{CB} = 8 \text{ V}; f = 1 \text{ MHz}$	-	0.48	-	pF
f_{T}	transition frequency	$I_C = 10 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $f = 900 \text{ MHz}$	-	10	-	GHz

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Table 9. Characteristics ... continued $T_{amb} = 25$ °C unless otherwise specified

Symbol	Parameter	Conditions	Mi	1 Тур	Max	Unit
$G_{p(max)}$	maximum power gain	$f = 433 \text{ MHz}; V_{CE} = 8 \text{ V}$	<u>[1]</u>			
		I _C = 1 mA	-	16.5	-	dB
		$I_C = 5 \text{ mA}$	-	23	-	dB
		I _C = 10 mA	-	24	-	dB
		$f = 900 \text{ MHz}; V_{CE} = 8 \text{ V}$	<u>[1]</u>			
		I _C = 1 mA	-	14.5	-	dB
		$I_C = 5 \text{ mA}$	-	19	-	dB
		I _C = 10 mA	-	20	-	dB
		f = 1800 MHz; V _{CE} = 8 V	<u>[1]</u>			
		I _C = 1 mA	-	11.5	-	dB
		$I_C = 5 \text{ mA}$	-	14.5	-	dB
		I _C = 10 mA	-	14	-	dB
$ s_{21} ^2$	insertion power gain	$f = 433 \text{ MHz}; V_{CE} = 8 \text{ V}$				
		I _C = 1 mA	-	10.5	-	dB
		$I_C = 5 \text{ mA}$	-	20	-	dB
		I _C = 10 mA	-	22	-	dB
		$f = 900 \text{ MHz}; V_{CE} = 8 \text{ V}$				
		I _C = 1 mA	-	9	-	dB
		$I_C = 5 \text{ mA}$	-	16	-	dB
		I _C = 10 mA	-	17	-	dB
		f = 1800 MHz; V _{CE} = 8 V				
		I _C = 1 mA	-	6.5	-	dB
		$I_C = 5 \text{ mA}$	-	11	-	dB
		I _C = 10 mA	-	11.5	-	dB
NF _{min}	minimum noise figure	f = 433 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	0.6	-	dB
		$I_C = 5 \text{ mA}$	-	0.7	-	dB
		I _C = 10 mA	-	0.9	-	dB
		f = 900 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	0.65	-	dB
		I _C = 5 mA	-	0.8	-	dB
		I _C = 10 mA	-	0.95	-	dB
		f = 1800 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	0.8	-	dB
		$I_C = 5 \text{ mA}$	-	0.85	-	dB
		I _C = 10 mA		1.0		dB

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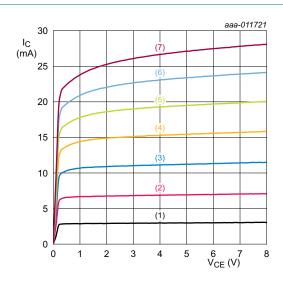
Table 9. Characteristics ... continued $T_{amb} = 25$ °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _{ass}	associated gain	f = 433 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	25	-	dB
		I _C = 5 mA	-	24	-	dB
		I _C = 10 mA	-	24	-	dB
		f = 900 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	17	-	dB
		I _C = 5 mA	-	18	-	dB
		I _C = 10 mA	-	18	-	dB
		f = 1800 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	10.5	-	dB
		I _C = 5 mA	-	12	-	dB
		I _C = 10 mA	-	12.5	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	f = 433 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 5 mA	-	1	-	dBm
		I _C = 10 mA	-	6	-	dBm
		f = 900 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 5 mA	-	2	-	dBm
		I _C = 10 mA	-	7	-	dBm
		f = 1800 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 5 mA	-	4	-	dBm
		I _C = 10 mA	-	8.5	-	dBm
IP3 _o	output third-order intercept point	f_1 = 433 MHz; f_2 = 434 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		I _C = 5 mA	-	10	-	dBm
		I _C = 10 mA	-	16	-	dBm
		f_1 = 900 MHz; f_2 = 901 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		$I_C = 5 \text{ mA}$	-	11	-	dBm
		I _C = 10 mA	-	17	-	dBm
		f_1 = 1800 MHz; f_2 = 1801 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		$I_C = 5 \text{ mA}$	-	14	-	dBm
		I _C = 10 mA	-	18	-	dBm

^[1] If K > 1 then $G_{p(max)}$ is the maximum power gain. If K < 1 then $G_{p(max)} = MSG$.

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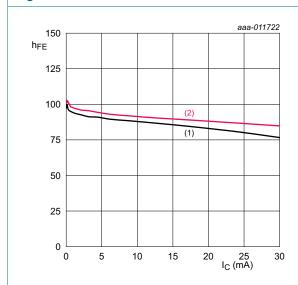
9.1 Graphs



 $T_{amb} = 25 \, ^{\circ}C.$

- (1) $I_B = 25 \mu A$
- (2) $I_B = 75 \mu A$
- (3) $I_B = 125 \mu A$
- (4) $I_B = 175 \mu A$
- (5) $I_B = 225 \mu A$
- (6) $I_B = 275 \mu A$
- (7) $I_B = 325 \mu A$

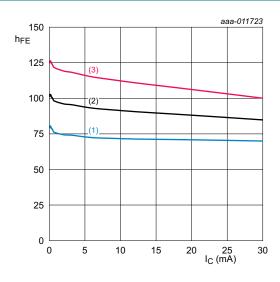
Fig 2. Collector current as a function of collector-emitter voltage; typical values



 $T_{amb} = 25 \, ^{\circ}C.$

- (1) $V_{CE} = 3.0 \text{ V}$
- (2) $V_{CE} = 8.0 \text{ V}$

Fig 3. DC current gain as function of collector current; typical values



 $V_{CE} = 8 V.$

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +125 \, ^{\circ}C$

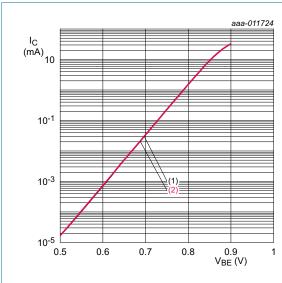
Fig 4. DC current gain as function of collector current; typical values

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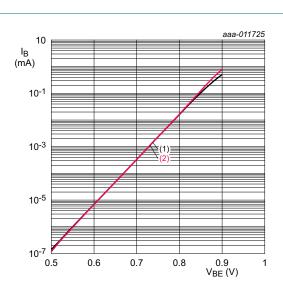
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 T_{amb} = 25 °C.

- (1) $V_{CE} = 3.0 \text{ V}$
- (2) $V_{CE} = 8.0 \text{ V}$

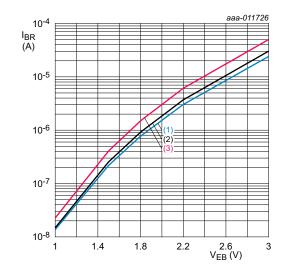
Fig 5. Collector current as a function of base-emitter voltage; typical values



 $T_{amb} = 25 \, ^{\circ}C.$

- (1) $V_{CE} = 3.0 \text{ V}$
- (2) $V_{CE} = 8.0 \text{ V}$

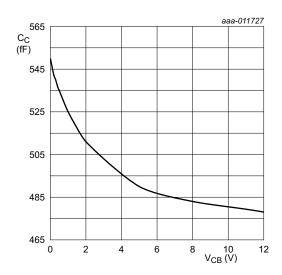
Fig 6. Base current as a function of base-emitter voltage; typical values



 $V_{CE} = 3 \text{ V}.$

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +125 \, ^{\circ}C$

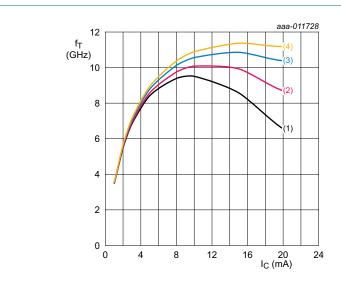
Fig 7. Reverse base current as a function of emitter-base voltage; typical values



 I_C = 0 mA; f = 1 MHz; T_{amb} = 25 °C.

Fig 8. Collector capacitance as a function of collector-base voltage; typical values

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 $T_{amb} = 25 \, ^{\circ}C.$

- (1) $V_{CE} = 3.3 \text{ V}$
- (2) $V_{CE} = 5.0 \text{ V}$
- (3) $V_{CE} = 8.0 \text{ V}$
- (4) $V_{CE} = 12.0 \text{ V}$

Fig 9. Transition frequency as a function of collector current; typical values

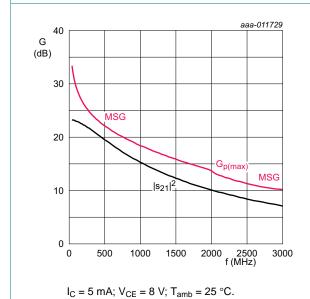
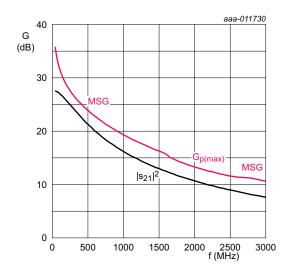


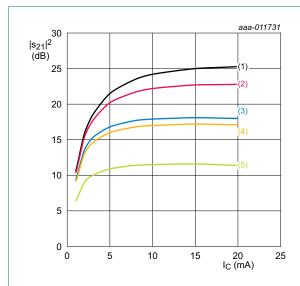
Fig 10. Gain as a function of frequency; typical values



 I_{C} = 10 mA; V_{CE} = 8 V; T_{amb} = 25 °C.

Fig 11. Gain as a function of frequency; typical values

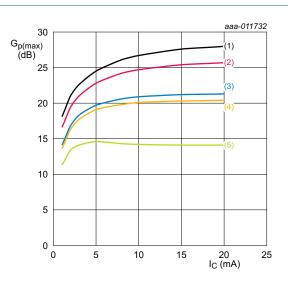
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 V_{CE} = 8 V; T_{amb} = 25 °C.

- (1) f = 300 MHz
- (2) f = 433 MHz
- (3) f = 800 MHz
- (4) f = 900 MHz
- (5) f = 1800 MHz





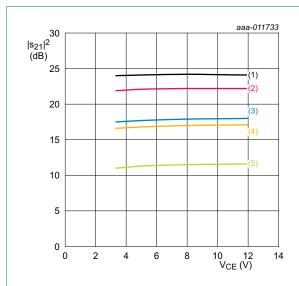
 V_{CE} = 8 V; T_{amb} = 25 °C.

If K >1 then $G_{p(max)}$ = maximum power gain. If K < 1 then $G_{p(max)}$ = MSG.

- (1) f = 300 MHz
- (2) f = 433 MHz
- (3) f = 800 MHz
- (4) f = 900 MHz
- (5) f = 1800 MHz

Fig 13. Maximum power gain as a function of collector current; typical values

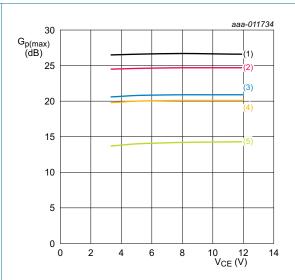
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 I_C = 10 mA; T_{amb} = 25 °C.

- (1) f = 300 MHz
- (2) f = 433 MHz
- (3) f = 800 MHz
- (4) f = 900 MHz
- (5) f = 1800 MHz

Fig 14. Insertion power gain as a function of collector-emitter voltage; typical values



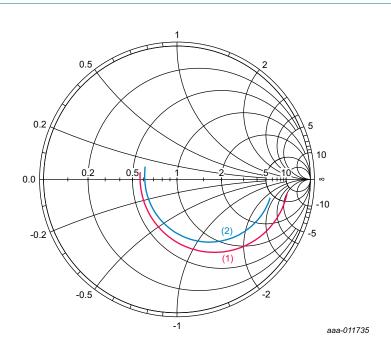
 I_C = 10 mA; T_{amb} = 25 °C.

If K >1 then $G_{p(max)}$ = maximum power gain. If K < 1 then $G_{p(max)}$ = MSG.

- (1) f = 300 MHz
- (2) f = 433 MHz
- (3) f = 800 MHz
- (4) f = 900 MHz
- (5) f = 1800 MHz

Fig 15. Maximum power gain as a function of collector-emitter voltage; typical values

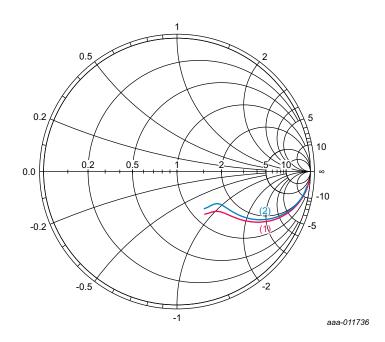
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 $V_{CE}=8~V;~40~MHz \leq f \leq 3~GHz.$

- (1) $I_C = 5 \text{ mA}$
- (2) $I_C = 10 \text{ mA}$

Fig 16. Input reflection coefficient (s₁₁); typical values



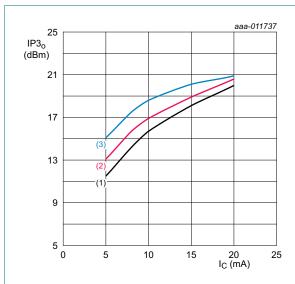
 V_{CE} = 8 V; 40 MHz \leq f \leq 3 GHz.

- (1) $I_C = 5 \text{ mA}$
- (2) $I_C = 10 \text{ mA}$

Fig 17. Output reflection coefficient (s_{22}) ; typical values

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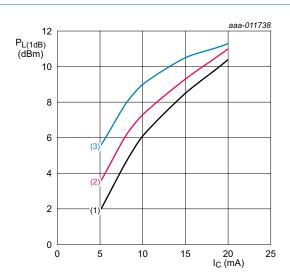
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 $V_{CE} = 8 \text{ V}; T_{amb} = 25 \,^{\circ}\text{C}.$

- (1) $f_1 = 433 \text{ MHz}$; $f_2 = 434 \text{ MHz}$
- (2) $f_1 = 900 \text{ MHz}$; $f_2 = 901 \text{ MHz}$
- (3) $f_1 = 1800 \text{ MHz}$; $f_2 = 1801 \text{ MHz}$

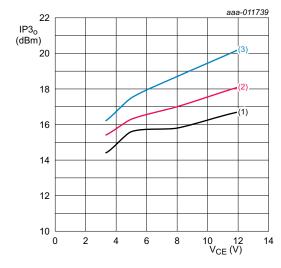
Fig 18. Output third-order intercept point as a function of collector current; typical values



 $V_{CE} = 8 \text{ V}; T_{amb} = 25 \text{ }^{\circ}\text{C}.$

- (1) f = 433 MHz
- (2) f = 900 MHz
- (3) f = 1800 MHz

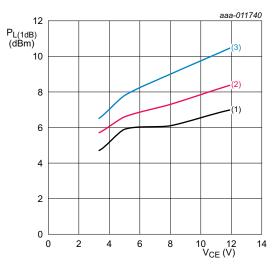
Fig 19. Output power at 1 dB gain compression as a function of collector current; typical values



 I_C = 10 mA; T_{amb} = 25 °C.

- (1) $f_1 = 433 \text{ MHz}$; $f_2 = 434 \text{ MHz}$
- (2) $f_1 = 900 \text{ MHz}$; $f_2 = 901 \text{ MHz}$
- (3) $f_1 = 1800 \text{ MHz}$; $f_2 = 1801 \text{ MHz}$

Fig 20. Output third-order intercept point as a function of collector-emitter voltage; typical values

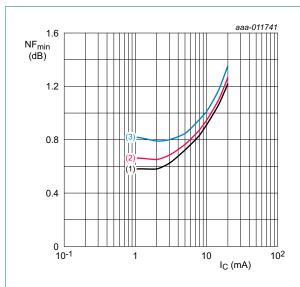


 $I_C = 10 \text{ mA}$; $T_{amb} = 25 \,^{\circ}\text{C}$.

- (1) f = 433 MHz
- (2) f = 900 MHz
- (3) f = 1800 MHz

Fig 21. Output power at 1 dB gain compression as a function of collector-emitter voltage; typical values

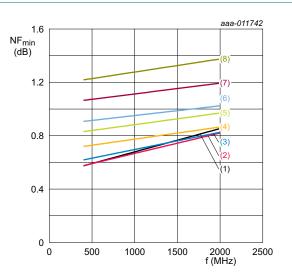
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$$V_{CE}$$
 = 8 V; T_{amb} = 25 °C; Γ_{S} = Γ_{opt} .

- (1) f = 433 MHz
- (2) f = 900 MHz
- (3) f = 1800 MHz

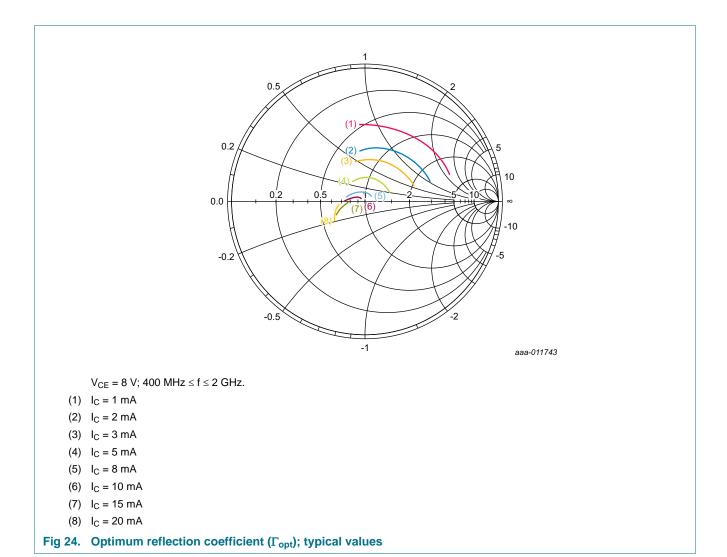
Fig 22. Minimum noise figure as a function of collector current; typical values



$$V_{CE} = 8 \text{ V}; T_{amb} = 25 \text{ °C}; \Gamma_{S} = \Gamma_{opt}.$$

- (1) $I_C = 1 \text{ mA}$
- (2) $I_C = 2 \text{ mA}$
- (3) $I_C = 3 \text{ mA}$
- (4) $I_C = 5 \text{ mA}$
- (5) $I_C = 8 \text{ mA}$
- (6) $I_C = 10 \text{ mA}$ (7) $I_C = 15 \text{ mA}$
- (8) $I_C = 20 \text{ mA}$
- Fig 23. Minimum noise figure as a function of frequency; typical values

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10. Package outline

Plastic surface-mounted package; 6 leads

SOT363

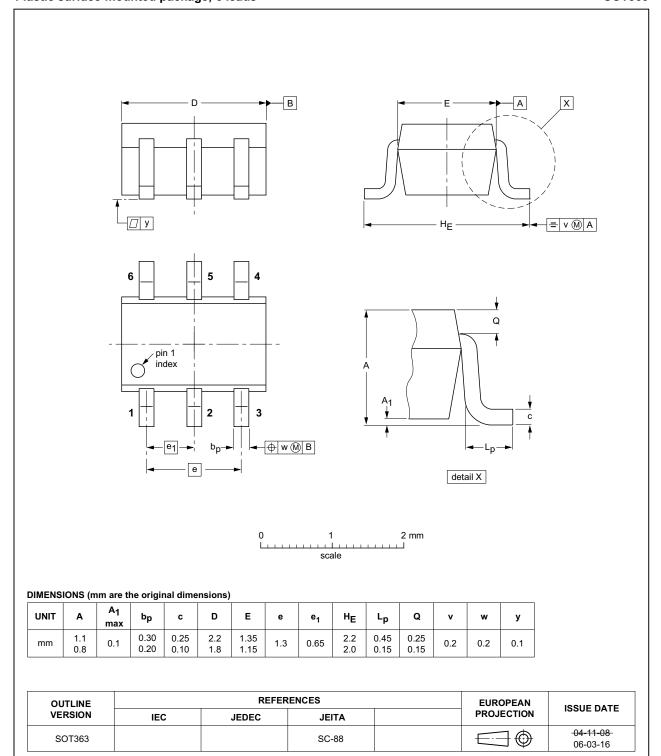


Fig 25. Package outline SOT363

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11. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

12. Abbreviations

Table 10. Abbreviations

Acronym	Description
AEC	Automotive Electronics Council
ISM	Industrial, Scientific and Medical
LNA	Low-Noise Amplifier
MSG	Maximum Stable Gain
NPN	Negative-Positive-Negative
SMA	SubMiniature version A

13. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFU520Y v.1	20140220	Product data sheet	-	-

Dual NPN wideband silicon RF transistor

14. Legal information

14.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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