CUI INC®



# SERIES: PDM2-S | DESCRIPTION: DC-DC CONVERTER

## FEATURES

- 2 W isolated output
- smaller package
- single/dual unregulated output
- 1,500 Vdc isolation
- short circuit protection
- extended temperature range (-40~105°C)
- antistatic protection up to 8kV
- UL 60950-1 approval
- high efficiency at light load
- efficiency up to 86%


RoHS c UL us

| MODEL | input voltage | | output voltage | output current | | output power | ripple and noise[2] | efficiency |
|---|---|---|---|---|---|---|---|---|
| | typ (Vdc) | range (Vdc) | (Vdc) | min (mA) | max (mA) | max (W) | typ (mVp-p) | typ (%) |
| PDM2-S5-S3-S | 5 | 4.5~5.5 | 3.3 | 40 | 400 | 1.32 | 75 | 79 |
| PDM2-S5-S5-S[1] | 5 | 4.5~5.5 | 5 | 40 | 400 | 2 | 75 | 84 |
| PDM2-S5-S12-S[1] | 5 | 4.5~5.5 | 12 | 17 | 167 | 2 | 75 | 84 |
| PDM2-S5-S15-S[1] | 5 | 4.5~5.5 | 15 | 13 | 133 | 2 | 75 | 84 |
| PDM2-S5-S24-S[1] | 5 | 4.5~5.5 | 24 | 8 | 83 | 2 | 75 | 84 |
| PDM2-S5-D3-S | 5 | 4.5~5.5 | ±3.3 | ±30 | ±303 | 2 | 75 | 71 |
| PDM2-S5-D5-S[1] | 5 | 4.5~5.5 | ±5 | ±20 | ±200 | 2 | 75 | 80 |
| PDM2-S5-D12-S[1] | 5 | 4.5~5.5 | ±12 | ±8 | ±83 | 2 | 75 | 84 |
| PDM2-S5-D15-S[1] | 5 | 4.5~5.5 | ±15 | ±7 | ±67 | 2 | 75 | 82 |
| PDM2-S5-D24-S[1] | 5 | 4.5~5.5 | ±24 | ±4 | ±42 | 2 | 75 | 84 |
| PDM2-S12-S3-S | 12 | 10.8~13.2 | 3.3 | 40 | 400 | 1.32 | 75 | 79 |
| PDM2-S12-S5-S[1] | 12 | 10.8~13.2 | 5 | 40 | 400 | 2 | 75 | 82 |
| PDM2-S12-S12-S[1] | 12 | 10.8~13.2 | 12 | 17 | 167 | 2 | 75 | 84 |
| PDM2-S12-S15-S[1] | 12 | 10.8~13.2 | 15 | 13 | 133 | 2 | 75 | 85 |
| PDM2-S12-D3-S | 12 | 10.8~13.2 | ±3.3 | ±20 | ±200 | 1.32 | 75 | 80 |
| PDM2-S12-D5-S[1] | 12 | 10.8~13.2 | ±5 | ±20 | ±200 | 2 | 75 | 80 |
| PDM2-S12-D12-S[1] | 12 | 10.8~13.2 | ±12 | ±8 | ±83 | 2 | 75 | 84 |
| PDM2-S12-D15-S[1] | 12 | 10.8~13.2 | ±15 | ±7 | ±67 | 2 | 75 | 84 |
| PDM2-S15-D15-S | 15 | 13.5~16.5 | ±15 | ±7 | ±67 | 2 | 75 | 84 |
| PDM2-S24-S3-S | 24 | 21.6~26.4 | 3.3 | 40 | 400 | 1.32 | 75 | 79 |
| PDM2-S24-S5-S[1] | 24 | 21.6~26.4 | 5 | 40 | 400 | 2 | 75 | 80 |
| PDM2-S24-S12-S[1] | 24 | 21.6~26.4 | 12 | 17 | 167 | 2 | 75 | 84 |
| PDM2-S24-S15-S[1] | 24 | 21.6~26.4 | 15 | 13 | 133 | 2 | 75 | 86 |
| PDM2-S24-S24-S[1] | 24 | 21.6~26.4 | 24 | 8 | 83 | 2 | 75 | 86 |
| PDM2-S24-D3-S | 24 | 21.6~26.4 | ±3.3 | ±30 | ±303 | 1.32 | 75 | 80 |

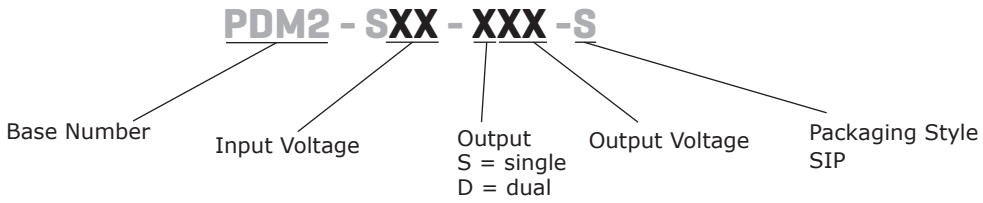
Notes: 1. UL approved
2. Ripple and noise are measured at 20 MHz BW by "parallel cable" method with 1 µF ceramic and 10 µF electrolytic capacitors on the output.

## MODEL (CONTINUED)

| | input voltage | | output voltage | output current | | output power | ripple and noise[2] | efficiency |
|---|---|---|---|---|---|---|---|---|
| | typ (Vdc) | range (Vdc) | (Vdc) | min (mA) | max (mA) | max (W) | typ (mVp-p) | typ (%) |
| PDM2-S24-D5-S[1] | 24 | 21.6~26.4 | ±5 | ±20 | ±200 | 2 | 60 | 80 |
| PDM2-S24-D12-S[1] | 24 | 21.6~26.4 | ±12 | ±8 | ±83 | 2 | 60 | 84 |
| PDM2-S24-D15-S[1] | 24 | 21.6~26.4 | ±15 | ±7 | ±67 | 2 | 75 | 84 |

Notes: 1. UL approved
2. Ripple and noise are measured at 20 MHz BW by "parallel cable" method with 1 µF ceramic and 10 µF electrolytic capacitors on the output.

## PART NUMBER KEY




## INPUT

| parameter | conditions/description | min | typ | max | units |
|---|---|---|---|---|---|
| operating input voltage | 5 Vdc input models | 4.5 | 5 | 5.5 | Vdc |
| | 12 Vdc input models | 10.8 | 12 | 13.2 | Vdc |
| | 15 Vdc input models | 13.5 | 15 | 16.5 | Vdc |
| | 24 Vdc input models | 21.6 | 24 | 26.4 | Vdc |
| surge voltage | for maximum of 1 second | | | | |
| | 5 Vdc input models | -0.7 | | 9 | Vdc |
| | 12 Vdc input models | -0.7 | | 18 | Vdc |
| | 15 Vdc input models | -0.7 | | 21 | Vdc |
| | 24 Vdc input models | -0.7 | | 30 | Vdc |
| filter | capacitance filter | | | | |

## OUTPUT

| parameter | conditions/description | min | typ | max | units |
|---|---|---|---|---|---|
| line regulation | for Vin change of ±1% | | | | |
| | 3.3 Vdc output models | | | ±1.5 | % |
| | all other models | | | ±1.2 | % |
| load regulation | measured from 10% load to full load | | | | |
| | 3.3 Vdc output models | | 18 | | % |
| | 5 Vdc output models | | 12 | | % |
| | 12 Vdc output models | | 8 | | % |
| | 15 Vdc output models | | 7 | | % |
| | 24 Vdc output models | | 6 | | % |
| voltage accuracy | see tolerance envelope curve | | | | |
| switching frequency | 100% load, nominal input voltage | | 100 | | kHz |
| temperature coefficient | 100% load | | | ±0.03 | %/°C |

## PROTECTIONS

| parameter | conditions/description | min | typ | max | units |
|---|---|---|---|---|---|
| short circuit protection[3] | | | | 1 | s |

Notes: 3. The supply voltage must be discontinued at the end of the short circuit duration

## SAFETY AND COMPLIANCE

| parameter | conditions/description | min | typ | max | units |
|---|---|---|---|---|---|
| isolation voltage | input to output, for 1 minute at 1 mA max. | 1,500 | | | Vdc |
| isolation resistance | input to output at 500 Vdc | 1,000 | | | MΩ |
| safety approvals[4] | UL 60950-1 | | | | |

Notes: 4. See specific models noted on page 1

## SAFETY AND COMPLIANCE (CONTINUED)

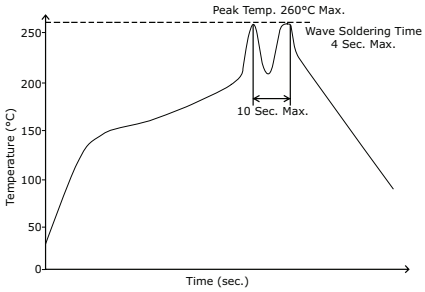
| parameter | conditions/description | min | typ | max | units |
|---|---|---|---|---|---|
| conducted emissions | CISPR22/EN55022 class B (external circuit required, see Figure 1) | | | | |
| radiated emissions | CISPR22/EN55022 class B (external circuit required, see Figure 1) | | | | |
| ESD | IEC/EN61000-4-2, class B, contact ± 8kV for single outputs<br>IEC/EN61000-4-2, class B, contact ± 6kV for dual outputs | | | | |
| MTBF | as per MIL-HDBK-217F at 25°C | 3,500,000 | | | hours |
| RoHS | 2011/65/EU | | | | |

## ENVIRONMENTAL

| parameter | conditions/description | min | typ | max | units |
|---|---|---|---|---|---|
| operating temperature | see derating curve | -40 | | 105 | °C |
| storage temperature | | -55 | | 125 | °C |
| storage humidity | non-condensing | | | 95 | % |
| temperature rise | at nominal input, full load, Ta = 25°C | | 25 | | °C |

## SOLDERABILITY

| parameter | conditions/description | min | typ | max | units |
|---|---|---|---|---|---|
| hand soldering | 1.5 mm from case for 10 seconds | | | 300 | °C |
| wave soldering | see wave soldering profile | | | 260 | °C |



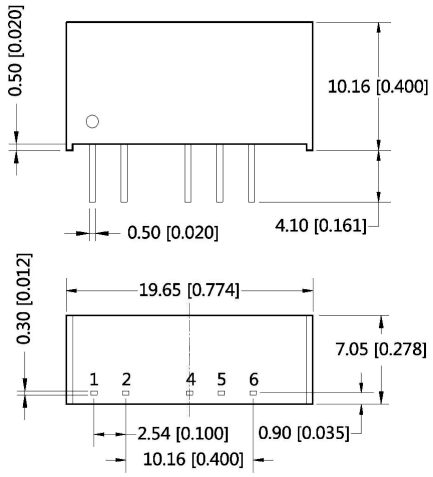

## MECHANICAL

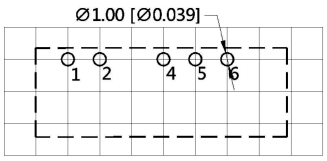
| parameter | conditions/description | min | typ | max | units |
|---|---|---|---|---|---|
| dimensions | 19.65 x 7.05 x 10.16 (0.774 x 0.278 x 0.400 inch) | | | | mm |
| case material | plastic (UL94-V0) | | | | |
| weight | | | 2.4 | | g |

## MECHANICAL DRAWING

units: mm[inch]
tolerance: ±0.25[±0.010]
pin section tolerance: ±0.10[±0.004]

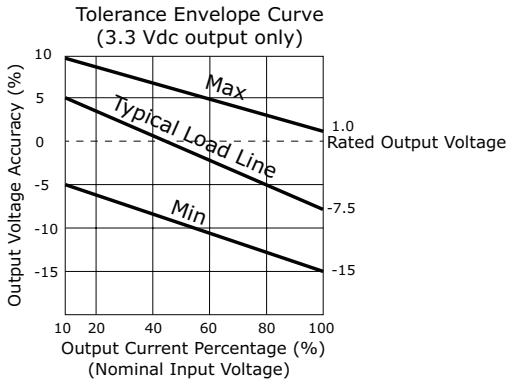
| PIN CONNECTIONS | | |
|---|---|---|
| PIN | Single Output | Dual Output |
| 1 | Vin | Vin |
| 2 | GND | GND |
| 4 | 0V | -Vo |
| 5 | No Pin | 0V |
| 6 | +Vo | +Vo |

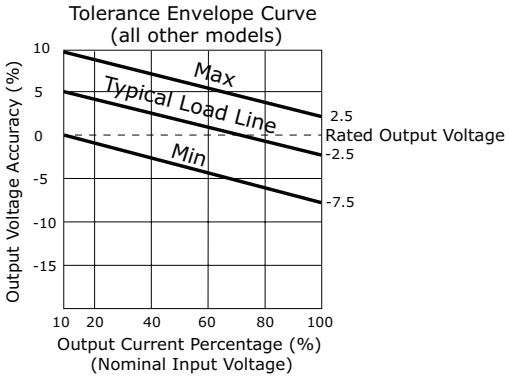


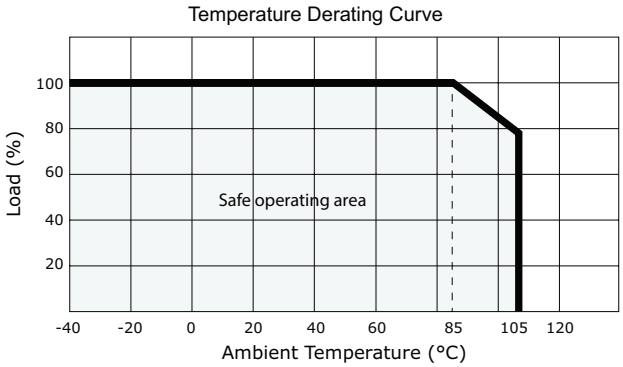




Note: Grid 2.54*2.54 mm
Recommended PCB Layout
Top View

## DERATING CURVES




## EMC RECOMMENDED CIRCUIT

**Figure 1**

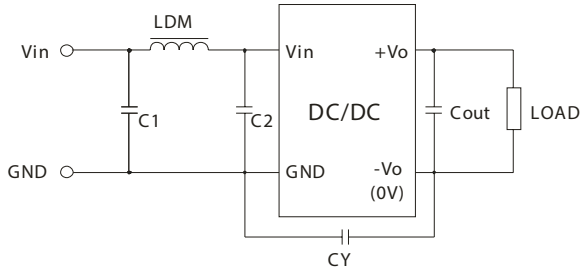



**Table 1**

| Recommended external circuit components | | | |
|---|---|---|---|
| Vin (Vdc) | C1, C2 | CY | LDM |
| 5 | 4.7µF/50V | -- | 6.8µH |
| 12 | 4.7µF/50V | -- | 6.8µH |
| 15 | 4.7µF/50V | -- | 6.8µH |
| 24 | 4.7µF/50V | 1nF/2kV | 6.8µH |

Note: 1. See Table 3 for Cout values.

## TEST CONFIGURATION

**Figure 2**

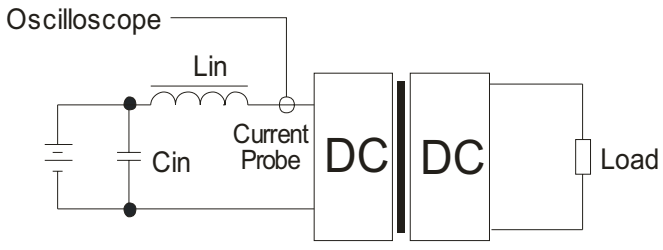



**Table 2**

| External components | |
|---|---|
| Lin | 4.7µH |
| Cin | 220µF, ESR < 1.0Ω at 100 kHz |

Note: 1. Input reflected-ripple current is measured with an inductor Lin and capacitor Cin to simulate source impedance.

## APPLICATION NOTES

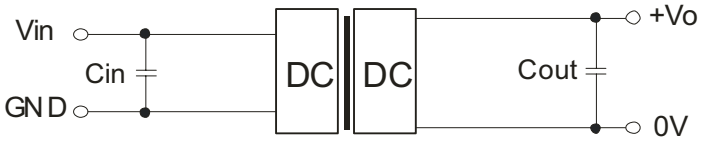
1. **Output load requirement**
   To ensure this module can operate efficiently and reliably, the minimum output load may not be less than 10% of the full load during operation. If the actual output power is low, connect a resistor at the output end in parallel to increase the load.

2. **Overload Protection**
   Under normal operating conditions, the output circuit of this product has no protection against overload. The simplest method to add this is to add a circuit breaker to the circuit.

3. **Recommended circuit**
   If you want to further decrease the input/output ripple, you can increase the capacitance accordingly or choose capacitors with low ESR(see Figure 3 & Table 3). However, the capacitance of the output filter capacitor must be appropriate. If the capacitance is too high, a startup problem might arise. For every channel of the output, to ensure safe and reliable operation, the maximum capacitance must be less than the maximum capacitive load (see Table 4).

**Figure 3**

Single Output




Dual Output

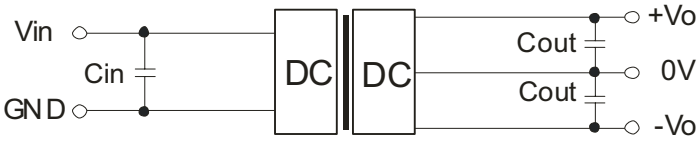



**Table 3**

| Vin (Vdc) | Cin (µF) | Single Vo (Vdc) | Cout (µF) | Dual Vo (Vdc) | Cout (µF) |
|---|---|---|---|---|---|
| 5 | 4.7 | 3.3 | 10 | ±3.3 | 4.7 |
| 12 | 2.2 | 5 | 10 | ±5 | 4.7 |
| 15 | 2.2 | 12 | 2.2 | ±12 | 1 |
| 24 | 1 | 15 | 1 | ±15 | 0.47 |
| -- | -- | 24 | 1 | ±24 | 0.47 |

**Table 4**

| Single Vout (Vdc) | Max. Capacitive Load (µF) | Dual Vout (Vdc) | Max. Capacitive Load[1] (µF) |
|---|---|---|---|
| 3.3 | 220 | 3.3 | 100 |
| 5 | 220 | 5 | 100 |
| 12 | 220 | 12 | 100 |
| 15 | 220 | 15 | 100 |
| 24 | 220 | 24 | 100 |

Note: 1. For each output.

Notes: 1. Operation under minimum load will not damage the converter; however, they may not meet all specifications listed.
2. Max. capacitive load tested at input voltage range and full load.
3. It is recommended to use either ceramic capacitors or electrolytic capacitors on the input and the output. Using tantalum capacitors may increase the risk of failure.
4. All specifications measured at: Ta=25°C, humidity<75%, nominal input voltage and rated output load, unless otherwise specified.

## REVISION HISTORY

| rev. | description | date |
|---|---|---|
| 1.0 | initial release | 03/18/2013 |
| 1.01 | added models, added UL approval to some models | 12/16/2014 |
| 1.02 | updated tolerance envelope curves | 02/04/2016 |
| 1.03 | updated datasheet | 10/12/2017 |

The revision history provided is for informational purposes only and is believed to be accurate.

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