TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7W04FU, TC7W04FK

#### 3 Inverters

The TC7W04 is a high speed  $C^2MOS$  Buffer fabricated with silicon gate  $C^2MOS$  technology.

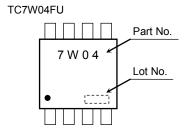
The internal circuit is composed of 3 stages including buffer output, which enables high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

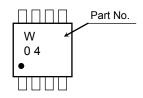
#### **Features**

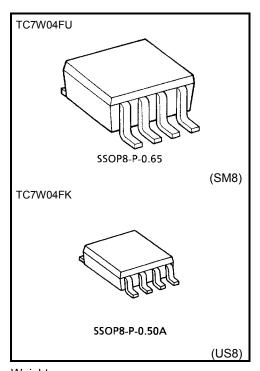
- High speed: t<sub>pd</sub> = 6 ns (typ.) at V<sub>CC</sub> = 5V
- Low power dissipation:  $I_{CC} = 1\mu A$  (max) at Ta = 25°C
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |I<sub>OH</sub>| = I<sub>OL</sub> = 4 mA (min)
- Balanced propagation delays: t<sub>pLH</sub> ≃ t<sub>pHL</sub>
- Wide operating voltage range: V<sub>CC</sub> (opr) = 2 to 6V

#### Marking



TC7W04FK





Weight SSOP8-P-0.65: 0.02 g (typ.) SSOP8-P-0.50A: 0.01 g (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

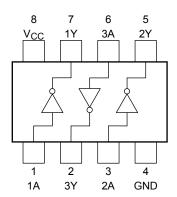
Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	$-0.5$ to $V_{CC}$ + $0.5$	٧
DC output voltage	V <sub>OUT</sub>	$-0.5$ to $V_{CC}$ + $0.5$	٧
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±25	mA
Dower discipation	PD	300 (SM8)	mW
Power dissipation	FD	200 (US8)	IIIVV
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

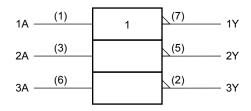
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

2

#### Pin Configuration (top view)



#### **Logic Diagram**



#### **Truth Table**

А	Y
L	Н
Н	L

### **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2 to 6	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	>
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C
		0 to 1000 (V <sub>CC</sub> = 2.0 V)	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 (V <sub>CC</sub> = 4.5 V)	ns
		0 to 400 (V <sub>CC</sub> = 6.0 V)	

#### **Electrical Characteristics**

#### **DC Electrical Characteristics**

Characteristics Symbol Test Condition		Condition		Ta = 25°C			Ta = -40 to 85°C		Unit		
		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Onit			
		V <sub>IH</sub>	_		2.0	1.5	_	_	1.5	_	
Input voltage  Low level	4.5				3.15	_	_	3.15	_		
		6.0			4.2	_	_	4.2	_		
				2.0	_	_	0.5	_	0.5	V	
	Low level	V <sub>IL</sub>	_		4.5	_	_	1.35	_	1.35	-
					6.0	_	_	1.8	_	1.8	
		High level V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	2.0	1.9	2.0	_	1.9	_	
Output voltage  Low level					4.5	4.4	4.5	_	4.4	_	
	High level				6.0	5.9	6.0	_	5.9	_	
				I <sub>OH</sub> = -4 mA	4.5	4.18	4.31	_	4.13	_	
				$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
		Low level V <sub>OL</sub>	$V_{IN} = V_{IH}$	I <sub>OL</sub> = 20 μA	2.0	_	0	0.1	_	0.1	V
					4.5	_	0	0.1	_	0.1	
	Low level				6.0	_	0	0.1	_	0.1	
				I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	_	0.33	
				I <sub>OL</sub> = 5.2 mA	6.0		0.18	0.26	_	0.33	
Input leakage	current	I <sub>IN</sub>	I <sub>IN</sub> V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	±0.1	_	±1.0	μА
Quiescent sup	ply current	rent I <sub>CC</sub> V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	1.0	_	10.0	μА	

# AC Electrical Characteristics (C $_L$ = 15 pF, $V_{CC}$ = 5 V, Ta = 25 $^{\circ} C)$

Characteristics	Symbol	Toot Condition	•	l lmit		
		Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> t <sub>THL</sub>	_	_	4	8	ns
Propagation delay time	t <sub>pLH</sub> t <sub>pHL</sub>	_	_	6	12	ns

# AC Electrical Characteristics ( $C_L = 50 \text{ pF}$ , input $t_r = t_f = 6 \text{ ns}$ )

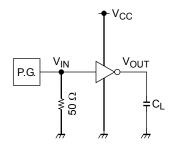
Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
2.13.13.13.10.100			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
Output transition time		_	2.0		30	75	_	95	ns
	t <sub>TLH</sub>		4.5	_	8	15	_	19	
	TIFIL		6.0		7	13	_	16	
Propagation delay time	t <sub>pLH</sub> t <sub>pHL</sub>	_	2.0		27	75	_	95	
			4.5		9	15	_	19	ns
			6.0		8	13	_	16	
Input capacitance	C <sub>IN</sub>				5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>		(Note)		20		_		pF

Note: C<sub>PD</sub> is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to test circuit).

Average operating current can be obtained by the equation hereunder.

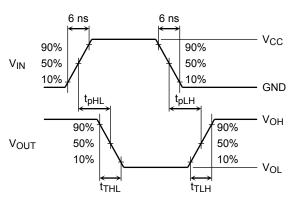
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3 \text{ (per gate)}$ 

#### **AC Electrical Characteristics Test Circuit**

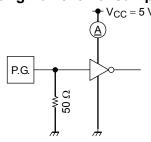


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#### **AC Electrical Characteristics Test Waveform**



#### **Operating Current Consumption Test Circuit**

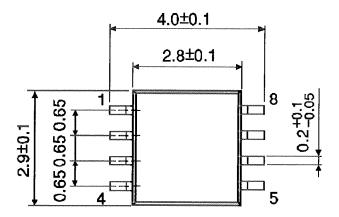


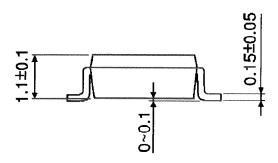
This input waveform is equal to the AC electrical characteristics test waveform.

TC7W04FU/FK

## **Package Dimensions**

SSOP8-P-0.65 Unit: mm





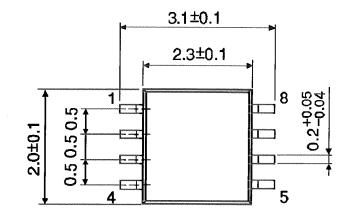
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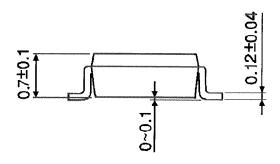
Weight: 0.02 g (typ.)

## **Package Dimensions**

SSOP8-P-0.50A

Unit: mm





Weight: 0.01 g (typ.)

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