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November 2013

FQPF20N06L

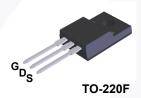
N-Channel QFET[®] MOSFET 60 V, 15.7 A, 55 m Ω

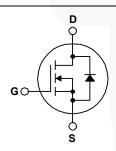
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 15.7 A, 60 V, $R_{DS(on)}$ = 55 m Ω (Max.) @ V_{GS} = 10 V, I_D = 7.85 A
- Low Gate Charge (Typ. 9.5 nC)
- Low Crss (Typ. 35 pF)
- · 100% Avalanche Tested
- · 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQPF20N06L	Unit
V_{DSS}	Drain-Source Voltage		60	V
I _D	Drain Current - Continuous (T _C = 25°C	C)	15.7	Α
	- Continuous (T _C = 100°	°C)	11.1	А
I _{DM}	Drain Current - Pulsed	(Note 1)	62.8	Α
V_{GSS}	Gate-Source Voltage		± 20	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	170	mJ
I _{AR}	Avalanche Current	(Note 1)	15.7	Α
E _{AR}	Repetitive Avalanche Energy	(Note 1)	3.0	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	7.0	V/ns
P _D	Power Dissipation (T _C = 25°C)		30	W
	- Derate above 25°C		0.2	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FQPF20N06L	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	5.00	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max. 62.5		°C/W	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQPF20N06L	FQPF20N06L	TO-220F	Tube	N/A	N/A	50 units

Fle	ctrical	l Chara	cter	istics
	Cuica	ı Cılala	CLEI	SHE

Symbol	Parameter	Test Conditions	Min	Тур	Max	Uni
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		0.06		V/°(
I _{DSS}	7 0 1 1/1 5 : 0 1	V _{DS} = 60 V, V _{GS} = 0 V			1	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = 48 V, T _C = 150°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Cha	aracteristics		•			
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0		2.5	V
R _{DS(on)}	Static Drain-Source	V _{GS} = 10 V, I _D = 7.85 A		0.042	0.055	
20(0)	On-Resistance	$V_{GS} = 5 \text{ V}, I_D = 7.85 \text{ A}$		0.055	0.07	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 25 V, I _D = 7.85 A		9		S
Dynam C _{iss}	ic Characteristics Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,		480	630	pF
C _{oss}	Output Capacitance	f = 1.0 MHz		175	230	pF
C _{rss}	Reverse Transfer Capacitance	1		35	45	pF
Switchi	ing Characteristics					
t _{d(on)}	Turn-On Delay Time	V 00 V 1 40 5 A		10	30	ns
t _r	Turn-On Rise Time	$V_{DD} = 30 \text{ V}, I_{D} = 10.5 \text{ A},$ $R_{G} = 25 \Omega$		165	340	ns
t _{d(off)}	Turn-Off Delay Time	11G - 23 22		35	80	ns
t _f	Turn-Off Fall Time	(Note 4)	/	70	150	ns
Q _g	Total Gate Charge	V _{DS} = 48 V, I _D = 21 A,		9.5	13	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 5 V		2.5		nC
Q _{gd}	Gate-Drain Charge	(Note 4)		5.5	/	nC
Drain S	Source Diede Characteristics of	nd Maximum Patings				
l _S	Source Diode Characteristics at Maximum Continuous Drain-Source Did		T		15.7	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				62.8	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 15.7 A			1.5	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_S = 21 \text{ A},$		54		ns
711 -	1.070100 1.000701y Tillio	165 0 4, 18 21 7 1,		J-T		113

Q_{rr}

- Notes: 1. Repetitive Rating : Pulse width limited by maximum junction temperature. 2. L = 800 μ H, I_{AS} = 15.7 A, V_{DD} = 25 V, R_G = 25 Ω , starting T_J = 25°C. 3. I_{SD} \leq 21 A, di/dt \leq 300 A/ μ s, V_{DD} \leq BV_{DSS}, starting T_J = 25°C. 4. Essentially independent of operating temperature.

Reverse Recovery Charge

nC

 $dI_F / dt = 100 A/\mu s$

Typical Characteristics

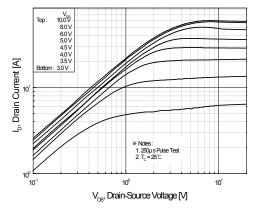


Figure 1. On-Region Characteristics

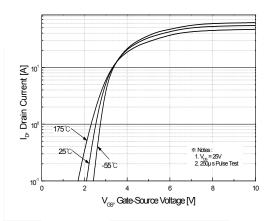


Figure 2. Transfer Characteristics

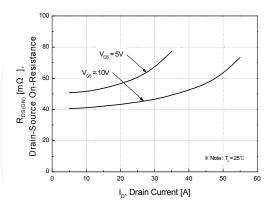


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

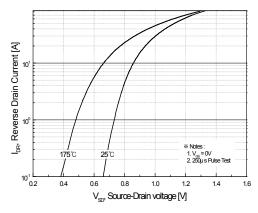


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

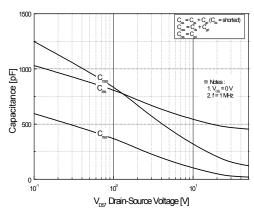


Figure 5. Capacitance Characteristics

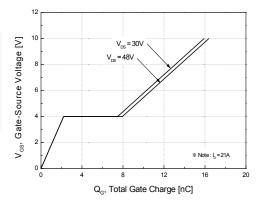


Figure 6. Gate Charge Characteristics

Typical Characteristics (continued)

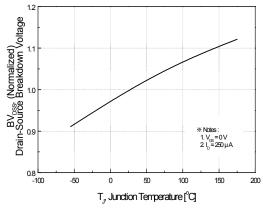


Figure 7. Breakdown Voltage Variation vs. Temperature

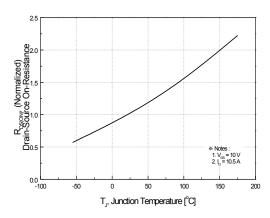


Figure 8. On-Resistance Variation vs. Temperature

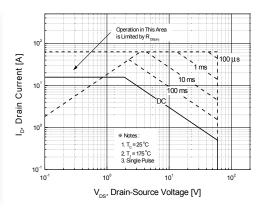


Figure 9. Maximum Safe Operating Area

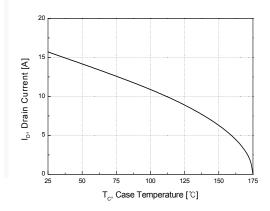


Figure 10. Maximum Drain Current vs. Case Temperature

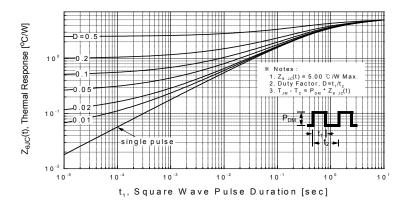


Figure 11. Transient Thermal Response Curve

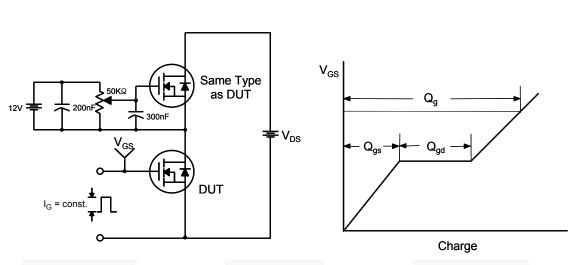


Figure 12. Gate Charge Test Circuit & Waveform

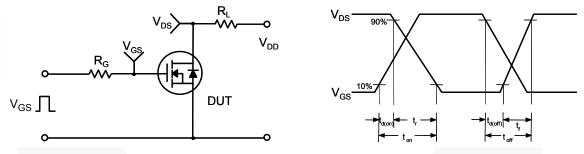


Figure 13. Resistive Switching Test Circuit & Waveforms

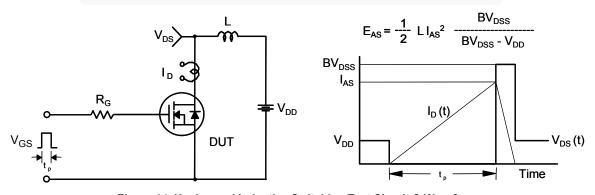
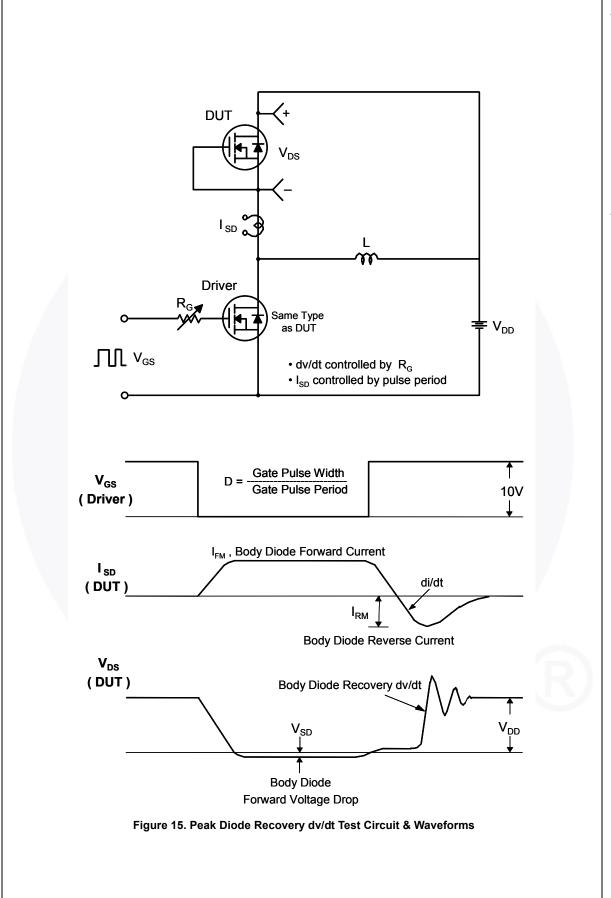


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

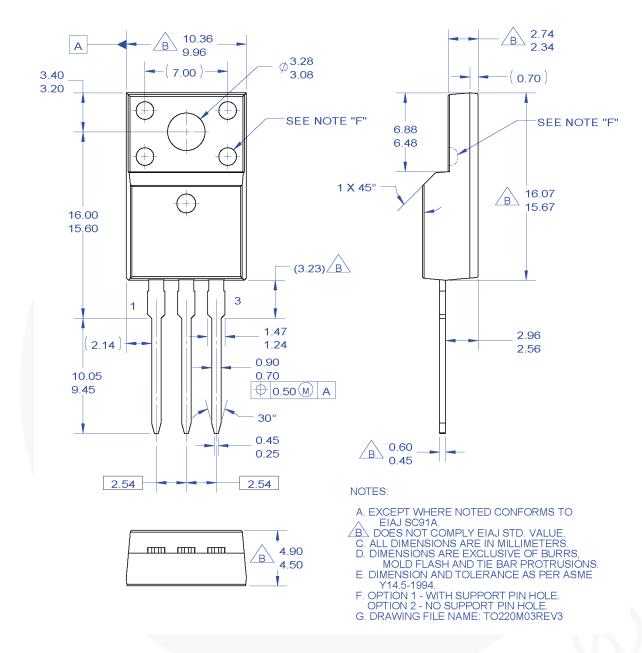


Figure 16. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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