

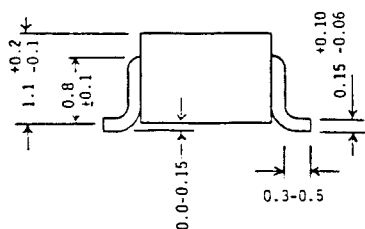
CUSTOMER	NO. MMST8098
	PAGE 1 of 2
	DATE January 14, 1987

SUBJECT
SOT-23 TRANSISTOR, NPN, SILICON

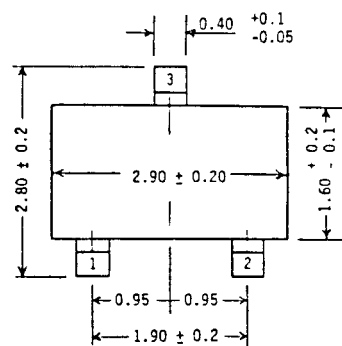
ABSOLUTE MAXIMUM RATINGS: (Ta = 25°C)

Collector-Base Voltage	V _{CB0}	60 V
Collector-Emitter Voltage	V _{CE0}	60 V
Emitter-Base Voltage	V _{EB0}	6 V
Collector Current	I _C	200 mA
Power Dissipation-Free Air	P _D	200 mW
Power Dissipation-Ceramic Substrate	P _D	350 mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to 150 °C
Solder Temperature (10 seconds)		260 °C

DIMENSIONS: Units (mm)



END VIEW

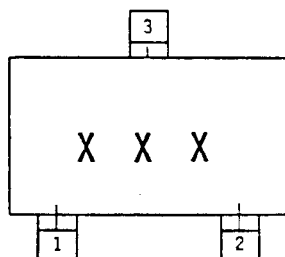


Pin-Out

1	B
2	E
3	C

THE JAPANESE STYLE SC-59 PACKAGE

MARKING:



Three Digit
Marking Code

R A V

PACKAGING:

- BULK, 500 per BAG
- MAGAZINES OF 50 EACH
- 8mm T&R, T-146 3K/REEL
- 8mm T&R, T-147 3K/REEL
- 8mm T&R, T-246 10K/REEL
- 8mm T&R, T-247 10K/REEL

REMARKS: PROCESS: C-22 Thermal Resistance R_{θJA} 625 °C/Watt
FREE AIR, T_A = 25°C

ROHM ELECTRONICS
3034 Owen Drive, Antioch, TN 37013
TEL: (615) 641-2020 FAX: (615) 641-2022

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CUSTOMER	SUBJECT TRANSISTOR, NPN ,SILICON SOT-23	NO. MMST8098
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ELECTRICAL CHARACTERISTICS: (Ta = 25°C Unless Otherwise Specified)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
BV _{CBO}	I _C = 100 μ A	60			V
BV _{CEO}	I _C = 10 mA	60			V
BV _{EBO}	I _E = 10 μ A	6			V
I _{CBO}	V _{CB} = 60 V		2.0	100	nA
I _{CEO}	V _{CE} = 60 V		1.5	100	nA
I _{EBO}	V _{EB} = 6 V		1.0	100	nA
h _{FE}	I _C = 1.0 mA, V _{CE} = 5.0 V	100		300	
h _{FE}	I _C = 10 mA, V _{CE} = 5.0 V	100			
h _{FE}	I _C = 100 mA, V _{CE} = 5.0 V	75			
V _{CE(SAT)}	I _C = 100 mA, I _B = 5.0 mA			0.4	V
V _{CE(SAT)}	I _C = 100 mA, I _B = 10 mA			0.3	V
V _{BE(ON)}	I _C = 1.0 mA, V _{CE} = 5.0 V	0.5		0.7	V
f _T	I _C = 10 mA, V _{CE} = 5.0 V, f = 100MHz	150	350		MHz
C _{ob}	V _{CB} = 5.0 V, I _E = 0, f = 1.0 MHz			6.0	pF
C _{ib}	V _{BE} = 5.0 V, I _C = 0, f = 1.0 MHz			25	pF

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