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November 2013

# **FDB15N50**

# N-Channel UniFET<sup>TM</sup> MOSFET 500 V, 15 A, 380 mΩ

### **Features**

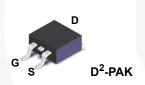
- $\,^{\bullet}$  Low gate charge  $Q_g$  results in simple drive requirement (Typ. 33 nC)
- Improved Gate, avalanche and high reapplied dv/dt ruggedness
- Reduced  $R_{DS(on)}$  (  $330m\Omega$  ( Typ.) @  $V_{GS}$  = 10 V,  $I_D$  = 7.5 A)
- Reduced Miller capacitance and low Input capacitance (Typ. C<sub>rss</sub> = 16 pF)
- Improved switching speed with low EMI
- 175°C rated junction temperature

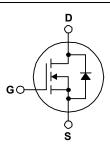
# **Description**

UniFET™ MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts

# **Applications**

- Lighting
- Uninterruptible Power Supply
- · AC-DC Power Supply





# **Absolute Maximum Ratings** T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Parameter	FDB15N50	Unit
V <sub>DSS</sub>	Drain to Source Voltage	500	V
V <sub>GS</sub>	Gate to Source Voltage	±30	V
	Drain Current Continuous (T <sub>C</sub> = 25°C, V <sub>GS</sub> = 10V)	15	А
I <sub>D</sub>	Continuous ( $T_C = 100^{\circ}C$ , $V_{GS} = 10V$ )	11	Α
	Pulsed (Note 1)	60	Α
$P_{D}$	Power dissipation Derate above 25°C	300 2	W W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature	-55 to 175	°C
	Soldering Temperature for 10 seconds	300 (1.6mm from case)	°C

### **Thermal Characteristics**

Symbol	Parameter	FDB15N50	Unit	
$R_{\theta JC}$	Thermal Resistance Junction to Case, Max.	0.50	°C/W	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient, Max.	62	°C/W	

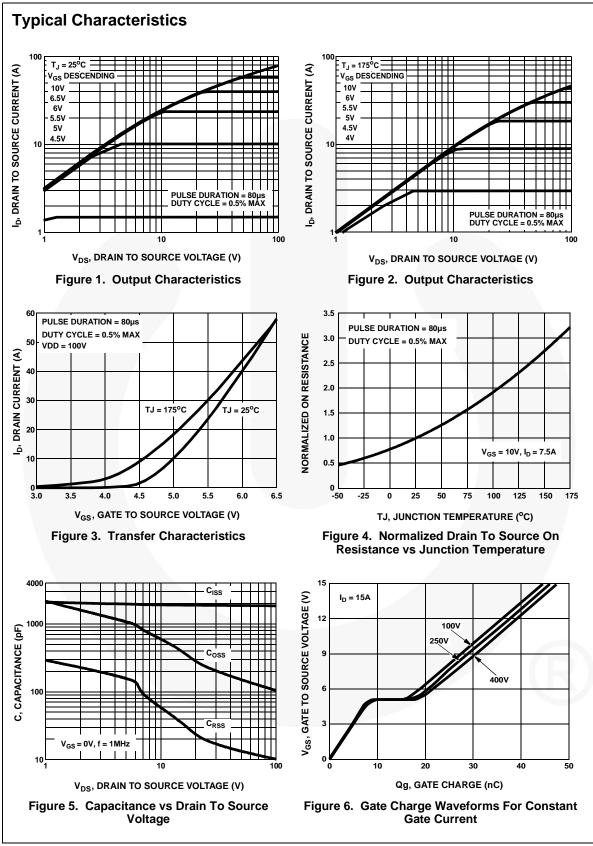
# **Package Marking and Ordering Information**

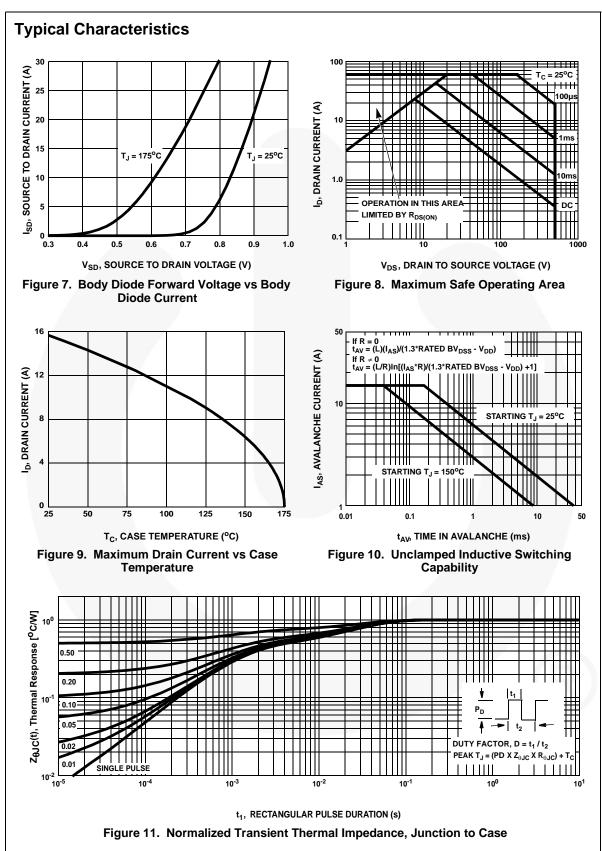
Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB15N50	FDB15N50	D <sup>2</sup> -PAK	330 mm	24 mm	800 units

# **Electrical Characteristics** $T_J = 25$ °C unless otherwise noted.

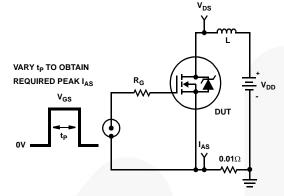
Symbol	Parameter	Test Conditions		Min	Тур	Max	Unit
Statics							
B <sub>VDSS</sub>	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS}$	s = 0V	500	-	-	V
ΔB <sub>VDSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temp. Coefficient	Reference to 25°C, I <sub>D</sub> = 1mA		-	0.58	-	V/°C
R <sub>DS(ON)</sub>	Drain to Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 7.5A		-	0.33	0.38	Ω
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} =$	250µA	2.0	3.4	4.0	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current		$T_{\rm C} = 25^{\rm o}{\rm C}$ $T_{\rm C} = 150^{\rm o}{\rm C}$	-	-	25 250	μΑ
I <sub>GSS</sub>	Gate to Source Leakage Current	$V_{GS} = \pm 30V$		-	-	±100	nA
Dynamics					•		
9 <sub>fs</sub>	Forward Transconductance	$V_{DD} = 10V, I_D =$	7.5A	10	-	-	S
Q <sub>g(TOT)</sub>	Total Gate Charge at 10V	$V_{GS} = 10V,$ $V_{DS} = 400V,$		-	33	41	nC
$Q_{gs}$	Gate to Source Gate Charge			- \	7.2	10	nC
$Q_{gd}$	Gate to Drain "Miller" Charge	I <sub>D</sub> = 15A		-	12	16	nC
t <sub>d(ON)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 250V,		-	9		ns
t <sub>r</sub>	Rise Time	$I_D = 15A,$ $R_G = 6.2\Omega,$		-	5.4	-	ns
t <sub>d(OFF)</sub>	Turn-Off Delay Time			-	26	-	ns
`t <sub>f</sub>	Fall Time	$R_D = 17\Omega$		-	5	-	ns
C <sub>ISS</sub>	Input Capacitance	.,	-1.	-	1850	-	pF
Coss	Output Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> f = 1MHz	$\varsigma = 0V,$	-	230	-	pF
C <sub>RSS</sub>	Reverse Transfer Capacitance	I = IIVITZ		-	16	-	pF
	e Characteristics						
E <sub>AS</sub>	Single Pulse Avalanche Energy (Note 2)			760	-	-	mJ
I <sub>AR</sub>	Avalanche Current			-	-	15	Α
Drain-Sou	rce Diode Characteristics						
I <sub>S</sub>	Continuous Source Current (Body Diode)	MOSFET symbol showing the integral reverse p-n junction diode.		-	-	15	А
I <sub>SM</sub>	Pulsed Source Current (Body Diode) (Note 1)			-	-	60	А
V <sub>SD</sub>	Source to Drain Diode Voltage	I <sub>SD</sub> = 15A		-	0.86	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	$I_{SD} = 15A$ , $di_{SD}$	/dt = 100A/μs	-	470	730	ns
Q <sub>RR</sub>	Reverse Recovered Charge	$I_{SD} = 15A$ , $di_{SD}$	/dt = 100A/μs	-	5	6.6	μC

Repetitive rating; pulse width limited by maximum junction temperature.
 Starting T<sub>J</sub> = 25°C, L = 7.0mH, I<sub>AS</sub> = 15A.





# **Test Circuits and Waveforms**



V<sub>DS</sub>
V<sub>DD</sub>
V<sub>DD</sub>

BV<sub>DSS</sub>

Figure 12. Unclamped Energy Test Circuit

Figure 13. Unclamped Energy Waveforms

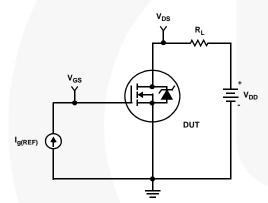


Figure 14. Gate Charge Test Circuit

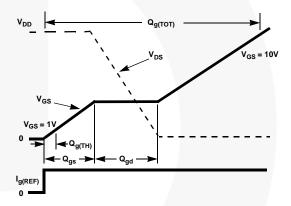


Figure 15. Gate Charge Waveforms

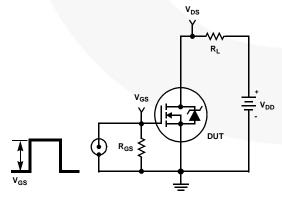


Figure 16. Switching Time Test Circuit

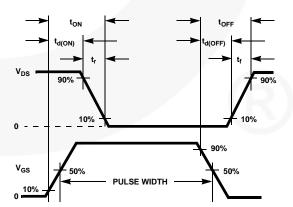


Figure 17. Switching Time Waveform

## **Mechanical Dimensions**

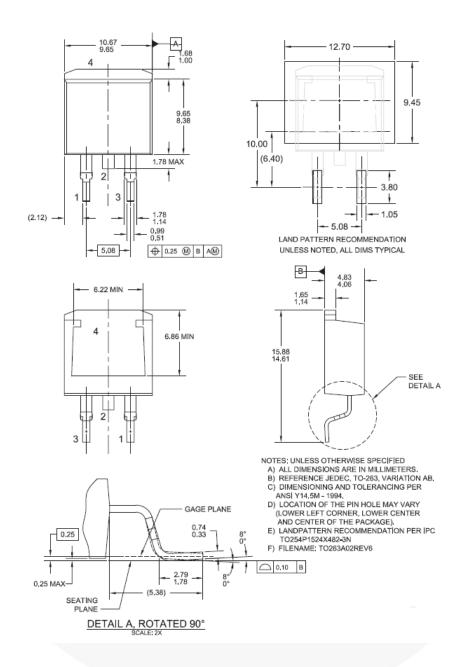


Figure 18. TO263 (D<sup>2</sup>PAK), Molded, 2-Lead, Surface Mount

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Rev. 166

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