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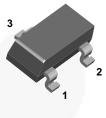
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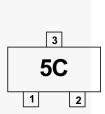


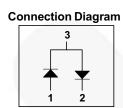
February 2015

MMBD7000 Small Signal Diode



SOT-23





Ordering Information

Part Number	Top Mark	Package	Packing Method	
MMBD7000	5C	SOT-23 3L	Tape and Reel	

Absolute Maximum Ratings(1), (2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter		Value	Unit
V _{RRM}	Maximum Repetitive Reverse Voltage		100	V
I _{F(AV)}	Average Rectified Forward Current		200	mA
I COM	Non-Repetitive Peak Forward Surge Current	Pulse Width = 1.0 second	1.0	A
		Pulse Width = 1.0 microsecond	2.0	
T _{STG}	Storage Temperature Range		-55 to +150	°C
T _J	Operating Junction Temperature		150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150° C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
P_{D}	Power Dissipation	350	mW
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	357	°C/W

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _R	Breakdown Voltage	I _R = 100 μA	100		V
V _F	Forward Voltage	I _F = 1.0 mA	550	700	mV
		I _F = 10 mA	670	820	mV
		I _F = 100 mA	0.75	1.1	V
I _R	Reverse Current	V _R = 100 V	\.	500	nA
		V _R = 50 V		300	nA
		V _R = 50 V, T _A = 125°C		100	μΑ
C_{T}	Total Capacitance	V _R = 0, f = 1.0 MHz		1.5	pF
t _{rr}	Reverse Recovery Time	$I_F = I_R = 10 \text{ mA}, I_{RR} = 1.0 \text{ mA},$ $R_L = 100 \Omega$		4.0	nS

Physical Dimensions

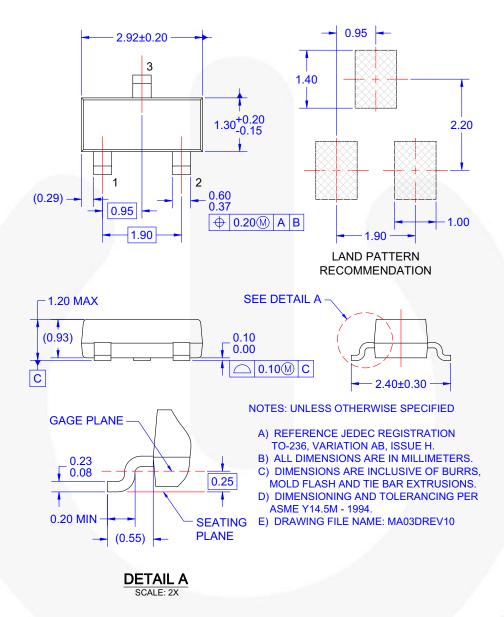


Figure 1. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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