

Product Summary

BV _{DSS}	R _{DS(ON)} MAX	Package	I _D T _A = +25°C
-20V	1.0Ω @ V _{GS} = -4.5V	X1-DFN1212-3	-600mA
	1.5Ω @ V _{GS} = -2.5V		-500mA
	2.0Ω @ V _{GS} = -1.8V		-400mA
	3.0Ω @ V _{GS} = -1.5V		-250mA

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

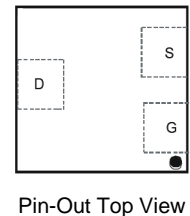
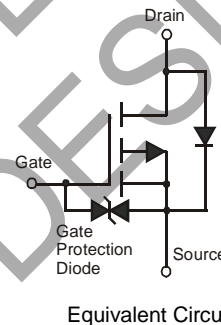
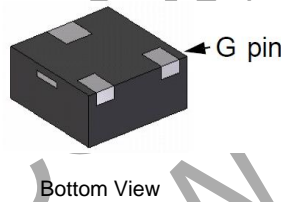
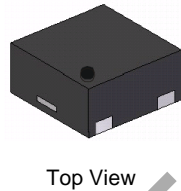
- DC-DC Converters
- Power Management Functions

Features

- Low On-Resistance
- Very Low Gate Threshold Voltage V_{GS(TH)}, 1.0V Max
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: X1-DFN1212-3
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Terminal Connections: See Diagram
- Weight: 0.005 grams (Approximate)

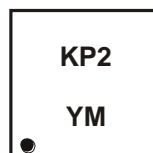


Ordering Information (Note 4)

Part Number	Case	Packaging
DMP21D5UFD-7	X1-DFN1212-3	3000/Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



KP2 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: E = 2017)
 M = Month (ex: 9 = September)

Date Code Key

Year	2011	2012	2013	2014	2015	2016	2017
Code	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 6) V _{GS} = -4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	-600 -500	mA
Continuous Drain Current (Note 6) V _{GS} = -1.8V	Steady State	T _A = +25°C T _A = +70°C	I _D	-400 -300	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-2	A
Maximum Body Diode Continuous Current			I _S	-800	mA

Thermal Characteristics

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 5)			P _D	0.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R _{θJA}	280	°C/W
Total Power Dissipation (Note 6)			P _D	0.8	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		R _{θJA}	140	°C/W
Operating and Storage Temperature Range			T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -1mA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	-80 -100	nA	V _{DS} = -4.5V, V _{GS} = 0V V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10.0	µA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	—	-1.0	V	V _{DS} = V _{GS} , I _D = -250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	0.7	1.0	Ω	V _{GS} = -4.5V, I _D = -100mA
		—	0.9	1.5		V _{GS} = -2.5V, I _D = -80mA
		—	1.2	2.0		V _{GS} = -1.8V, I _D = -40mA
		—	1.5	3.0		V _{GS} = -1.5V, I _D = -30mA
		—	5	—		V _{GS} = -1.2V, I _D = -1mA
Forward Transfer Admittance	Y _{fs}	—	0.7	—	s	V _{DS} = -3V, I _D = -100mA
Diode Forward Voltage	V _{SD}	—	-0.75	-1.2	V	V _{GS} = 0V, I _S = -330mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{ISS}	—	46.1	—	pF	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{OSS}	—	7.2	—		
Reverse Transfer Capacitance	C _{RSS}	—	4.9	—		
Total Gate Charge V _{GS} = -4.5V	Q _g	—	0.5	—	nC	V _{DS} = -10V, I _D = -250mA
Total Gate Charge V _{GS} = -8V	Q _g	—	0.8	—		
Gate-Source Charge	Q _{gs}	—	0.1	—		
Gate-Drain Charge	Q _{gd}	—	0.1	—		
Turn-On Delay Time	t _{D(ON)}	—	8.5	—	ns	V _{DD} = -3V, V _{GS} = -2.5V, R _L = 300Ω, R _G = 25Ω, I _D = -100mA
Turn-On Rise Time	t _R	—	4.3	—		
Turn-Off Delay Time	t _{D(OFF)}	—	20.2	—		
Turn-Off Fall Time	t _F	—	19.2	—		

- Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.

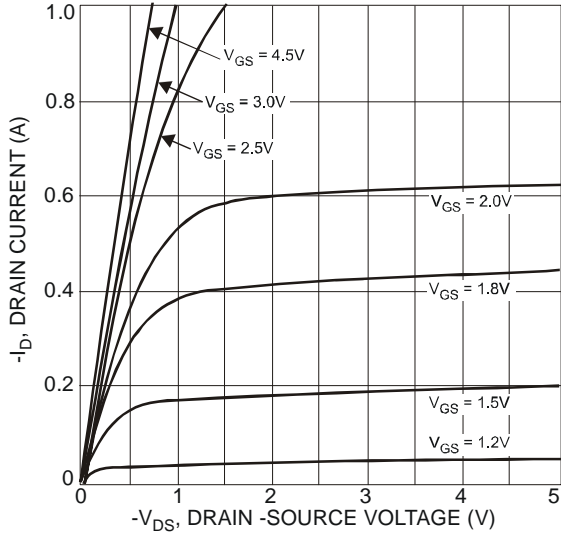


Fig. 1 Typical Output Characteristics

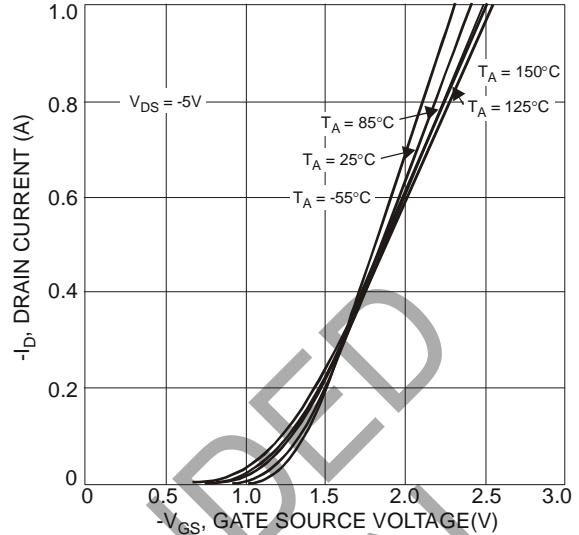


Fig. 2 Typical Transfer Characteristics

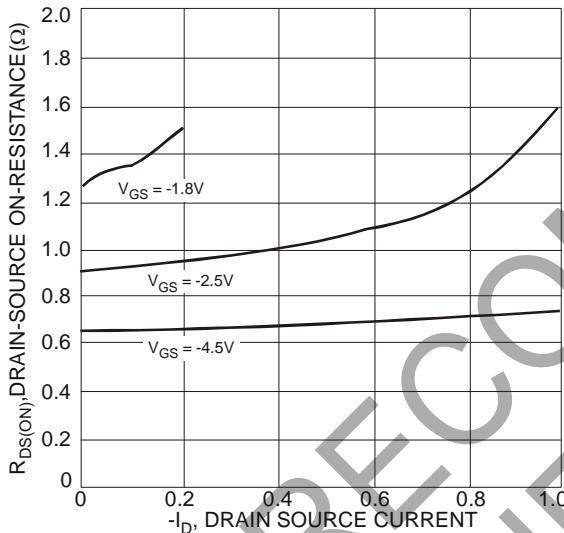


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

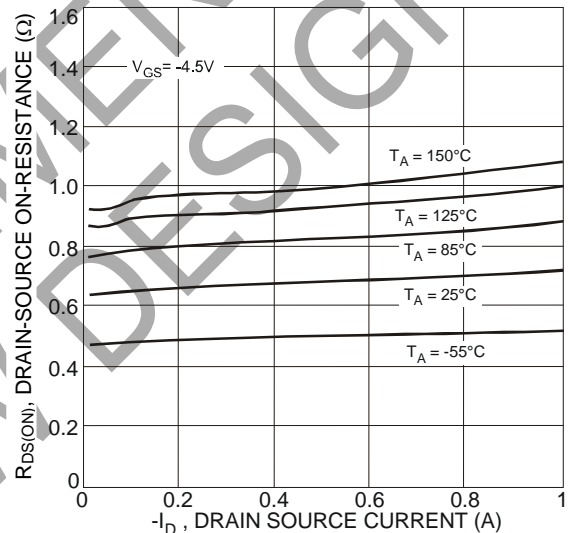


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

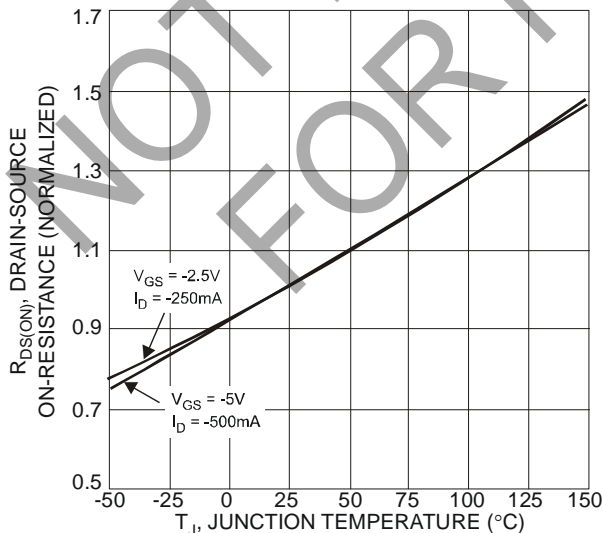


Fig. 5 On-Resistance Variation with Temperature

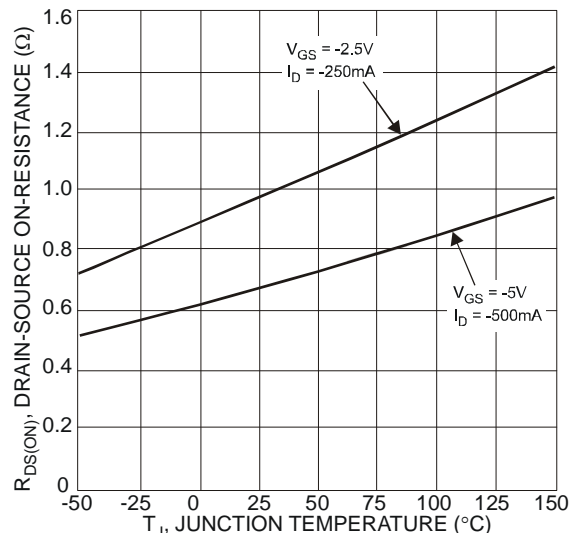


Fig. 6 On-Resistance vs. Temperature

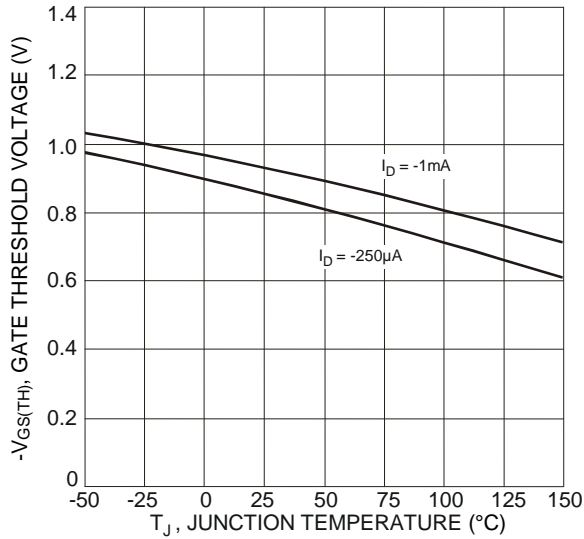


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

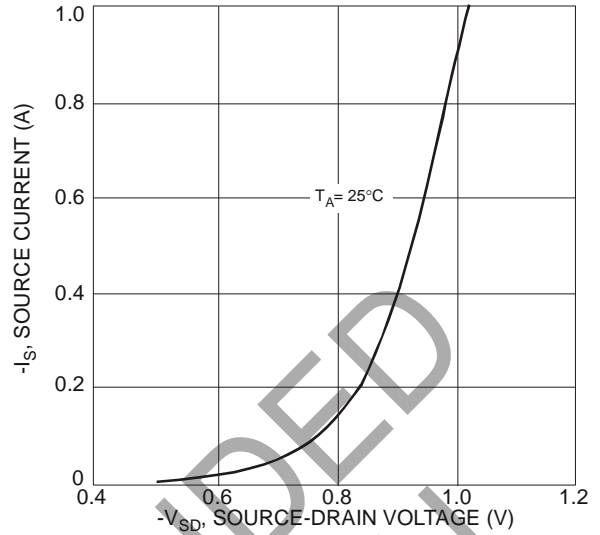


Fig. 8 Diode Forward Voltage vs. Current

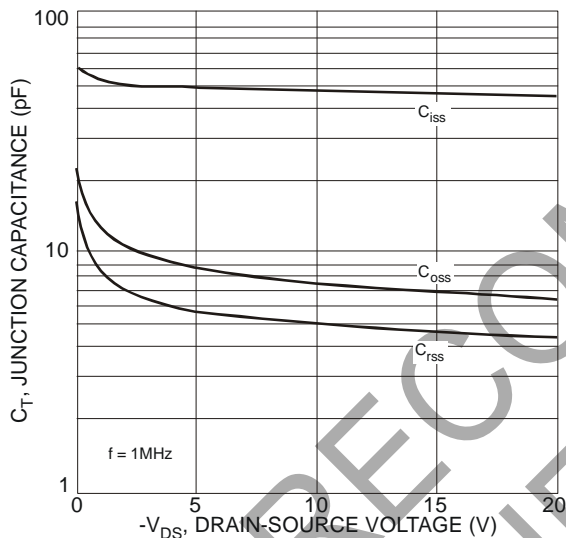


Fig. 9 Typical Junction Capacitance

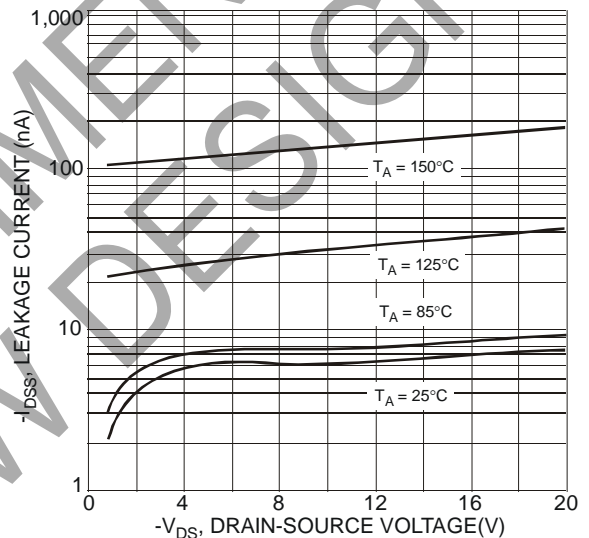


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage

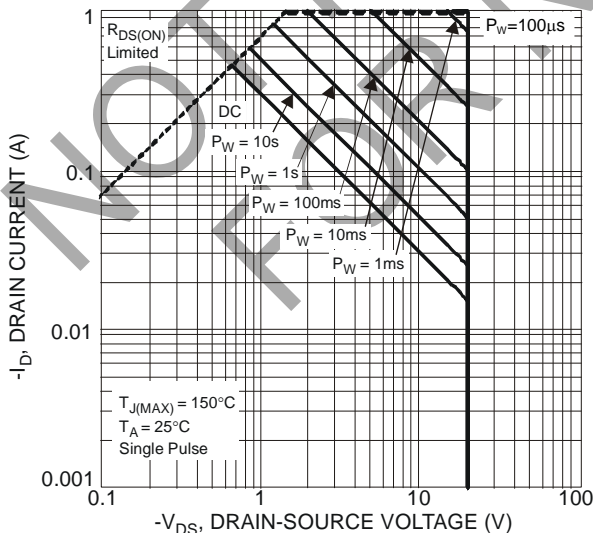


Fig. 11 SOA, Safe Operation Area

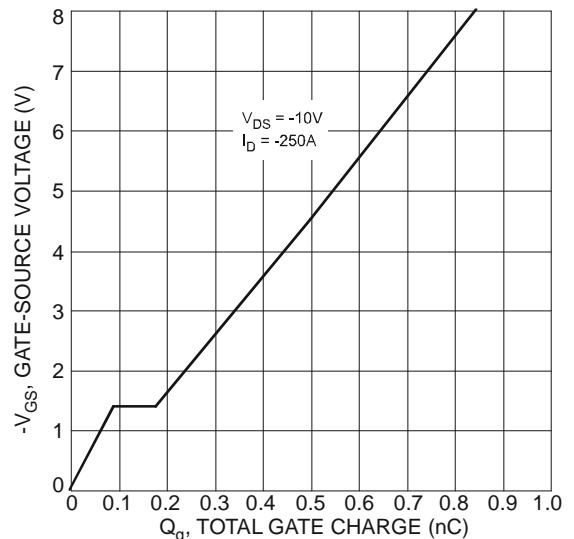
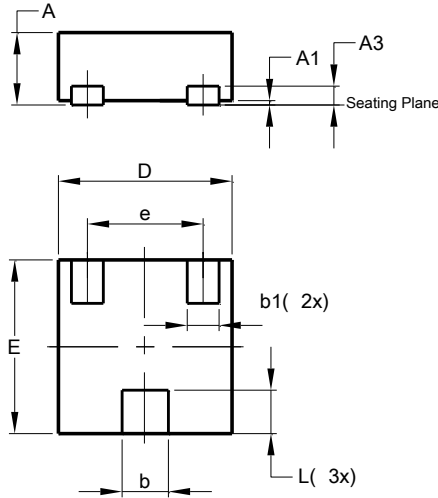


Fig. 12 Gate-Charge Characteristics

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X1-DFN1212-3

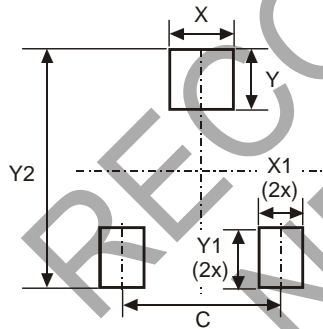


X1-DFN1212-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.02
A3	-	-	0.13
b	0.27	0.37	0.32
b1	0.17	0.27	0.22
D	1.15	1.25	1.20
E	1.15	1.25	1.20
e	-	-	0.80
L	0.25	0.35	0.30
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X1-DFN1212-3



Dimensions	Value (in mm)
C	0.80
X	0.42
X1	0.32
Y	0.50
Y1	0.50
Y2	1.50

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