

Standard Rectifier Module

= 2x 1600 V

59 A

 V_{F} 1.26 V

Phase leg

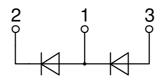
Part number

MDD44-16N1B



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For single and three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: TO-240AA

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- · Reduced weight
- Advanced power cycling

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

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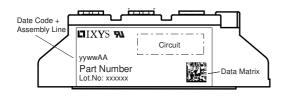




Rectifier			Ratings				
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			1700	V
V_{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1600	V
I _R	reverse current	V _R = 1600 V	$T_{VJ} = 25^{\circ}C$			100	μΑ
		$V_R = 1600 \text{ V}$	$T_{VJ} = 150$ °C			10	mΑ
V _F	forward voltage drop	I _F = 100 A	$T_{VJ} = 25^{\circ}C$			1.30	V
		$I_F = 200 \text{ A}$				1.60	٧
		$I_F = 100 \text{ A}$	T _{VJ} = 125°C			1.26	V
		$I_F = 200 \text{ A}$				1.67	٧
I FAV	average forward current	T _C = 100°C	$T_{VJ} = 150$ °C			59	Α
I _{F(RMS)}	RMS forward current	180° sine				100	Α
V _{F0}	threshold voltage		T _{VJ} = 150°C			0.80	V
r _F	slope resistance \(\) for power	loss calculation only				4.3	mΩ
R _{thJC}	thermal resistance junction to ca	ase				0.59	K/W
R _{thCH}	thermal resistance case to heats	sink			0.20		K/W
P _{tot}	total power dissipation		$T_{C} = 25^{\circ}C$			212	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			1.15	kA
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.24	kA
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			980	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.06	kA
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			6.62	kA2s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			6.40	kA2s
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			4.80	kA2s
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			4.63	kA2s
CJ	junction capacitance	$V_{R} = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		27		pF



Package TO-240AA			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
RMS	RMS current	per terminal					200	Α
T _{VJ}	virtual junction temperature				-40		150	°C
Top	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						76		g
M _D	mounting torque				2.5		4	Nm
$\mathbf{M}_{_{\mathrm{T}}}$	terminal torque				2.5		4	Nm
d _{Spp/App}	oroonogo diatanoo on curfoos	e striking distance through air	terminal to terminal	13.0	9.7			mm
d _{Spb/Apb}	creepage distance on surface	e Striking distance through an	terminal to backside 16.		16.0			mm
V _{ISOL}	isolation voltage	t = 1 second	50/60 Hz, RMS; lisoL ≤ 1 mA		3600			٧
.002		t = 1 minute			3000			٧



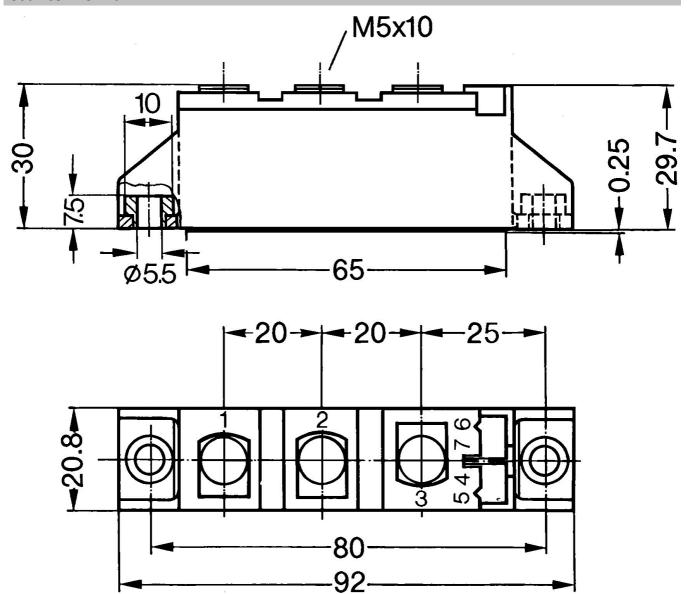
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MDD44-16N1B	MDD44-16N1B	Box	36	458058

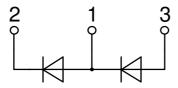
Similar Part	Package	Voltage class
MDD44-08N1B	TO-240AA	800
MDD44-12N1B	TO-240AA	1200
MDD44-14N1B	TO-240AA	1400
MDD44-18N1B	TO-240AA	1800

Equiva	alent Circuits for	Simulation	* on die level	T _{vJ} = 150 °C
$I \rightarrow V_0$	R_0	Rectifier		
V _{0 max}	threshold voltage	8.0		V
$R_{0 max}$	slope resistance *	3.1		mΩ



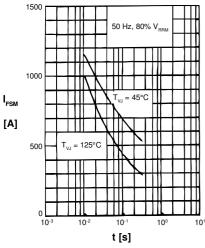
Outlines TO-240AA

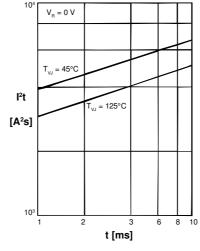






Rectifier





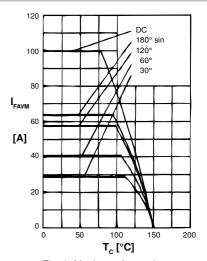


Fig. 1 Surge overload current I_{TSM} , I_{FSM} : Crest value, t: duration

Fig. 2 I2t versus time (1-10 ms)

Fig. 3 Maximum forward current at case temperature

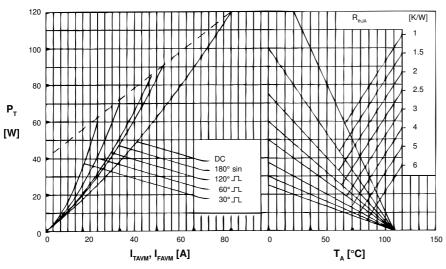


Fig. 4 Power dissipation vs. onstate current and ambient temperature (per diode)

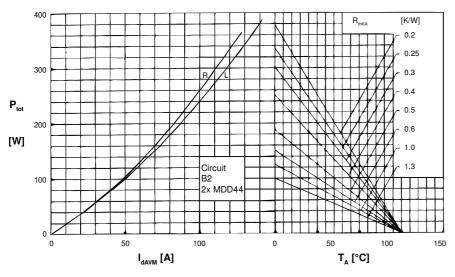


Fig. 6 Single phase rectifier bridge: Power dissipation versus direct output current and ambient temperature; R = resistive load,L = inductive load



Rectifier

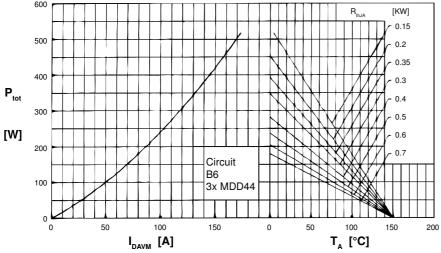


Fig. 6 Three phase rectifier bridge: Power dissipation versus direct output current and ambient temperature

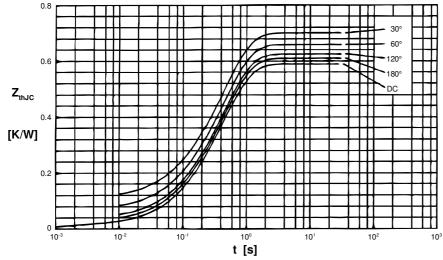


Fig. 7 Transient thermal impedance junction to case (per diode)

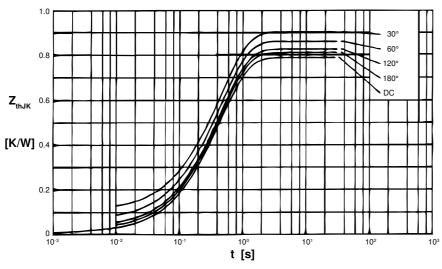


Fig. 8 Transient thermal impedance junction to heatsink (per thyristor)

 $\boldsymbol{R}_{\text{thJC}}$ for various conduction angles d:

100	
d	R _{thJC} [K/W
DC	0.59
180°	0.61
120°	0.63
60°	0.66
30°	0.70

Constants for $\boldsymbol{Z}_{\text{thJC}}$ calculation:

i I	R _{thi} [K/W]	t, [s]
1	0.012	0.0012
2	0.045	0.0950
3	0.533	0.4550

 $R_{th,IK}$ for various conduction angles d:

าปห	-	
	d	R _{thJK} [K/W
	DC	0.79
•	180°	0.81
•	120°	0.83
	60°	0.86
	30°	0.90

Constants for $\mathbf{Z}_{\text{\tiny thJK}}$ calculation:

i	R_{thi} [K/W]	t, [s]
1	0.012	0.0012
2	0.045	0.0950
3	0.533	0.4550
4	0.200	0.4950

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