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FGL35N120FTD 1200 V, 35 A Field Stop Trench IGBT

Features

- Field Stop Trench Technology
- High Speed Switching
- Low Saturation Voltage: V_{CE(sat)} = 1.68 V @ I_C = 35 A
- High Input Impedance

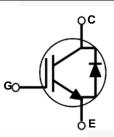
Applications

• Solar Inverter, UPS, Welder, PFC

General Description

Using advanced field stop trench IGBT technology, Fairchild's 1200V trench IGBTs offer the optimum performance for hard switching application such as solar inverter, UPS, welder applications.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit	
V _{CES}	Collector to Emitter Voltage		1200	V	
V _{GES}	Gate to Emitter Voltage		± 25	V	
	Collector Current	@ T _C = 25°C	70	A	
I _C	Collector Current	@ T _C = 100 ^o C	35	A	
I _{CM (1)}	Pulsed Collector Current@ $T_C = 25^{\circ}C$		105	A	
	Diode Continuous Forward Current	@ T _C = 25°C	80	A	
IF	Diode Continuous Forward Current	@ T _C = 100 ^o C	40	A	
P _D	Maximum Power Dissipation	@ T _C = 25 ^o C	368	W	
D	Maximum Power Dissipation	@ T _C = 100°C	147	W	
TJ	Operating Junction Temperature		-55 to +150	°C	
T _{stg}	Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 second	s	300	°C	

Notes: 1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Max.	Unit	
$R_{\theta JC}$ (IGBT)	Thermal Resistance, Junction to Case	0.34	°C/W	
R _{0JC} (Diode) Thermal Resistance, Junction to Case		0.9	°C/W	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient	25	°C/W	

Part NumberTop MarkPackageFGL35N120FTDTUFGL35N120FTDTO-264		Packag	ge Packing Method R		Reel	Size	Tape Width		Quantity	
		Tube		N/A		N/A		30		
Electric	al Cha	aracteristics o	of the IC	GB.	T _C = 25°C unless otherwise	e noted				
Symbol		Parameter			Test Conditions		Min.	Тур.	Мах	. Unit
Off Charac	teristics									·
BV _{CES}	Collecto	r to Emitter Breakdow	n Voltage	VGF	= 0 V, I _C = 250 μA		1200	-	-	V
I _{CES}		r Cut-Off Current		$V_{CE} = V_{CES}, V_{GE} = 0 V$			-	-	1	mA
I _{GES}	G-E Lea	kage Current		$V_{GE} = V_{GES}, V_{CE} = 0 V$			-	-	±250	nA
On Charac									1	
V _{GE(th)}	G-E Threshold Voltage		$I_C = 35 \text{ mA}, V_{CE} = V_{GE}$			3.5	6.2	7.5	V	
V	Collecto	to Emitter Saturation Voltage		$I_{C} = 35 \text{ A}, V_{GE} = 15 \text{ V}$		-	1.68	2.2	V	
V _{CE(sat)}	(sat) Collector to Emitter Saturation Voltage		$I_{C} = 35 \text{ A}, V_{GE} = 15 \text{ V},$ $T_{C} = 125^{\circ}\text{C}$			-	2.0	-	V	
Dynamic C	haracter	istics						1		
C _{ies}		apacitance	_				-	5090	-	pF
C _{oes}	Output 0	Capacitance		$V_{CE} = 30 V, V_{GE} = 0 V,$			-	180	-	pF
C _{res}	Reverse	se Transfer Capacitance		f = 1 MHz			-	95	-	pF
Switching	Characte	ristics		I		k				
t _{d(on)}		Delay Time	_				-	34	-	ns
t _r	Rise Tin		_				-	63	-	ns
t _{d(off)}		Delay Time	_	$V_{\rm CC} = 600 \text{ V}, \text{ I}_{\rm C} = 35 \text{ A},$		_	-	172	-	ns
-u(011) t _f	Fall Tim				$R_{G} = 10 \Omega, V_{GE} = 15 V,$		-	107	-	ns
E _{on}	Turn-On	Switching Loss		Indu	uctive Load, $T_C = 25^{\circ}C$		-	2.5	-	mJ
E _{off}		Switching Loss					-	1.7	-	mJ
E _{ts}		vitching Loss				-	-	4.2	-	mJ
t _{d(on)}	Turn-On	Delay Time						33	-	ns
t _r	Rise Tin					F		66	-	ns
t _{d(off)}		Delay Time		Var	₂ = 600 V, I _C = 35 A,		<u> </u>	180	-	ns
t _f	Fall Tim	e		R_{G}	$R_{G} = 10 \Omega$, $V_{GE} = 15 V$,		-	146	-	ns
E _{on}	Turn-On	Switching Loss		Inductive Load, T _C = 125 ^o C	-	-	3.1	-	mJ	
E _{off}		f Switching Loss			-	-	2.1	-	mJ	
E _{ts}	Total Sw	vitching Loss					-	5.2	-	mJ
Qg	Total Ga	ite Charge					-	210	-	nC
Q _{ge}		Emitter Charge			$= 600 \text{ V}, \text{ I}_{\text{C}} = 35 \text{ A},$		-	42	-	nC
Q _{gc}		Collector Charge		VGE	= 15 V	-	-	101	-	nC

FGL35N120FTD
— 1200 V,
V, 35 A Field
A Field Stop Tre
rench IGBT

Symbol	Parameter	Test Condition	Min.	Тур.	Max	Unit	
V _{FM}	Diode Forward Voltage	I _F = 35 A	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	2.7	3.4	V
		·F 007.	T _C = 125°C	-	2.5	-]
t _{rr}	Diode Reverse Recovery Time		$T_{\rm C} = 25^{\rm o}{\rm C}$	-	337	-	ns
			$T_{C} = 125^{\circ}C$	-	520	-	
I _{rr}	Diode Peak Reverse Recovery Current	I _F = 35 A, di _F /dt = 200 A/μs	$T_{C} = 25^{\circ}C$	-	7.6	-	А
			$T_{C} = 125^{\circ}C$	-	12.9	-	
Q _{rr}	Diode Reverse Recovery Charge		$T_C = 25^{\circ}C$	-	1292	-	nC
∽rr	Diede Hereice Heedvery enarge		$T_{\rm C} = 125^{\rm o}{\rm C}$	-	3377	-	

Typical Performance Characteristics Figure 1. Typical Output Characteristics Figure 2. Typical Output Characteristics 180 180 $T_C = 25^{\circ}C$ T_C = 125^oC 17V 17V 20V 20\ 15V 15V 150 150 12V 12V Collector Current, I_c [A] Collector Current, I_c [A] 120 120 90 90 10V 10V 60 60 9V 9V 30 30 V_{GE} = 8V V_{GE} = 8V 0 0 2 4 6 Collector-Emitter Voltage, V_{CE} [V] 2 4 6 Collector-Emitter Voltage, V_{CE} [V] 0 8 8 0 Figure 3. Typical Saturation Voltage **Figure 4. Transfer Characteristics Characteristics** 120 120 Common Emitter Common Emitter $V_{CE} = 20V$ $V_{GE} = 15V$ 100 100 $T_C = 25^{\circ}C$ $T_{C} = 25^{\circ}C$ — Collector Current, I_c [A] Collector Current, I_c [A] T_C = 125^oC T_C = 125^oC ... 80 80 60 60 40 40 20 20 0 0 0 1 2 3 8 10 12 4 4 6 Collector-Emitter Voltage, V_{CE} [V] Gate-Emitter Voltage, VGE [V] Figure 6. Saturation Voltage vs. V_{GE} Figure 5. Saturation Voltage vs. Case **Temperature at Variant Current Level** 2.8 20 Common Emitter Common Emitter 70A V_{GE} = 15V $T_{C} = 25^{\circ}C$ Collector-Emitter Voltage, V_{CE} [V] 2.6 Collector-Emitter Voltage, V cE [V] 16 2.4 2.2 12 2.0 35A 8 1.8 70Å 35A 1.6 I_C = 18A 4 1.4 I_C = 18A 0 ∟ 4 1.2 50 75 100 125 20 25 8 12 16 Case Temperature, T_C [°C] Gate-Emitter Voltage, VGE [V]

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Figure 7. Saturation Voltage vs. V_{GE} 20 Common Emitter $T_C = 125^{\circ}C$ \geq 16 Collector-Emitter Voltage, V_{CE} 12 8 70A 35A 4 $I_{\rm C} = 18A$ 0 8 20 4 12 16 Gate-Emitter Voltage, V_{GE} [V] **Figure 9. Capacitance Characteristics** 8000 Common Emitter V_{GE} = 0V, f = 1MHz Cies T_C = 25°C 6000

Typical Performance Characteristics

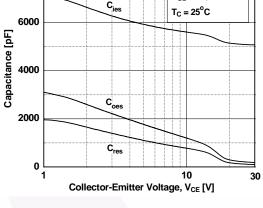


Figure 11. SOA Characteristics

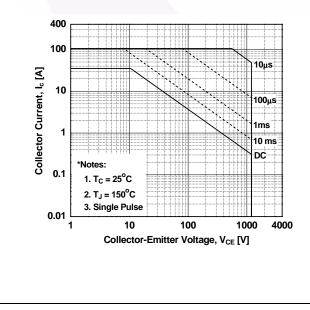


Figure 8. Load Current vs. Frequency

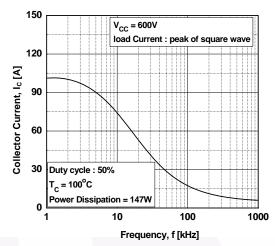


Figure 10. Gate Charge Characteristics

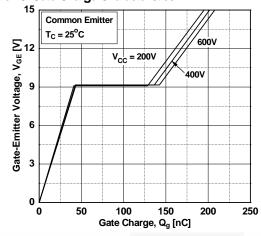
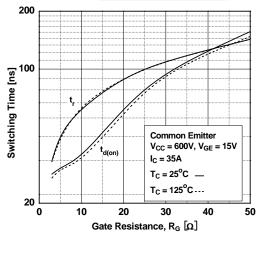
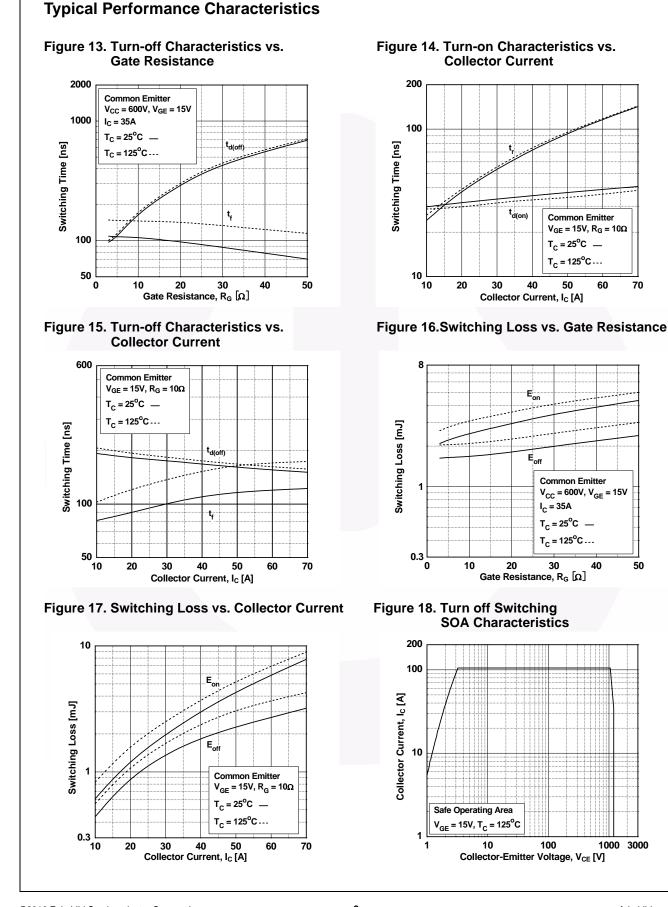


Figure 12. Turn-on Characteristics vs. Gate Resistance

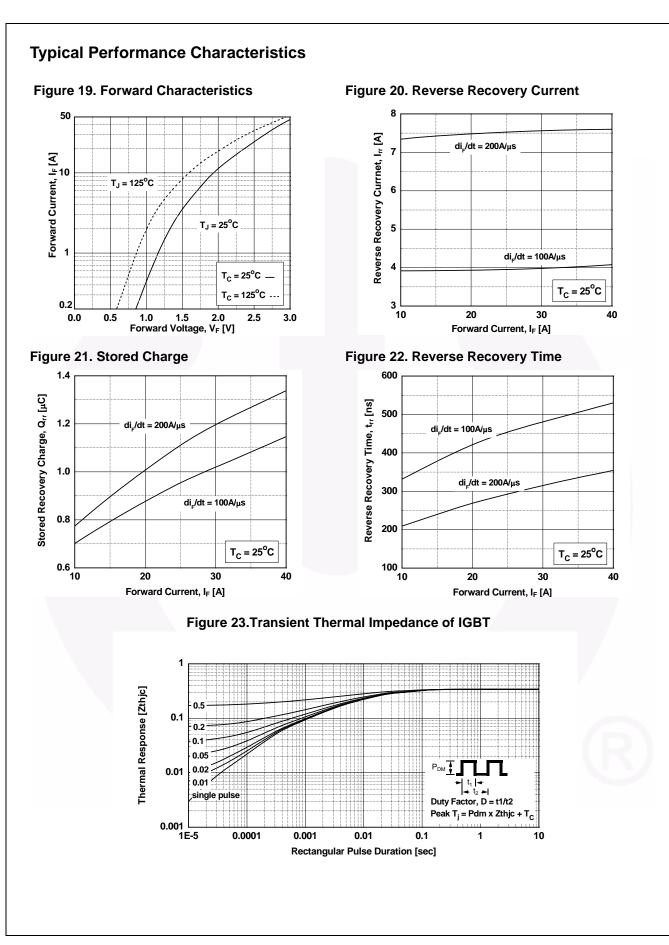


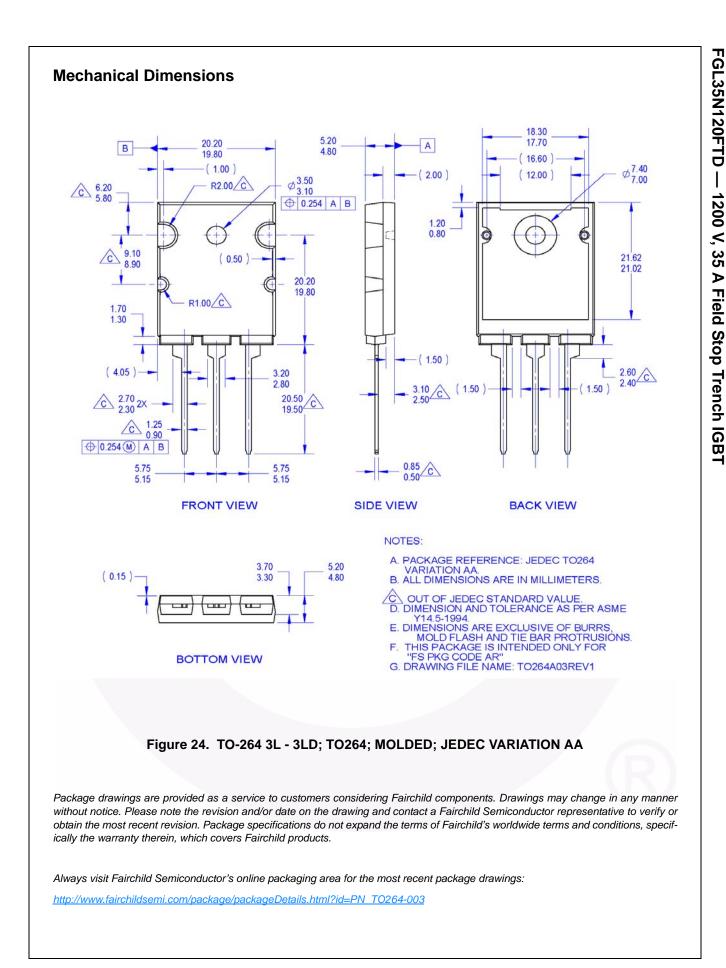
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