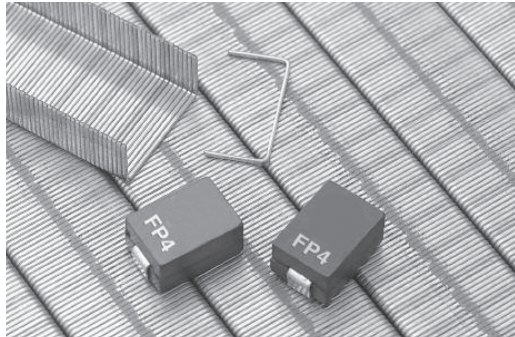


FP4

High current power inductors



Product description

- High current carrying capacity
- Inductance range from 0.090uH to 0.200uH
- Current range 30 to 72 Amps
- 10.2 x 6.8mm footprint surface mount package in a 5.0mm height
- Ferrite core material
- Halogen free, lead free, RoHS compliant

Applications

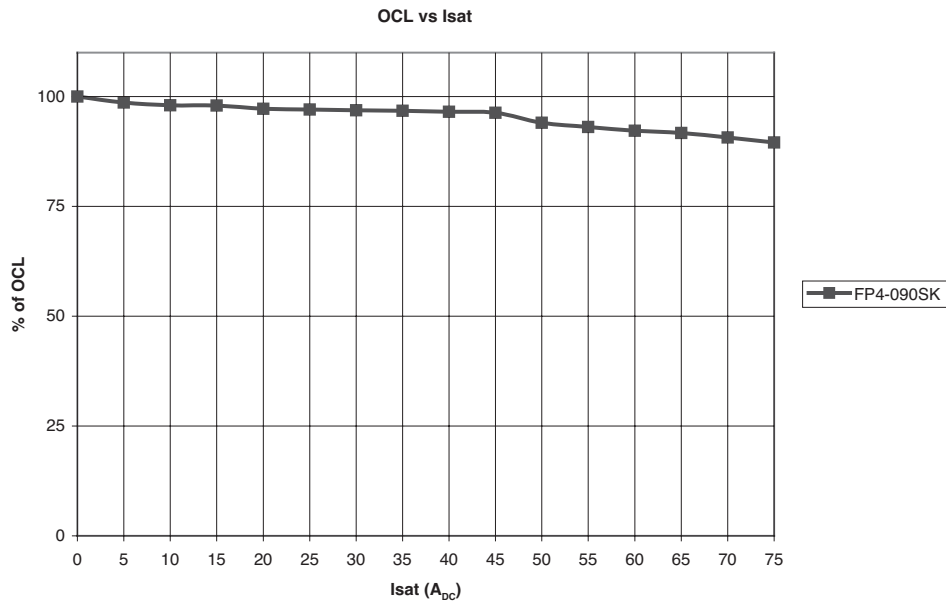
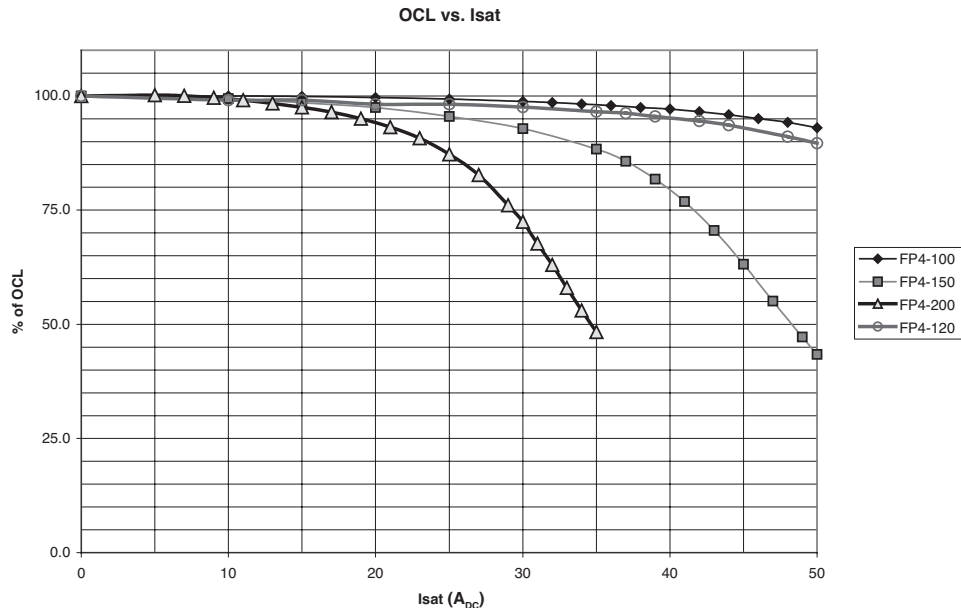
- Servers
- Multi-phase and Vcore regulators
- Voltage Regulator Modules (VRMs)
 - Server and desktop
 - Central processing unit (CPU)
 - Graphics processing unit (GPU)
 - Application specific integrated circuit (ASIC)
 - High power density
- Battery power systems
- Graphics cards

Environmental data

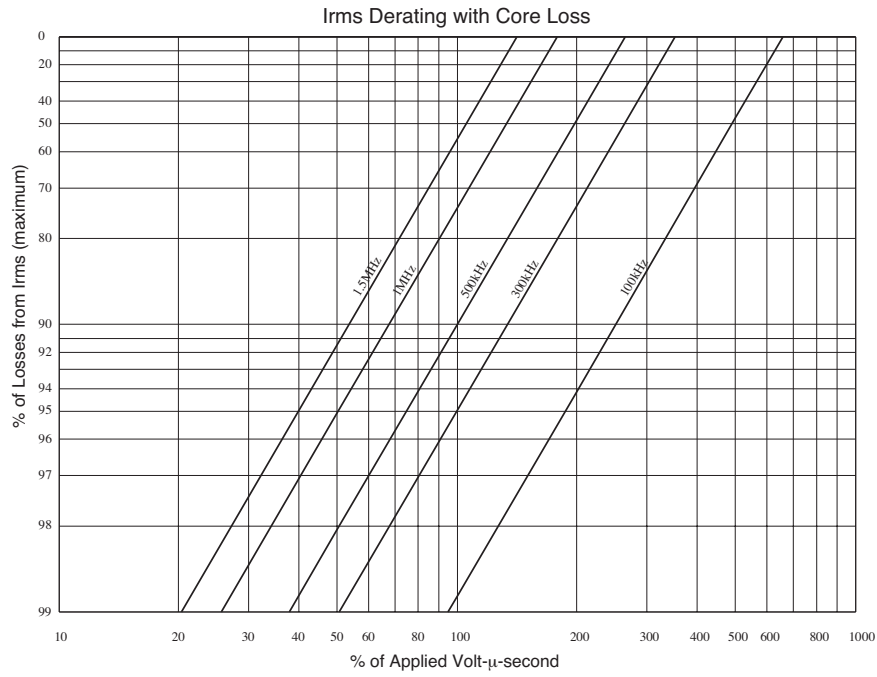
- Storage temperature range (component): -40°C to +155°C
- Operating temperature range: -45°C to +155°C (Ambient plus self temperature rise)
- Solder reflow temperature: J-STD-020D compliant



Inductance characteristics



Core loss



Solder reflow profile

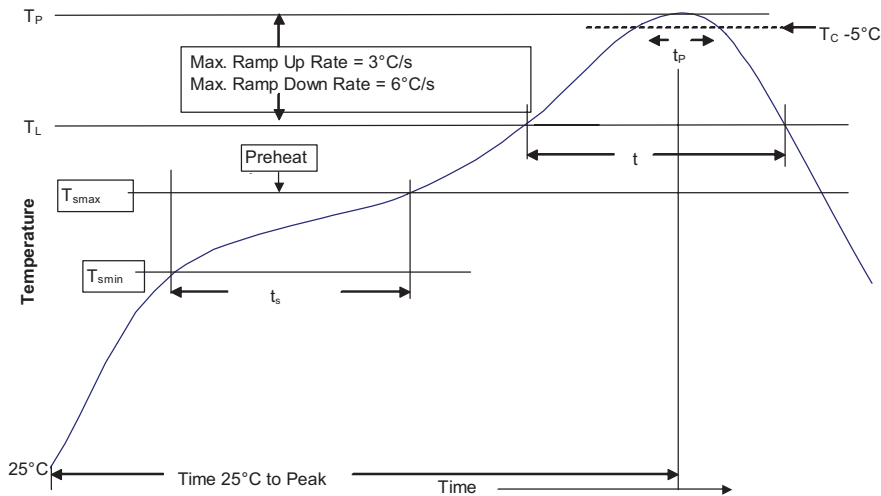


Table 1 - Standard SnPb Solder (T_C)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5mm)	235°C	220°C
≥2.5mm	220°C	220°C

Table 2 - Lead (Pb) Free Solder (T_C)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350 - 2000	Volume mm ³ >2000
<1.6mm	260°C	260°C	260°C
1.6 – 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

Reference JEDEC J-STD-020D

Profile Feature	Standard SnPb Solder	Lead (Pb) Free Solder
Preheat and Soak		
• Temperature min. (T _{smin})	100°C	150°C
• Temperature max. (T _{smax})	150°C	200°C
• Time (T _{smin} to T _{smax}) (t _s)	60-120 Seconds	60-120 Seconds
Average ramp up rate T _{smax} to T _p	3°C/ Second Max.	3°C/ Second Max.
Liquidous temperature (T _L)	183°C	217°C
Time at liquidous (t _L)	60-150 Seconds	60-150 Seconds
Peak package body temperature (T _p)*	Table 1	Table 2
Time (t _p)** within 5 °C of the specified classification temperature (T _C)	20 Seconds**	30 Seconds**
Average ramp-down rate (T _p to T _{smax})	6°C/ Second Max.	6°C/ Second Max.
Time 25°C to Peak Temperature	6 Minutes Max.	8 Minutes Max.

* Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.
 ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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