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May 2014



FQA32N20C

N-Channel QFET® MOSFET

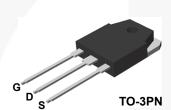
200 V, 32 A, 82 mΩ

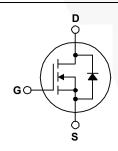
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- * 32 A, 200 V, $R_{DS(on)}$ = 82 m Ω (Max.) @ V_{GS} = 10 V, I_{D} = 16 A
- Low Gate Charge (Typ. 82.5 nC)
- Low Crss (Typ. 185 pF)
- · 100% Avalanche Tested





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQA32N20C	Unit
V_{DSS}	Drain-Source Voltage		200	V
I _D	Drain Current - Continuous (T _C = 25°C	C)	32	А
	- Continuous (T _C = 100°	°C)	20.4	А
I _{DM}	Drain Current - Pulsed	(Note 1)	128	А
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	955	mJ
I _{AR}	Avalanche Current	(Note 1)	32	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	20.4	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		5.5	V/ns
P _D Power Dissipation (T _C = 25°C)			204	W
- Derate above 25°C			1.63	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	ge	-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FQA32N20C	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.61	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.24	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQA32N20C	FQA32N20C	TO-3PN	Tube	N/A	N/A	30 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Cha	racteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	200			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.24		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 200 V, V _{GS} = 0 V			10	μА
	Zero Gate Voltage Drain Current	V _{DS} = 160 V, T _C = 125°C			100	μА
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
R _{DS(on)} Static Drain-Source On-Resistance		V _{GS} = 10 V, I _D = 16 A		0.068	0.082	Ω
9 _{FS}	Forward Transconductance	$V_{DS} = 40 \text{ V}, I_{D} = 16 \text{ A}$		20	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		1700	2220	pF
Coss	Output Capacitance	f = 1.0 MHz		400	520	pF
C _{rss}	Reverse Transfer Capacitance			185	245	pF

Switching Characteristics

t _{d(on)}	Turn-On Delay Time	V _{DD} = 100 V, I _D = 32 A,	 25	60	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$	 270	550	ns
t _{d(off)}	Turn-Off Delay Time		 245	500	ns
t _f	Turn-Off Fall Time	(Note 4)	 210	430	ns
Q_g	Total Gate Charge	V _{DS} = 160 V, I _D = 32 A,	 82.5	110	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V	 10.5		nC
Q_{gd}	Gate-Drain Charge	(Note 4)	 44.5		nC

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current				32	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				128	Α
V _{SD}	Drain-Source Diode Forward Voltage V _{GS} = 0 V, I _S = 32 A				1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 32 A,		265		ns
Q _{rr}	Reverse Recovery Charge	dI _F / dt = 100 A/μs		2.73	/	μС

- Notes: Notes: 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2. L = 1.4 mH, I_{AS} = 32 A, V_{DD} = 50 V, R_{G} = 25 Ω , starting T_{J} = 25°C. 3. $I_{SD} \le$ 32 A, di/dt \le 300 A/ μ s, $V_{DD} \le$ BV $_{DSS}$, starting T_{J} = 25°C. 4. Essentially independent of operating temperature.

Typical Performance Characteristics

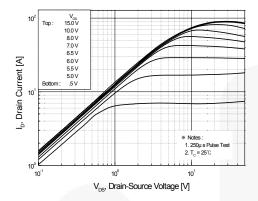


Figure 1. On-Region Characteristics

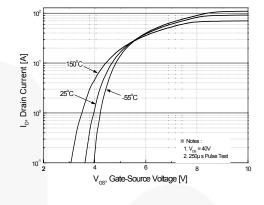


Figure 2. Transfer Characteristics

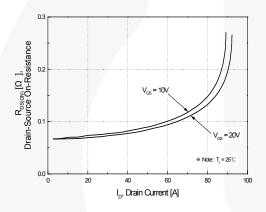


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

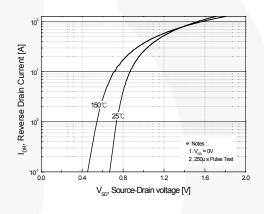


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

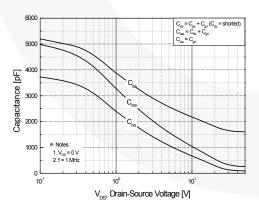


Figure 5. Capacitance Characteristics

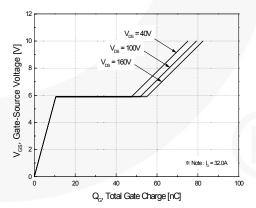


Figure 6. Gate Charge Characteristics

Typical Performance Characteristics (Continued)

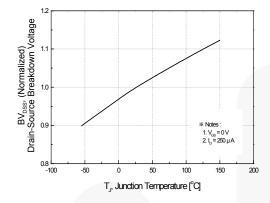


Figure 7. Breakdown Voltage Variation vs Temperature

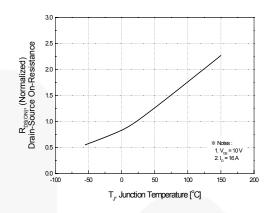


Figure 8. On-Resistance Variation vs Temperature

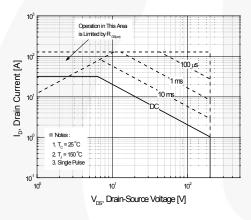


Figure 9. Maximum Safe Operating Area

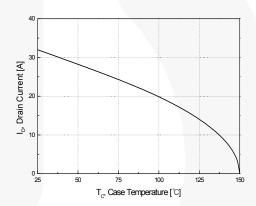


Figure 10. Maximum Drain Current vs Case Temperature

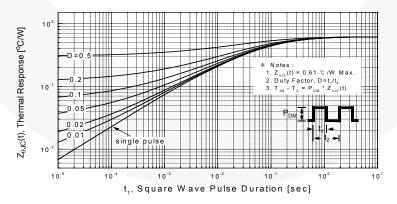


Figure 11. Transient Thermal Response Curve

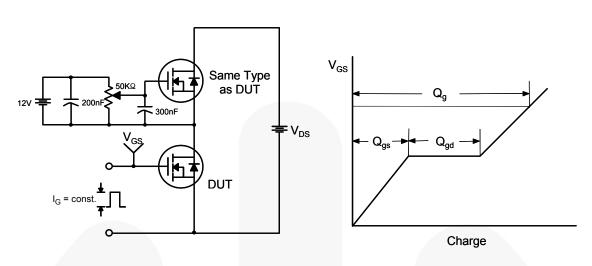


Figure 12. Gate Charge Test Circuit & Waveform

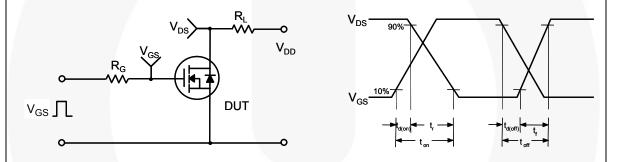


Figure 13. Resistive Switching Test Circuit & Waveforms

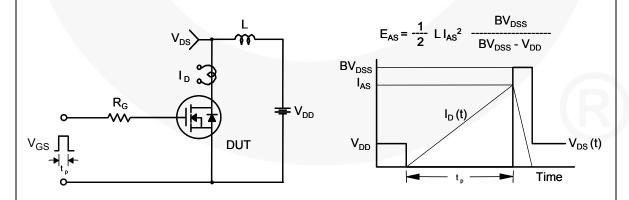
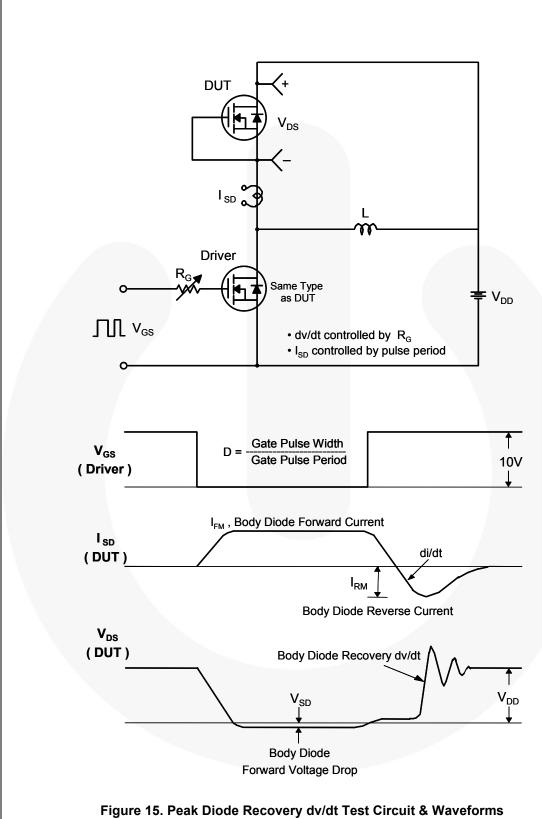
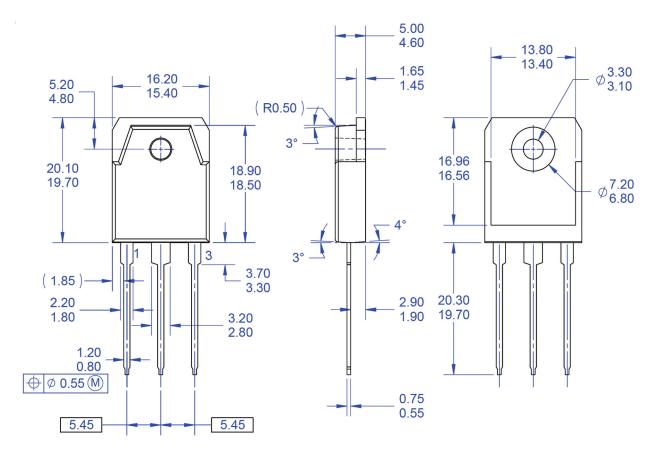
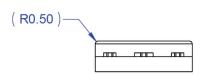


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions





NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE CONFORMS TO EIAJ SC-65 PACKAGING STANDARD.
- ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSION AND TOLERANCING PER ASME14.5-2009.
- D) DIMENSIONS ARE EXCLUSSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSSIONS.
 DRAWING FILE NAME: TO 3PN03AREV1.
- FAIRCHILD SEMICONDUCTOR.

Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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